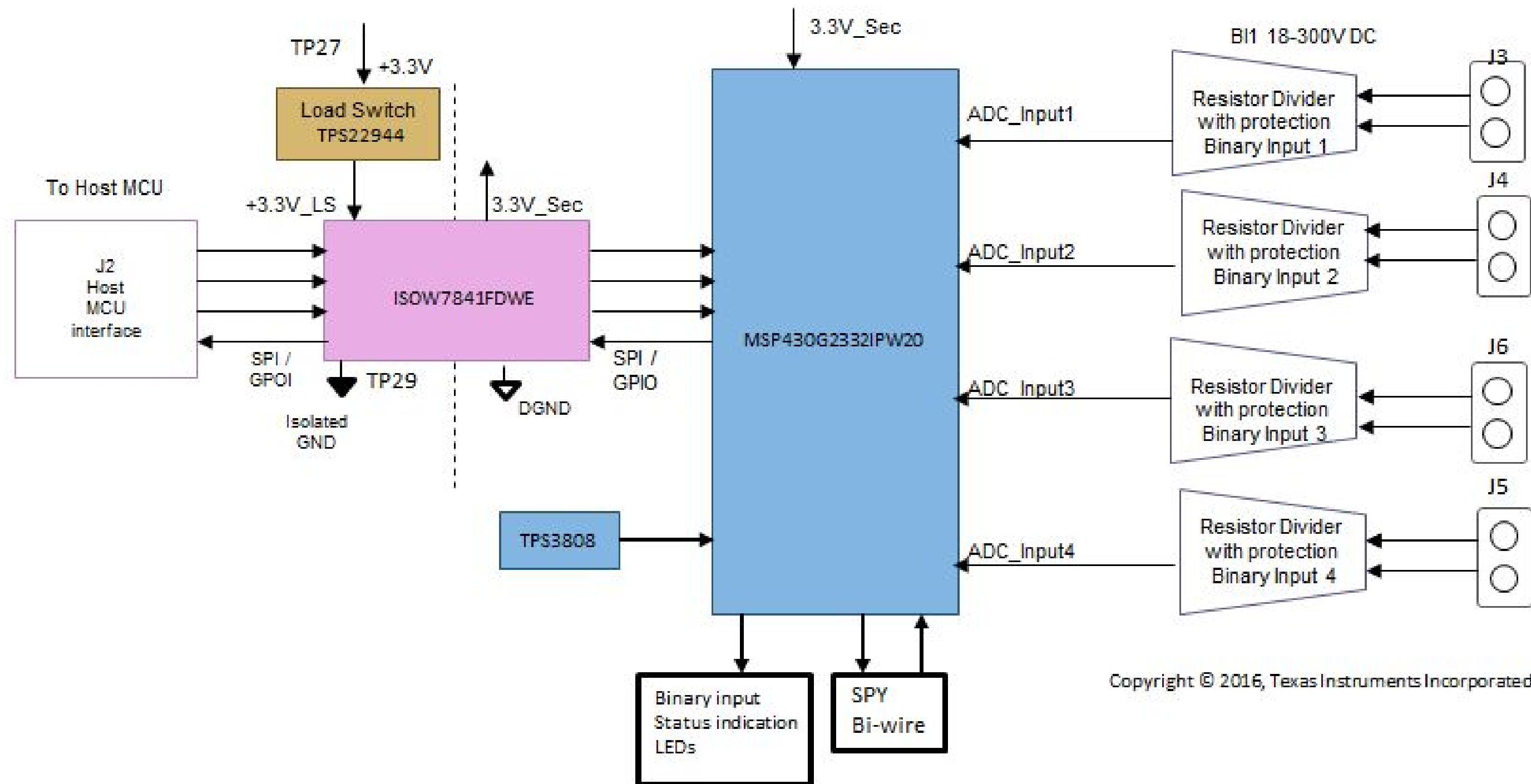
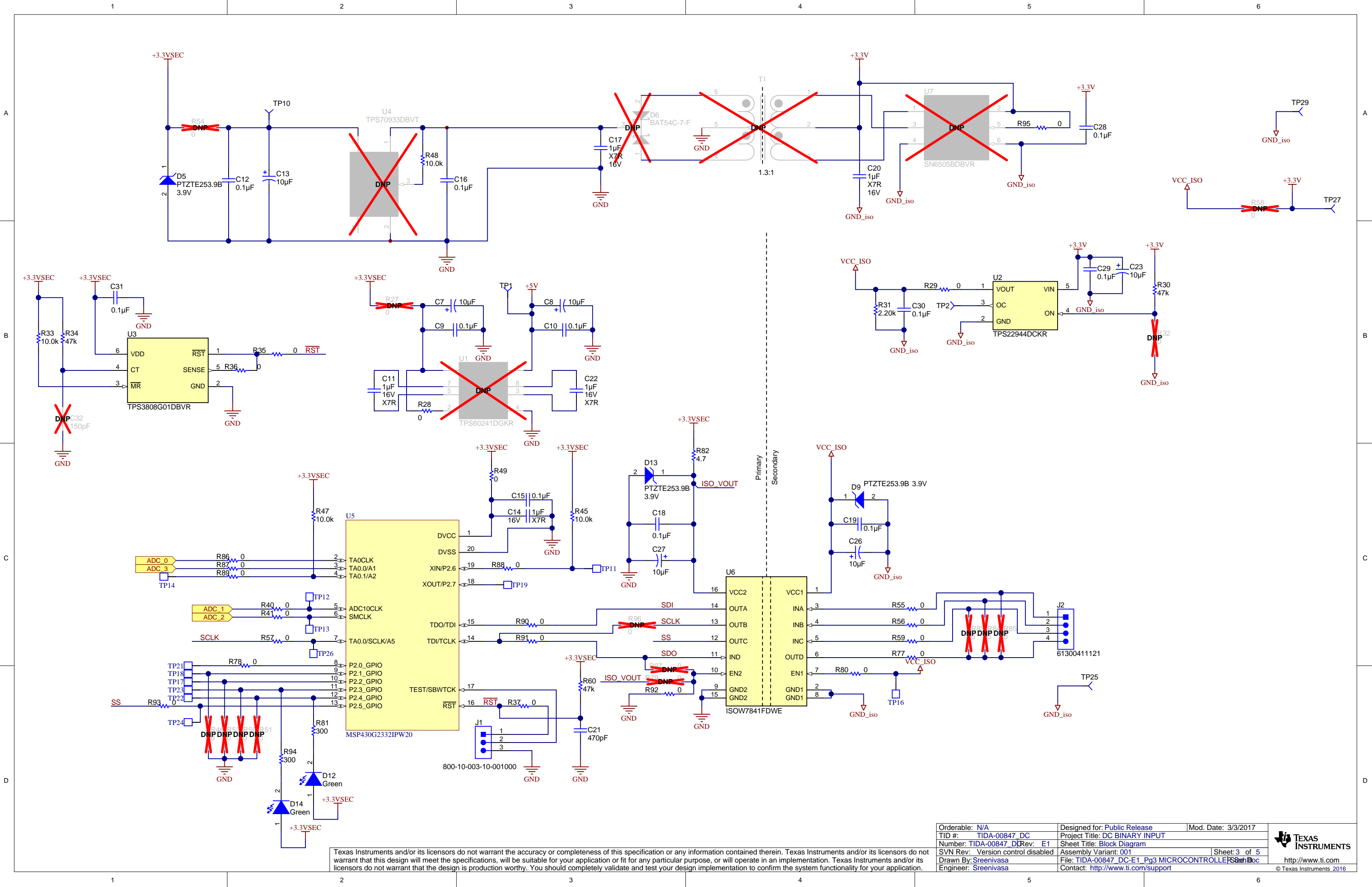


1		2		3		4		5		6				
										Revision History				
										Rev	ECN #	Approved Date	Approved by	Notes
										N/A	N/A	N/A	N/A	N/A



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Orderable: N/A	Designed for: Public Release	Mod. Date: 3/3/2017
TID #: TIDA-00847_DC	Project Title: DC BINARY INPUT	
Number: TIDA-00847_DC Rev: E1	Sheet Title: Block Diagram	
SVN Rev: Version control disabled	Assembly Variant: 001	Sheet: 3 of 5
Drawn By: Sreenivasa	File: TIDA-00847_DC-E1_Pg3 MICROCONTROLLER Schematic	
Engineer: Sreenivasa	Contact: http://www.ti.com/support	

Label Table	
Variant	Label Text
001	ChangeMe!
002	ChangeMe!

PCB Number: TIDA-00847_DC
PCB Rev: E1

PCB
LOGO
Pb_Free

PCB
LOGO
Pb-Free Symbol

PCB
LOGO
Texas Instruments

LBL1

PCB Label

Size: 0.65" x 0.20 "

ZZ1

Label Assembly Note

This Assembly Note is for PCB labels only

Z72

Assembly Note

These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3


Assembly Note

These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4

Assembly Note

These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

Orderable: N/A	Designed for: Public Release	Mod. Date: 12/15/2016	 TEXAS INSTRUMENTS http://www.ti.com © Texas Instruments 2016
TID #: TIDA-00847_DC	Project Title: DC BINARY INPUT		
Number: TIDA-00847_DC Rev: E1	Sheet Title: Block Diagram		
SVN Rev: Version control disabled	Assembly Variant: 001	Sheet: 5 of 5	
Drawn By: Sreenivasa	File: TIDA-00847_DC-E1_Pg 5 Hardware.SchDoc	Size: B	
Engineer: Sreenivasa	Contact: http://www.ti.com/support		

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