Reliability Report

ADC3664-SEP Production Flow and Reliability Report

ABSTRACT

This report presents the reliability and qualification results for the ADC3664-SEP, a 14-bit 125-MSPS, low noise, ultra-low power, dual channel ADC. The ADC3664-SEP is a Space Enhanced Product and is manufactured with a controlled baseline and has the following advantages compared to commercial devices:

- Single baseline: One fab, assembly and test site
- Product traceability
- An extended product life cycle
- Lot acceptance testing

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Trademarks

1 Texas Instruments Enhanced Product Qualification and Reliability Report

TI qualification testing is a risk mitigation process that is engineered to assure device longevity in customer applications. Wafer fabrication process and package level reliability are evaluated in a variety of ways that may include accelerated environmental test conditions with subsequent derating to actual use conditions. Manufacturability of the device is evaluated to verify a robust assembly flow and assure continuity of supply to customers. TI Enhanced Products are qualified with industry standard test methodologies performed to the intent of Joint Electron Devices Engineering Council (JEDEC) standards and procedures. Texas Instruments Enhanced Products meet GEIA-STD-0002-1 Aerospace Qualified Electronic Components.

2 Space Enhanced Plastic Production Flow

2.1 Device Introduction

ADC3664-SEP is a radiation hardened device in a plastic package which allows this device to be used in space applications. The device was verified immune to 43MeV × cm^2 / mg at 125°C for single event latch-up (SEL). Each fabrication lot was tested according to MIL-STD-883 for Radiation Lot Acceptance Tested (RLAT) up to 30krad(Si) and each assembly and test lot follows the process flow shown in Figure 2-1. To verify the quality of ADC3664-SEP, the device is qualified with Space-EP requirements. See Section 3 for further details.

2.2 ADC3664-SEP Space Enhanced Product Production Flow

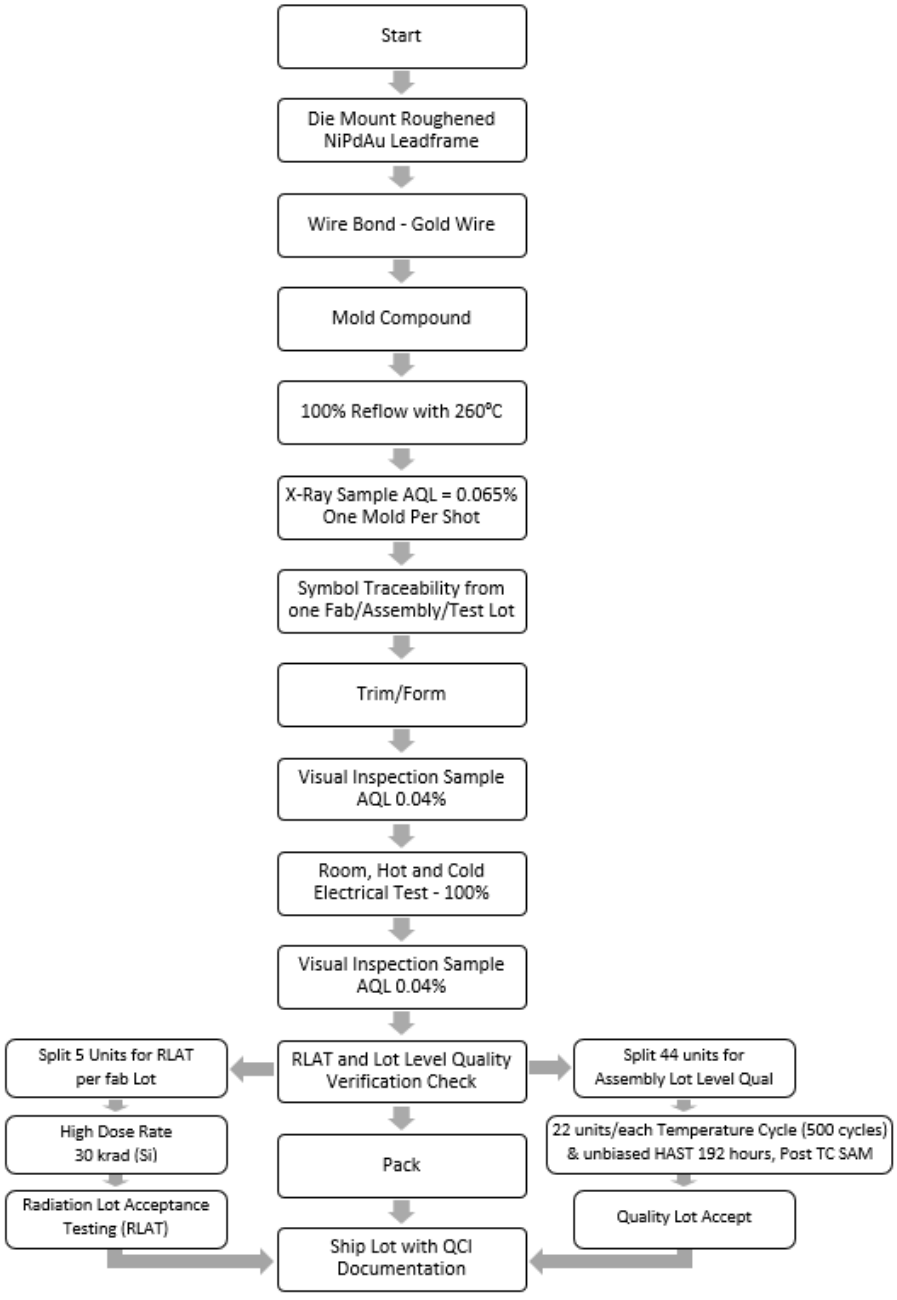

Figure 2-1. ADC3664-SEP Space Enhanced Product Production Flow Chart

3 Device Qualification

The following is the device qualification summary.

Qualification by Similarity (Qualification Family)

A new device can be qualified either by performing full scale quality and reliability tests on the actual device or using previously qualified devices through *Qualification by Similarity* (QBS) rules. By establishing similarity between the new device and those qualified previously, repetitive tests are eliminated, allowing for timely production release. When adopting QBS methodology, the emphasis is on qualifying the differences between a previously qualified product and the new product under consideration.

The QBS rules for a technology, product, test parameters, or package shall define which attributes are required to remain fixed for the QBS rules to apply. The attributes which are expected and allowed to vary is reviewed and a QBS plan is developed, based on the reliability impact assessment above, specifying what subset of the full complement of environmental stresses is required to evaluate the reliability impact of those variations. Each new device is reviewed for conformance to the QBS rule sets applicable to that device. See JEDEC JESD47 for more information.

Table 3-1. Space Enhanced Products New Device Qualification Matrix

Note that qualification by similarity (“qualification family”) per JEDEC JESD47 is allowed.				
Description	**Condition**	**Sample Size Used and Rejects**	**Lots Required**	**Test Method**
Electromigration	Maximum recommended operating conditions	N/A	N/A	Per TI Design rules
Wire bond life	Maximum recommended operating conditions	N/A	N/A	Per TI Design rules
Electrical characterization	TI data sheet	30	1	N/A
Electrostatic discharge sensitivity	HBM	3 units/voltage	1	JEDEC JS-001 or EIA/ JESD22-A114
	CDM			JEDEC JS-002 or EIA/ JESD22-C101
Latch-up	Per technology	3/0	1	EIA/JESD78
Physical dimensions	TI data sheet	5/0	1	EIA/JESD22- B100
Thermal impedance	Theta-JA on board	Per pin-package	N/A	EIA/JESD51
Biased HAST	130°C / 85% / 96 hours	77/0	1	JESD22-A110/A101*
Extended biased HAST	130°C / 85% / 250 hours (for reference)	77/0	1	JESD22-A110/A101*
Unbiased HAST	130°C / 85% / 192 hours	77/0	1	JESD22-A118*
Temperature cycle	-65°C to +150°C non-biased for 500 cycles	77/0	1	JESD22-A104*
Solderability	Bake Preconditioning	22/0	1	ANSI/J-STD-002
Bond pull strength	Per wire size	Two units × 30/0 bonds	1	ASTM F-459
High temperature storage	150°C / 1000 hours	77/0	2	JESD22-A103*
Moisture sensitivity	Surface mount only	12	1	J-STD-020*
Radiation response characterization	Total ionization dose, single-event latch-up	5 units / dose level	1	MIL-STD-883/Method 1019
Outgassing characterization	TML <=1% (Total Mass Lost) CVCM <=0.1% (Collected Volatile Condensable Material)	5	1	ASTM E595

*Precondition performed per JEDEC Std. 22, Method A112/A113.

4 Outgas Test Report

Outgassing test was performed on Die attach and Mold compound used for the ADC3664-SEP. A total mass loss (TML) of 1.00% and collected volatile condensable material (CVCM) of 0.1% were used as screening levels for rejection of spacecraft materials. The outgas test was performed in a vacuum environment of less than 5×10^{-5} Torr according to ASTM E595, for a duration of 24 hours, at 125°C. The TML and CVCM were measured after the test.

Table 4-1. Outgas Test Results

Device	TML < 1.0%	CVCM < 0.1%
ADC3664RSBTSEP	Pass	Pass

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