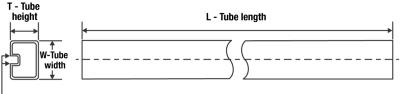


Carrier Material Report for TLIN2029DQ1

Tube Information:



– B - Alignment groove width

Device	Package	Package Type	Pins	SPQ	L (mm)	W (mm)	Τ (μm)	B (mm)
	Drawing							
TLIN2029DQ1	D	SOIC	8	75	517	7.87	635	4.25
TLIN2029DQ1	D	SOIC	8	75	507	8	3940	4.32
TLIN2029DQ1	D	SOIC	8	75	507	8	3940	4.32

Images above are just a representation of the carrier. Actual carrier may vary. Please reference the dimensions provided in the table.

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