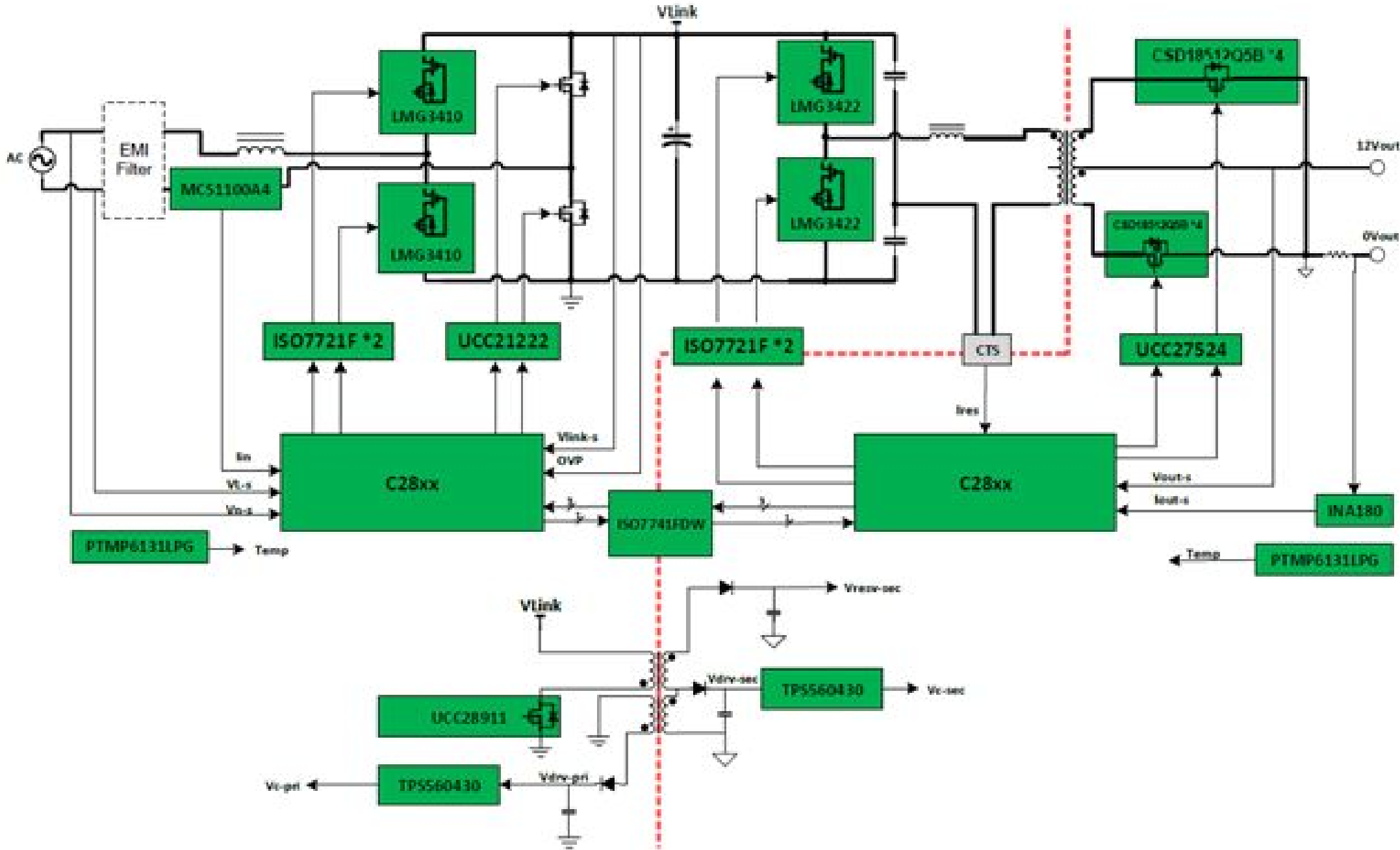


A
B
C
D

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Revision History				
Rev	ECN #	Approved Date	Approved by	Notes
V4.1	N/A	N/A	N/A	Added VCR sense for PMP41006 LLC control
V5.1	N/A	2023-03-14	N/A	changed to LMG3422 on LLC primary side added DCAEF circuits for testing



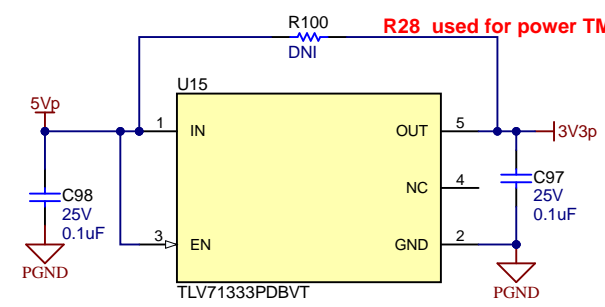
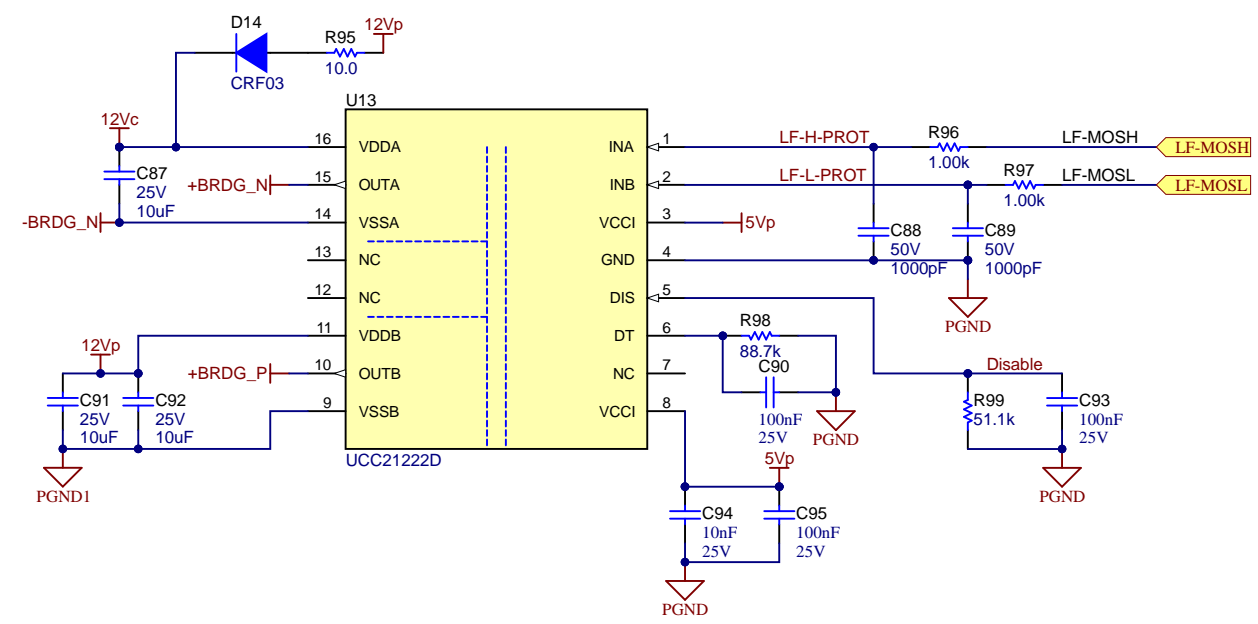
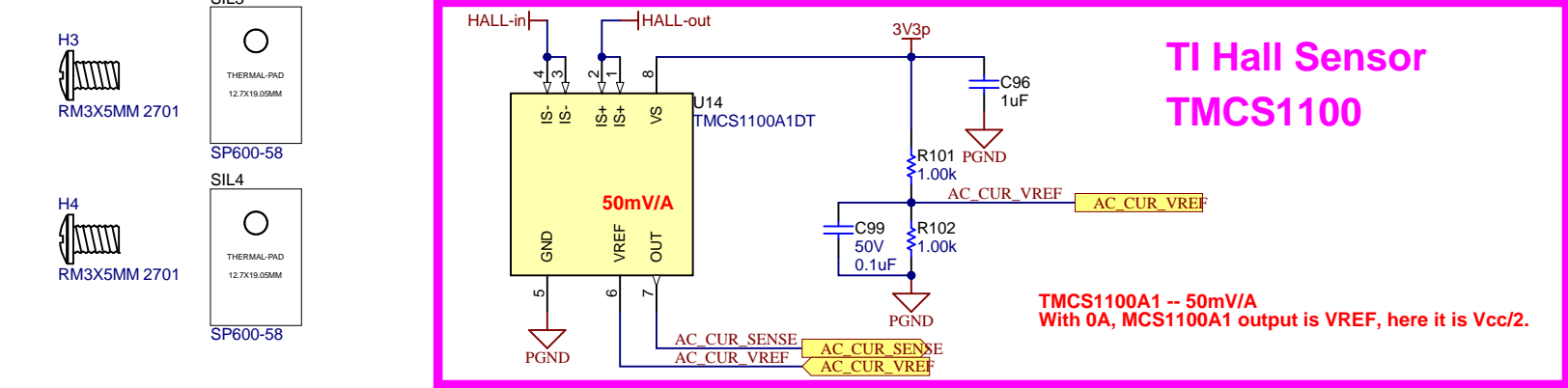
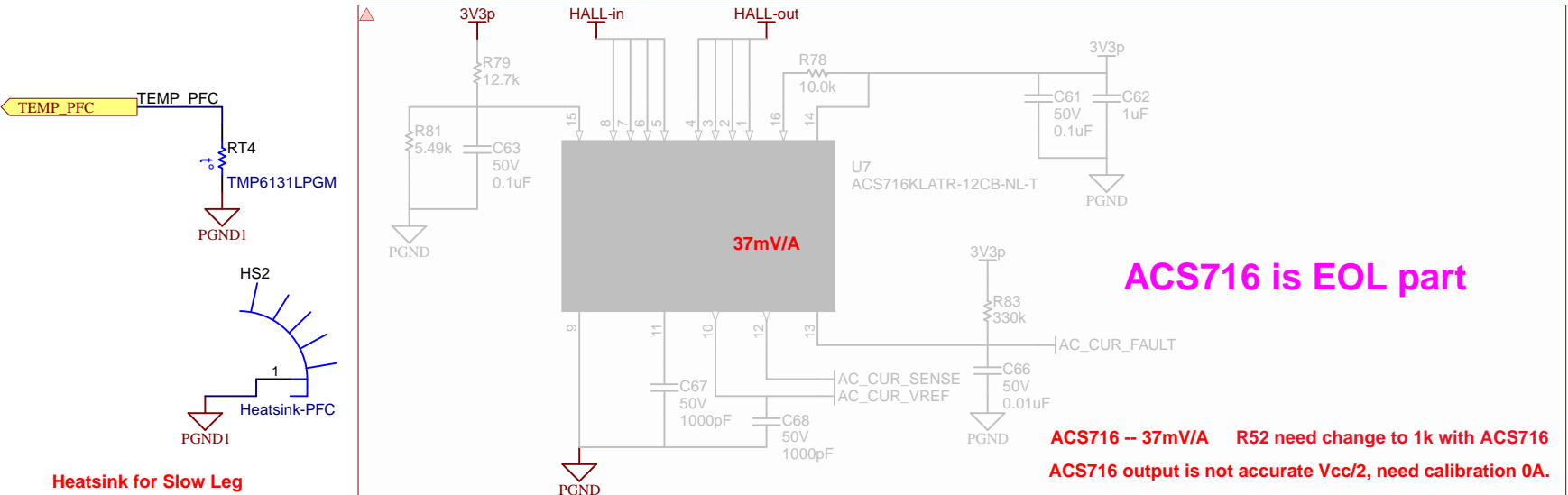
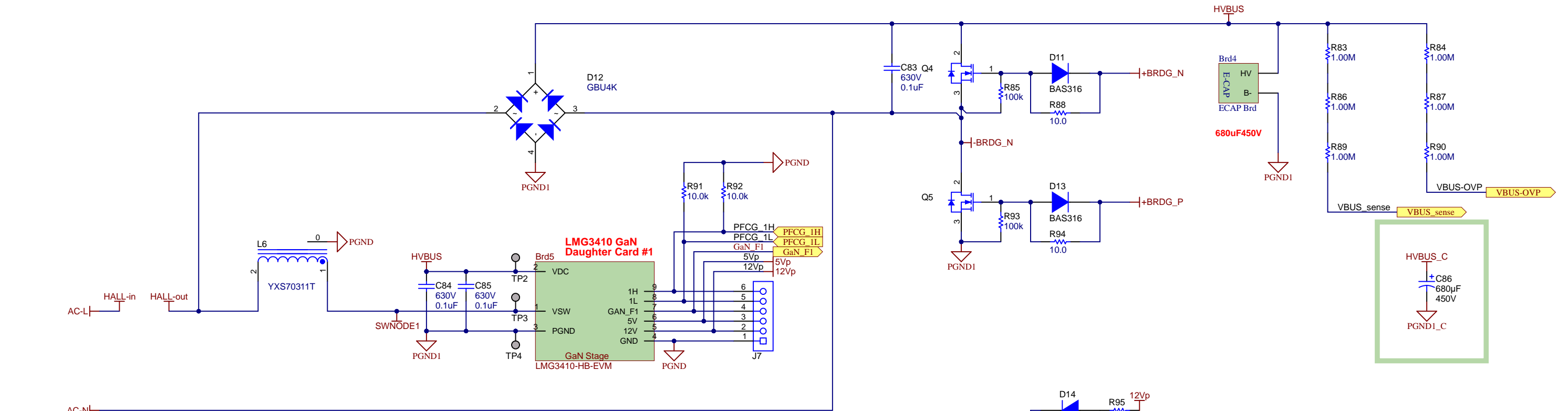
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Orderable: ChangeMe in variant	Designed for: Public Release	Mod. Date: 3/14/2023
TID #: TIDA-010062	Project Title: 1kW Titanium Server PSU	
Number: PMP41006	Rev: V5.1	Sheet Title: Assembly Variant: 001
SVN Rev: Not in version control	File: PMP41006_CoverSheet_SchDoc	Sheet: 1 of 10
Drawn By: Desheng Guo Kevin Zeng	Contact: http://www.ti.com/support	Size: B

123456

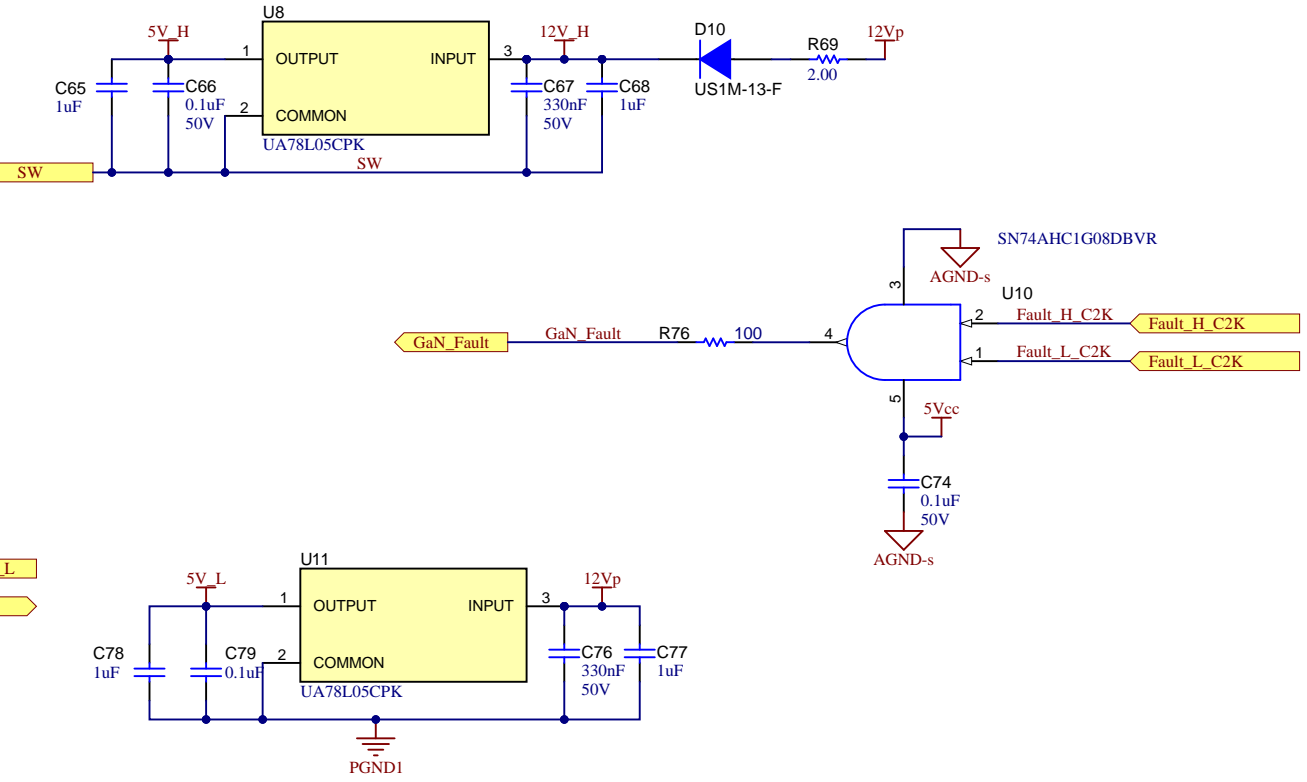
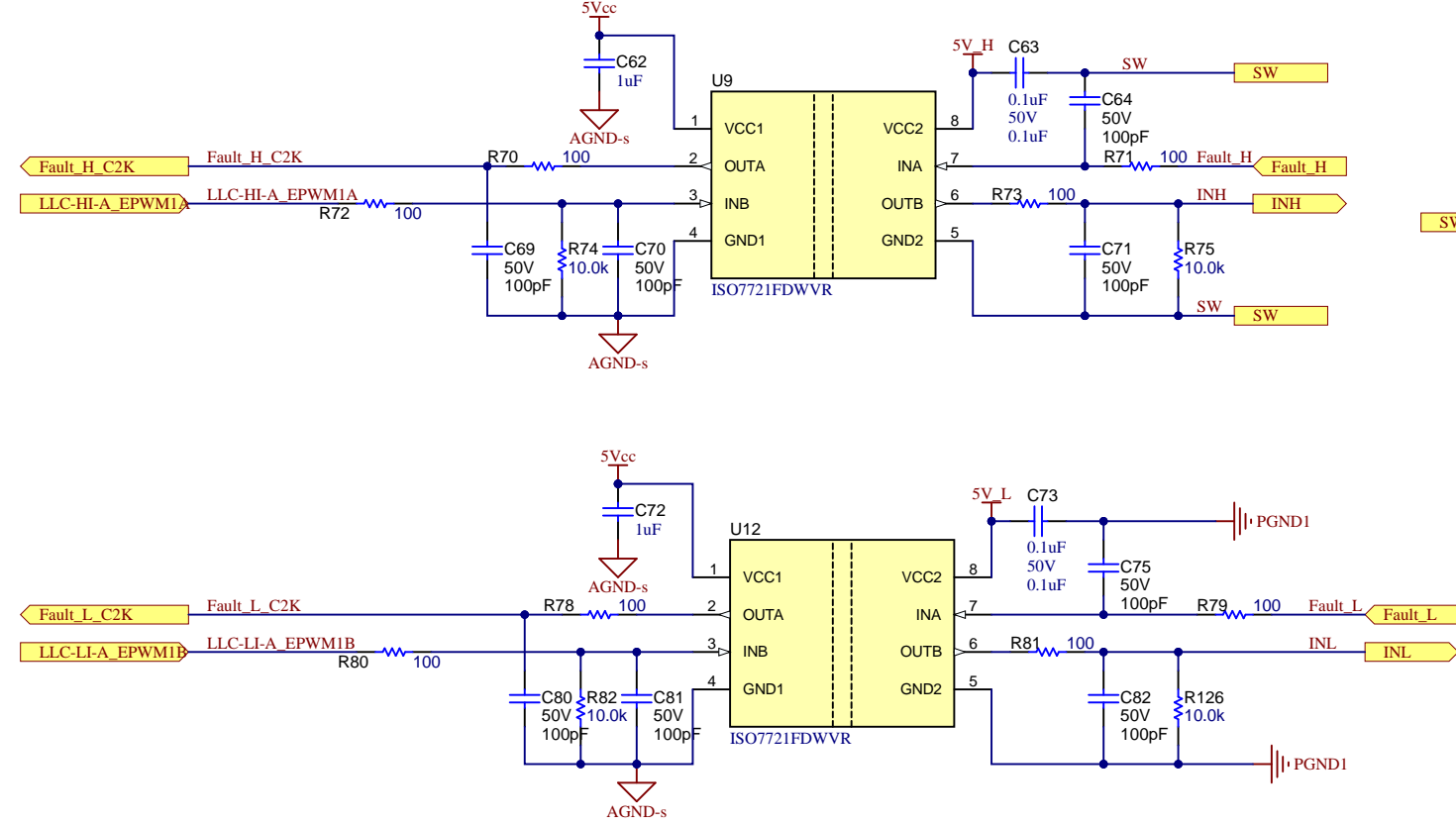
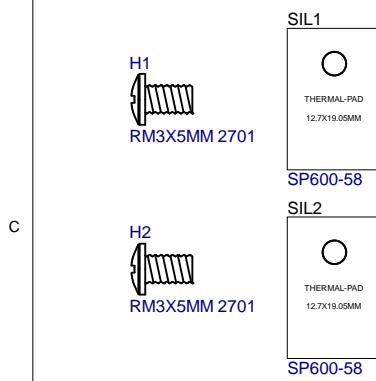
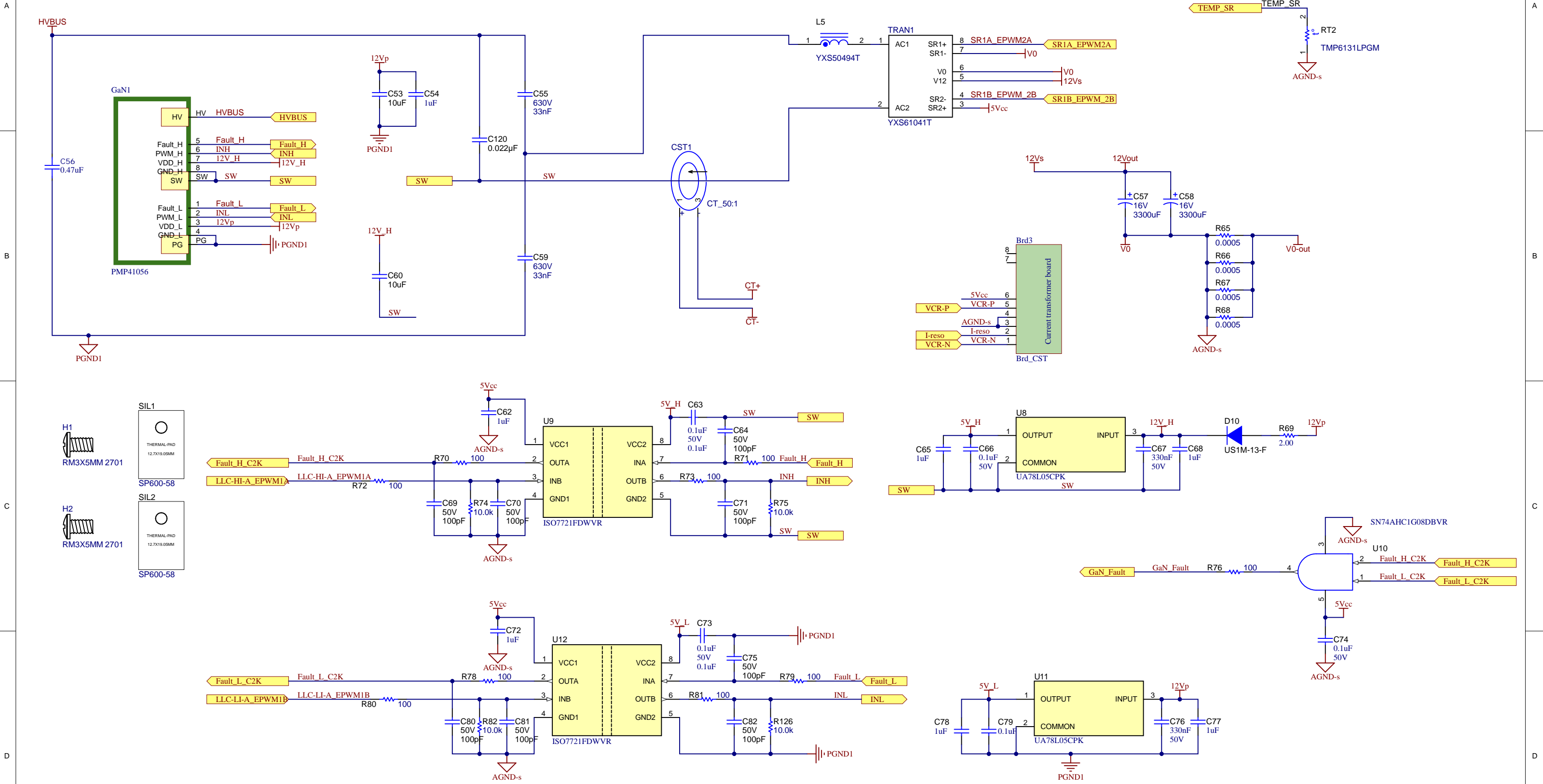
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PFC Power Stages



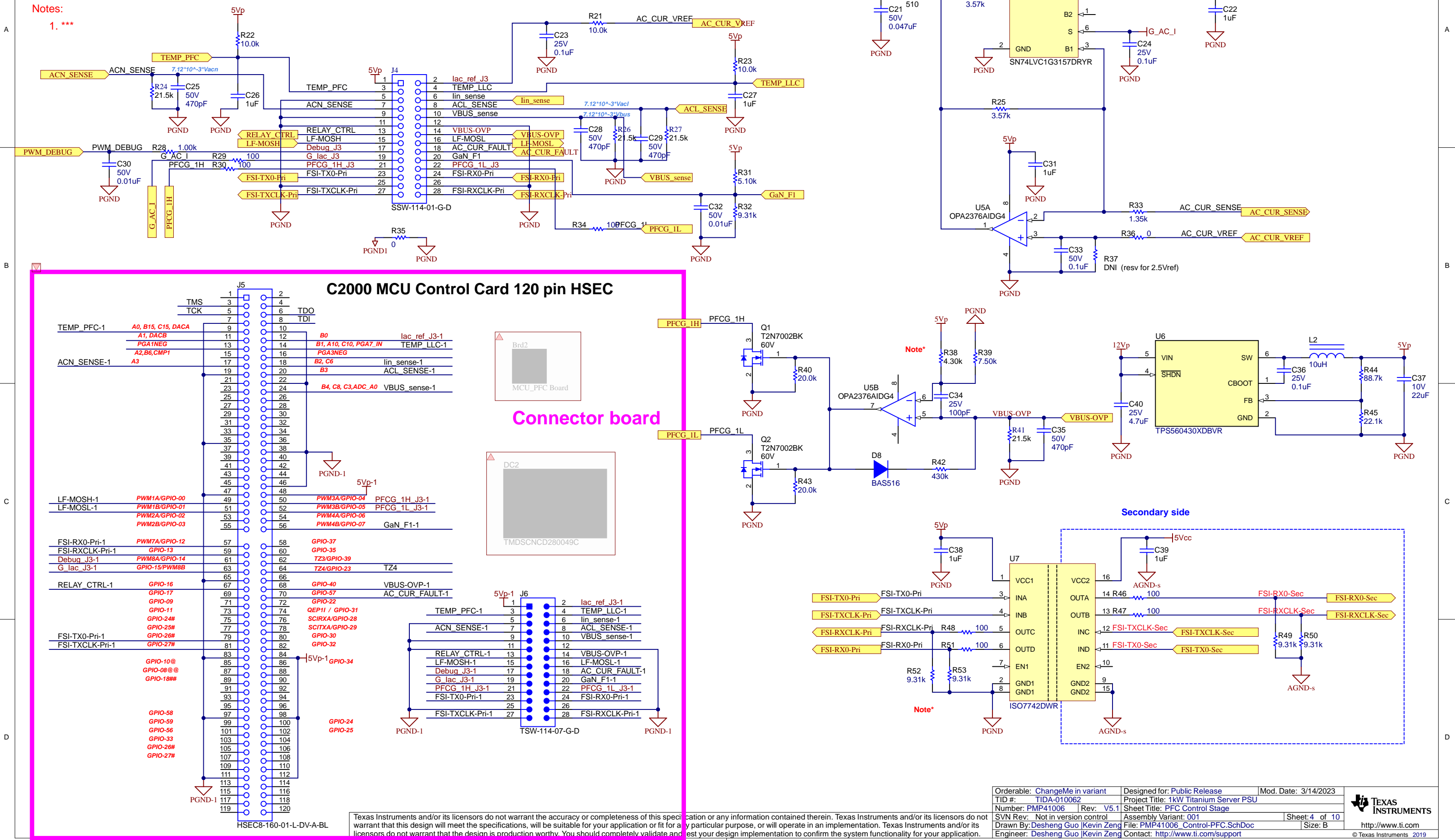
LLC Power Stages

Lr=13uH Lm=200uH Cr=66nF



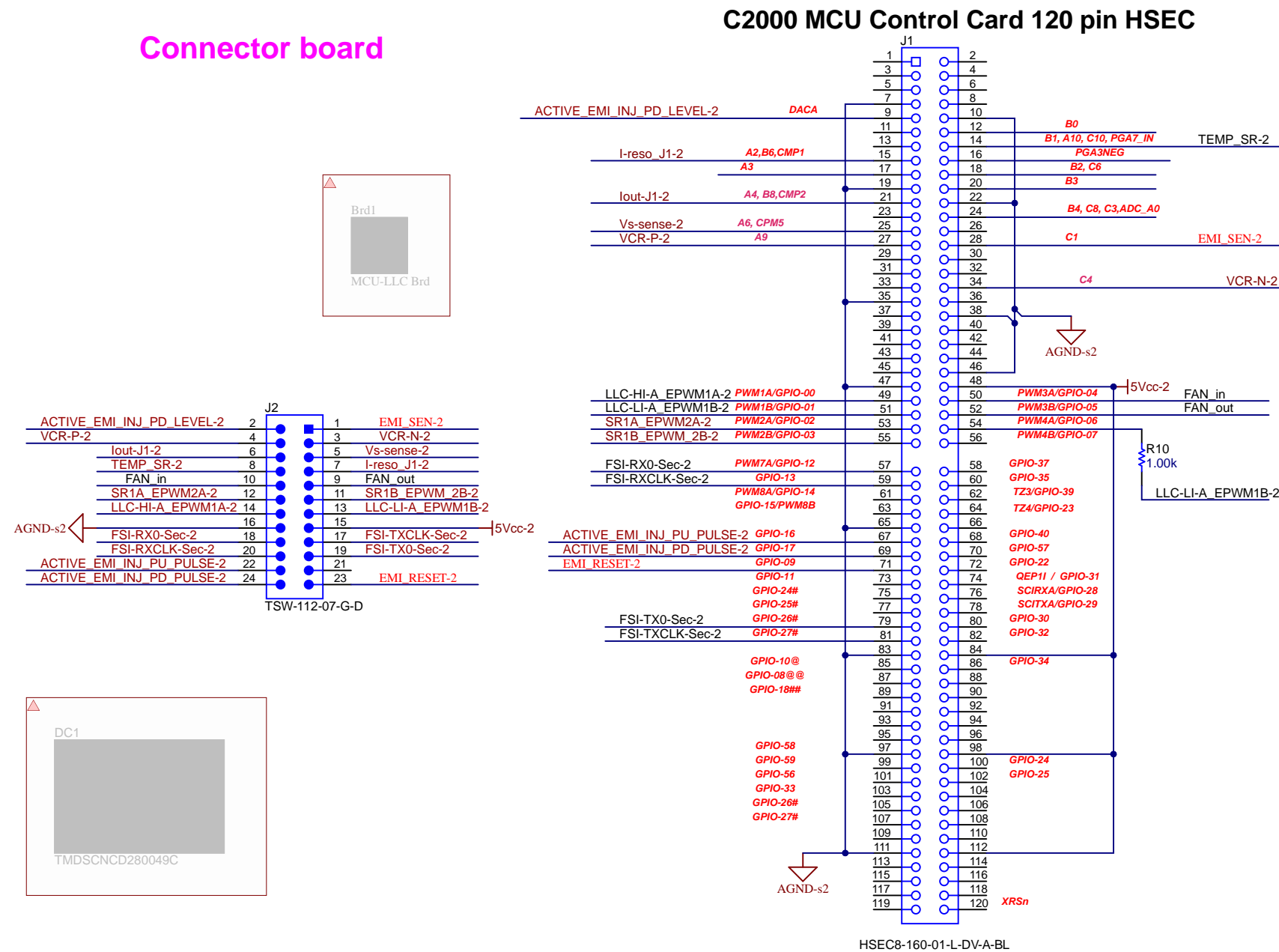
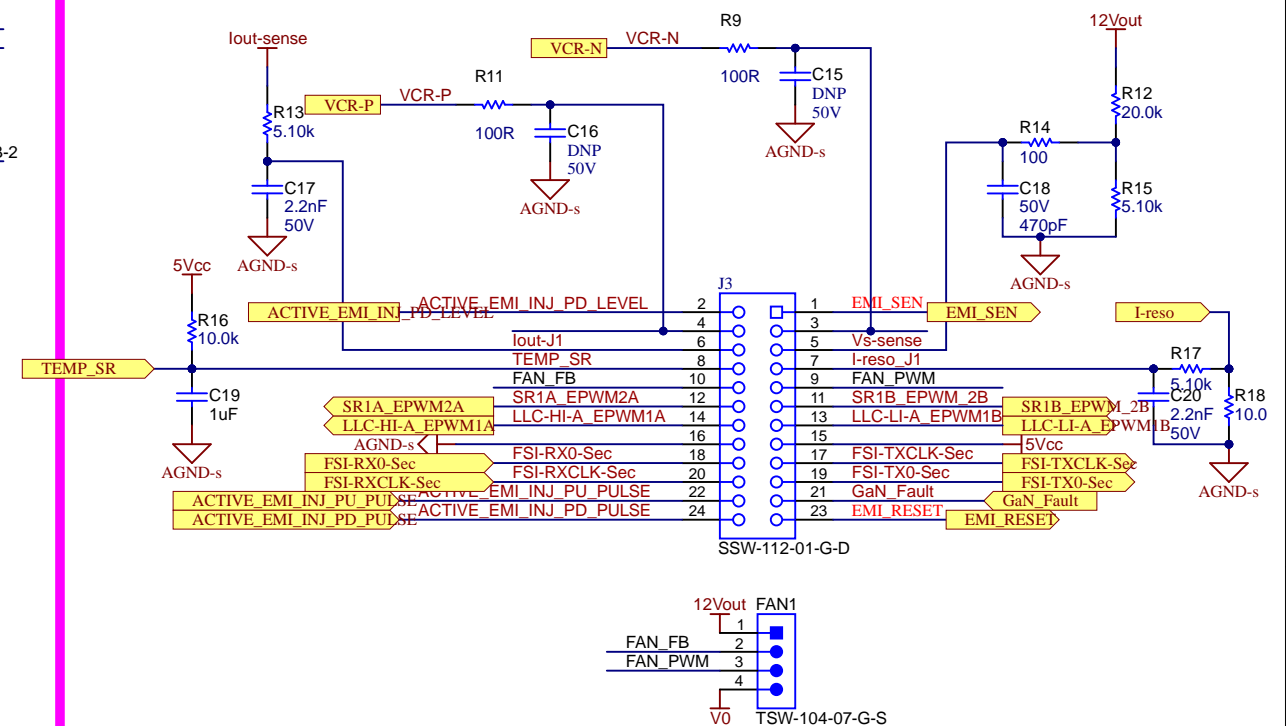
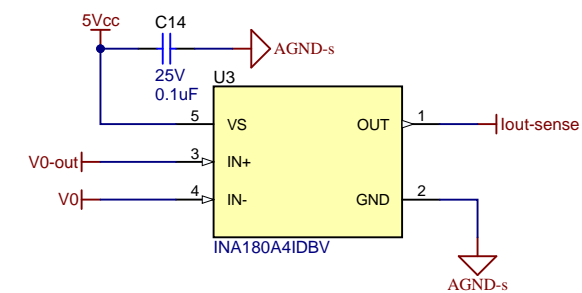
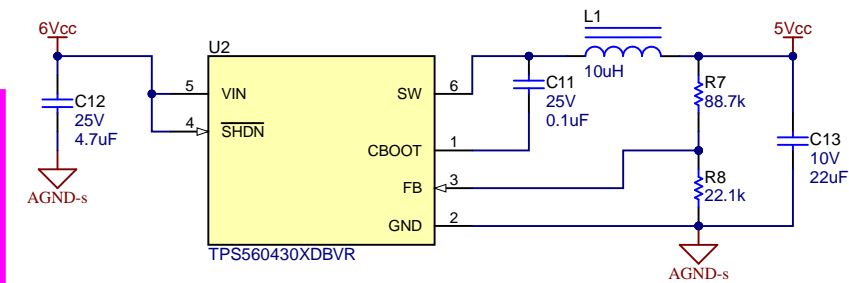
Control for PFC


Notes:
1. ***



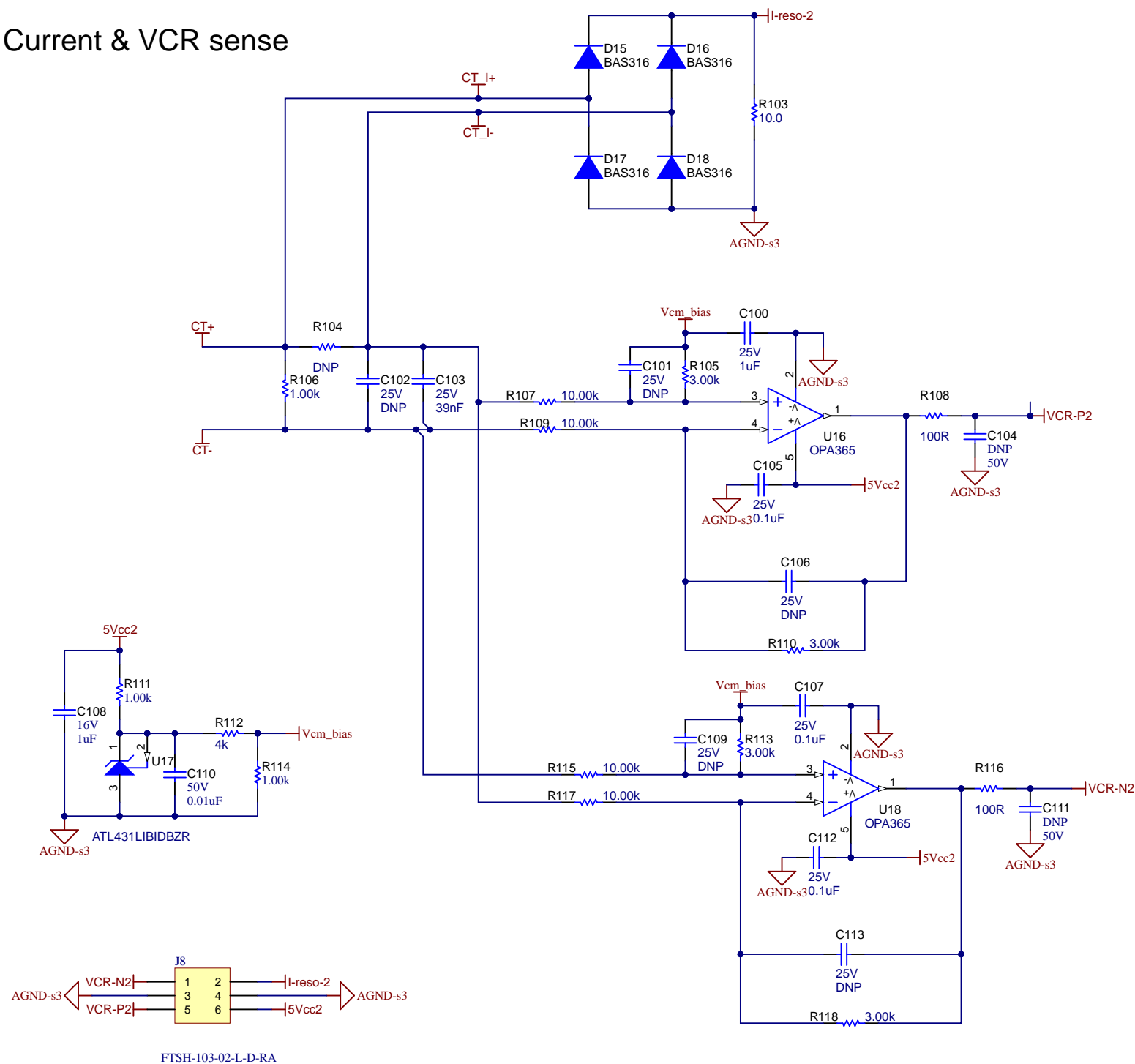
Notes:

1. ***



Orderable: ChangeMe in variant	Designed for: Public Release	Mod. Date: 3/14/2023	 http://www.ti.com © Texas Instruments 2019
TID #: TIDA-010062	Project Title: 1kW Titanium Server PSU		
Number: PMP41006	Rev: V5.1	Sheet Title: LLC Control Stage	
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 5 of 10	
Drawn By: Desheng Guo Kevin Zeng	File: PMP41006_Control-LLC_SchDoc	Size: B	
Engineer: Desheng Guo Kevin Zeng	Contact: http://www.ti.com/support		

LLC Current & VCR sense



VCR sensing design process:

1, get the peak-peak voltage of the Cr (resonant CAP) @ min Vin and full load;

2, select the CT and its turn ratio N:1;

3, select Cs (sensing CAP) by keeping the CT within its volt-sec;

$$V_{s_pp} = V_{cr_pp} * Cr / (N * Cs)$$

4, select Ks (ratio of OPA circuits) to adjust the sensing volt to 2.0V max;

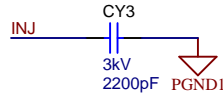
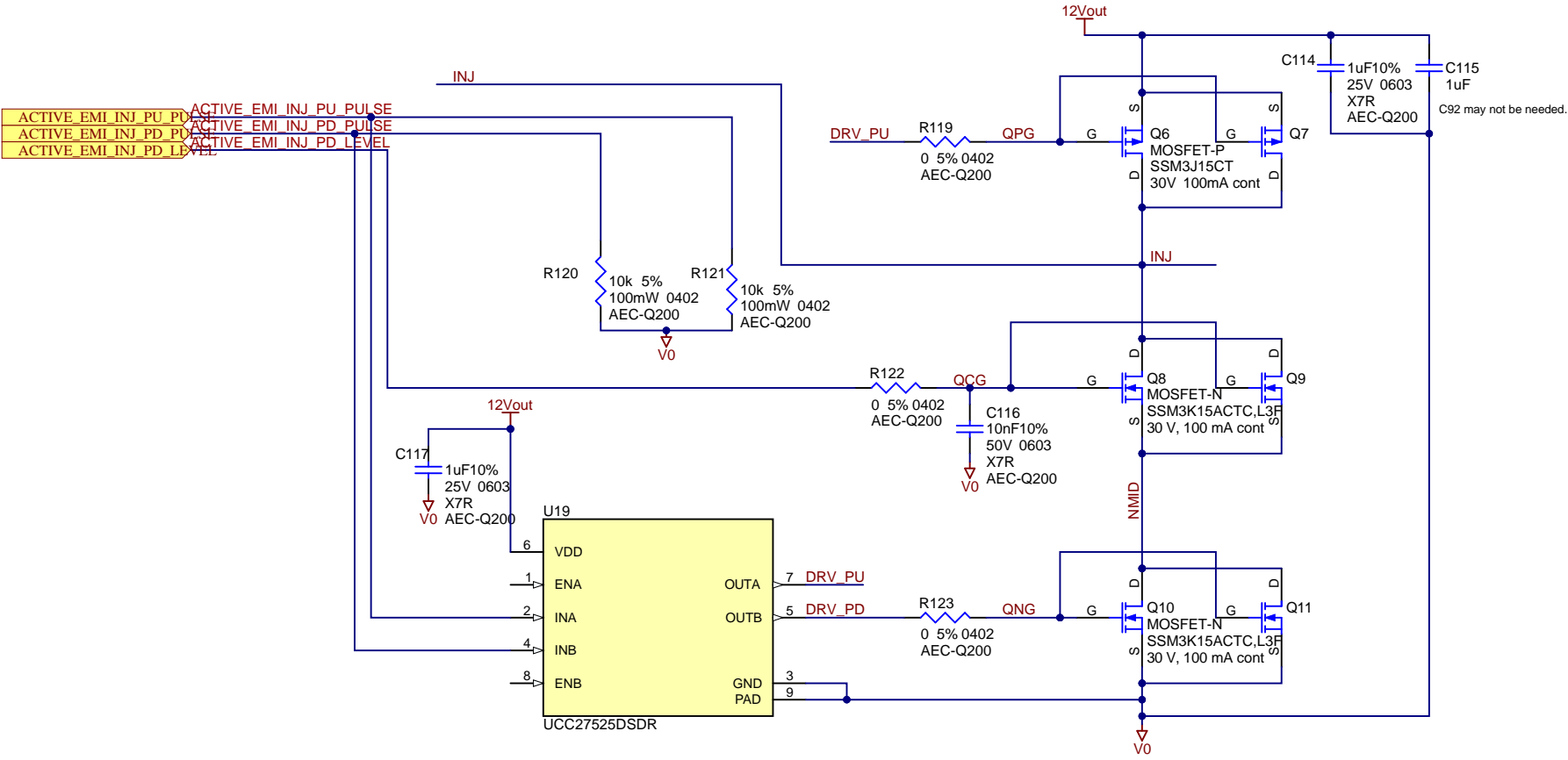
$$V_{CR_sense_p} = V_{cm} + 0.5 * V_{cr_pp} * Cr / (N * Cs) * K_s$$

Min volt is clamped by Vcm, and Vcm is set at 0.5V by default.

Discrete Canceller Active EMI Filter

Discrete Canceller Inject Daughter Card with Non-Automotive Devices

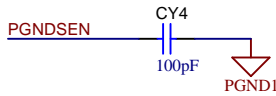
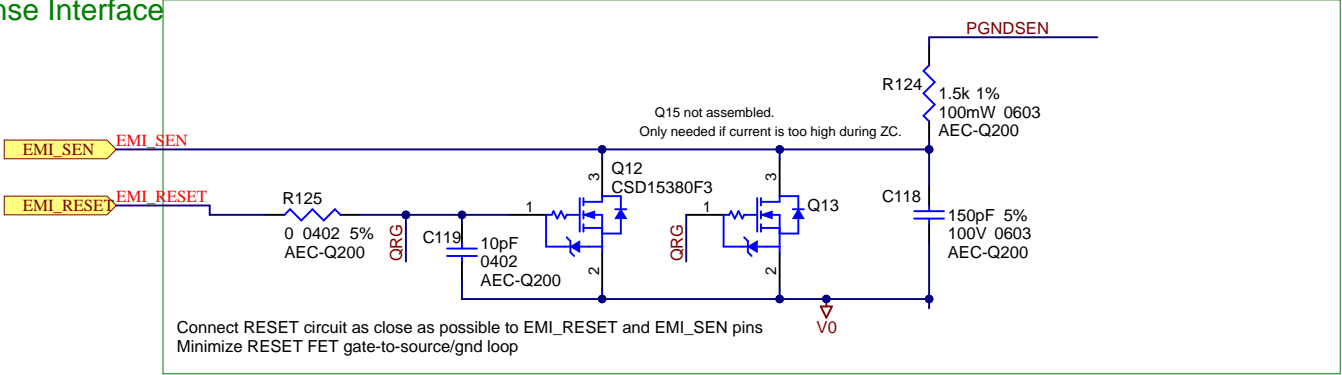
Connect inject card close to high-frequency switching bridge



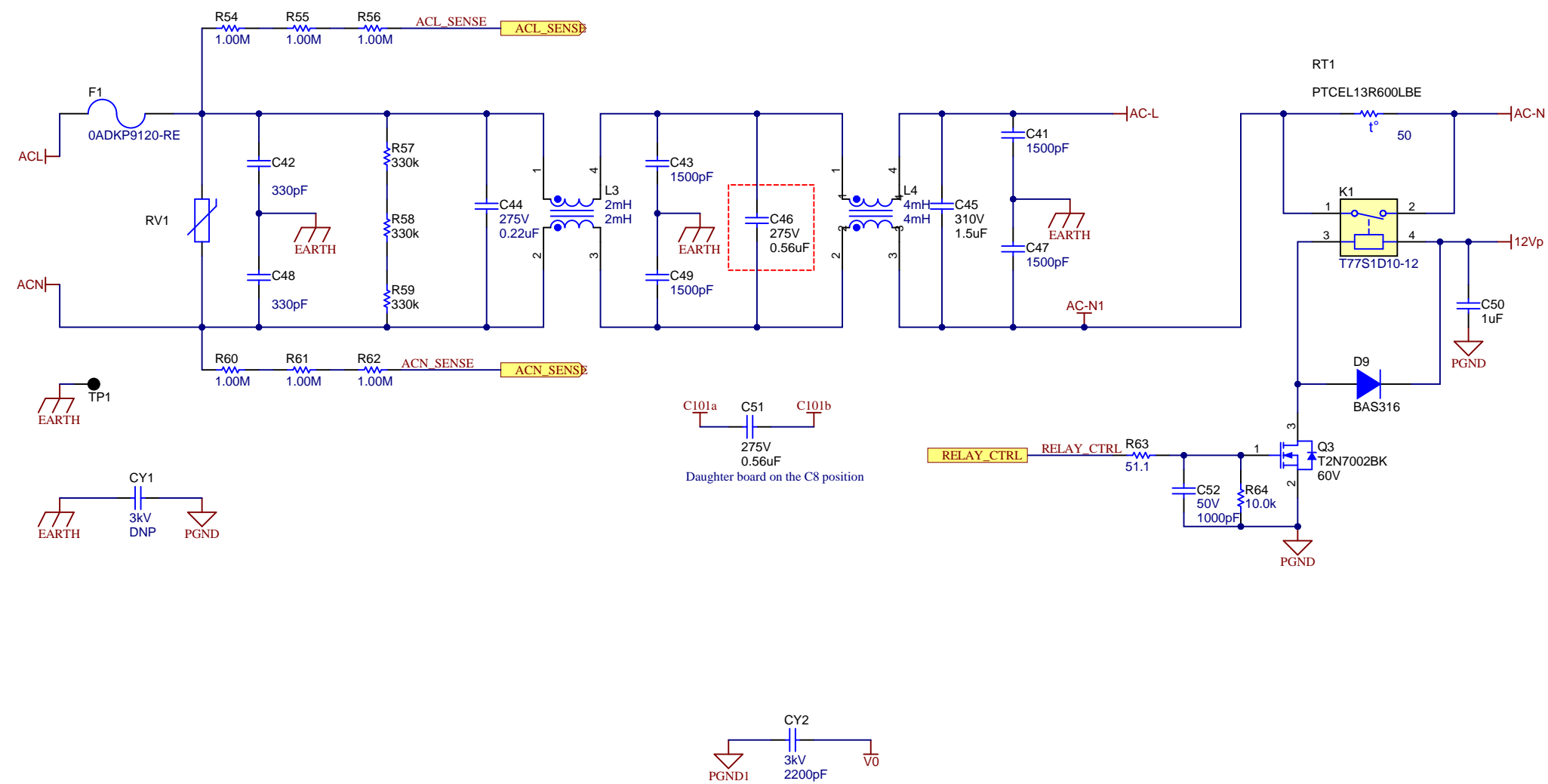
C2000 Sense Interface

Sense Card

Connect sense card close to C2000 control card



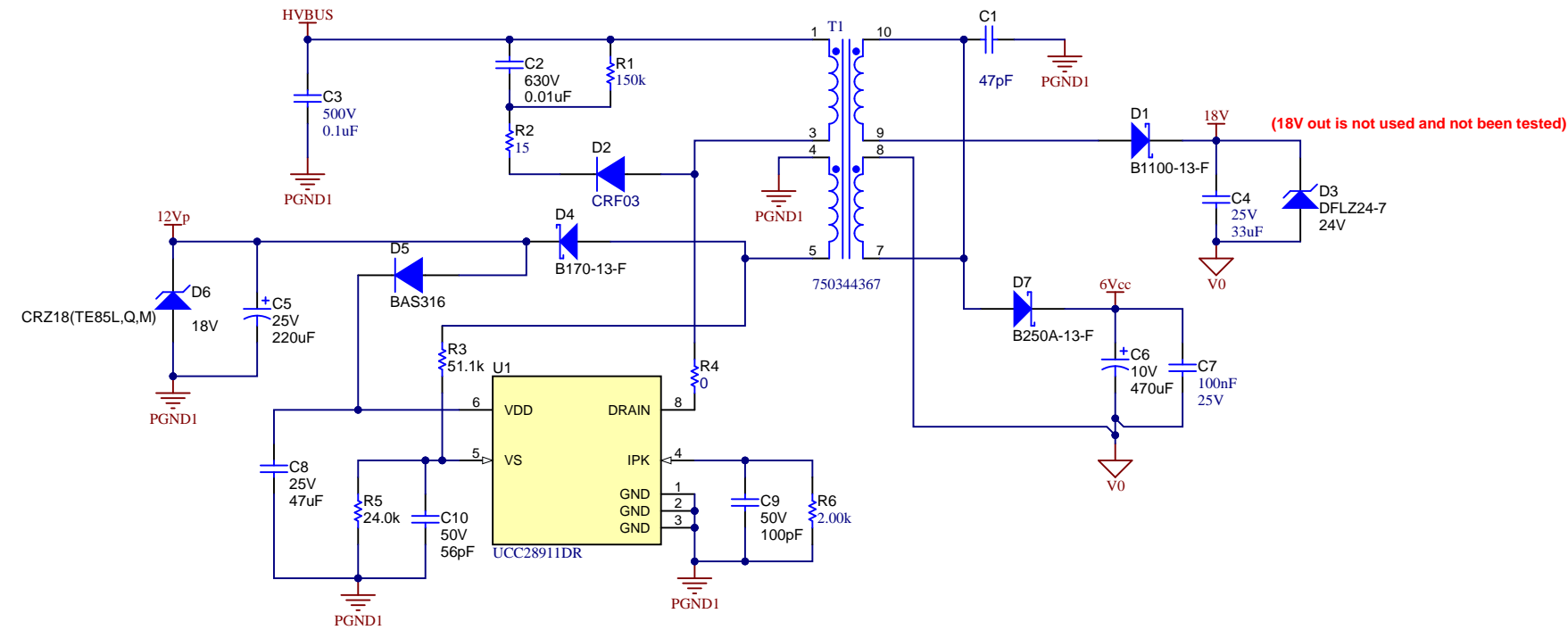
EMI Part



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Aux Power Stage



LOGO1



DANGER HIGH VOLTAGE




LOGO2



CAUTION HOT SURFACE

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		Designed for: Public Release	Mod. Date: 3/14/2023	 TEXAS INSTRUMENTS http://www.ti.com © Texas Instruments 2019
		Project Title: 1kW Titanium Server PSU		
		Sheet Title: Bias Power Stage		
Number: PMP41006	Rev: V5.1			
SVN Rev: Not in version control		Assembly Variant: 001	Sheet: 9 of 10	
Drawn By: Desheng Guo		File: PMP41006_Bias_SchDoc	Size: B	
Engineer: Desheng Guo Kevin Zeng		Contact: http://www.ti.com/support		



PCB Number: PMP41006
PCB Rev: V5.1

LBL1
PCB Label
THT-14-423-10
Size: 0.65" x 0.20 "

ZZ1
Label Assembly Note
This Assembly Note is for PCB labels only

ZZ2
Assembly Note
These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3
Assembly Note
These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4
Assembly Note
These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

Variant/Label Table	
Variant	Label Text
001	TIDA-010062
002	PMP41006