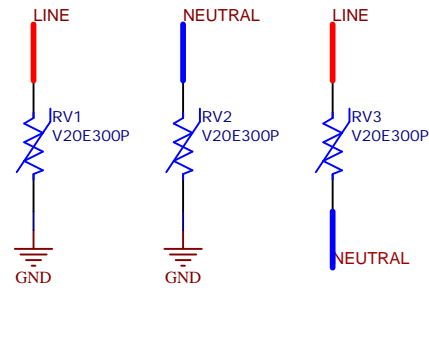
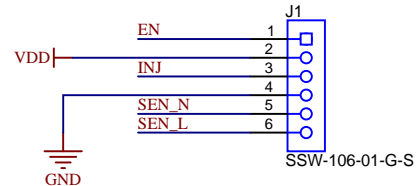


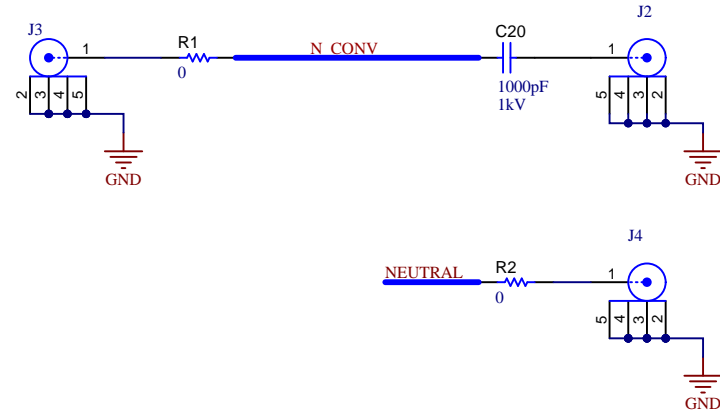
SURGE PROTECTION
MAX LINE VOLTAGE: 305Vrms



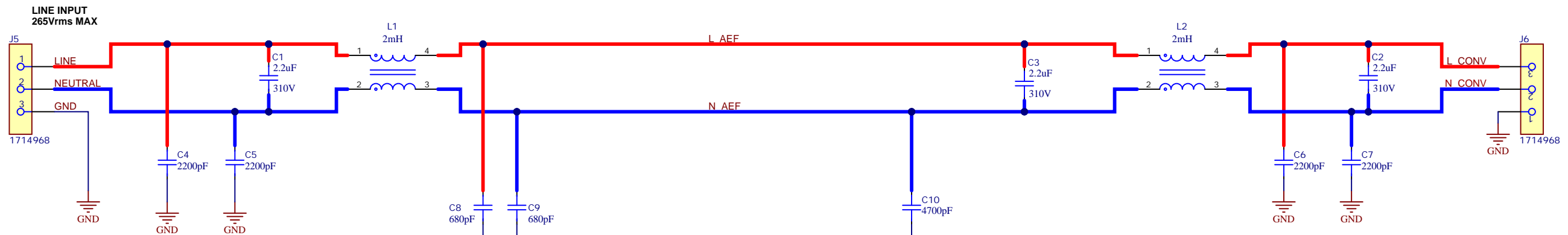
TPSF12C1 EVM INTERFACE



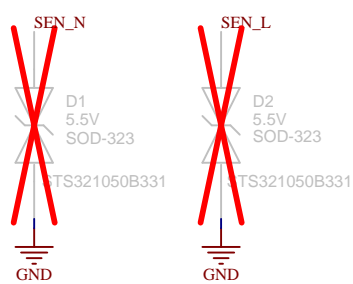
VALIDATION INTERFACE



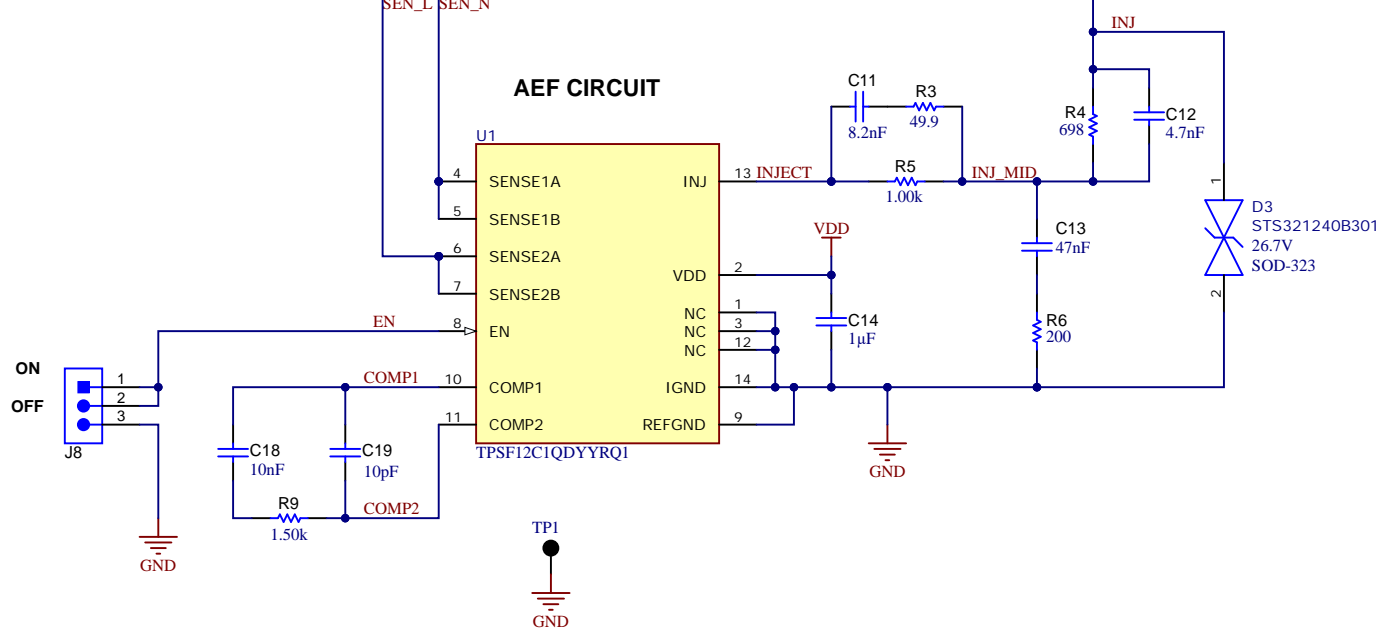
MAIN LINE/NEUTRAL INPUTS



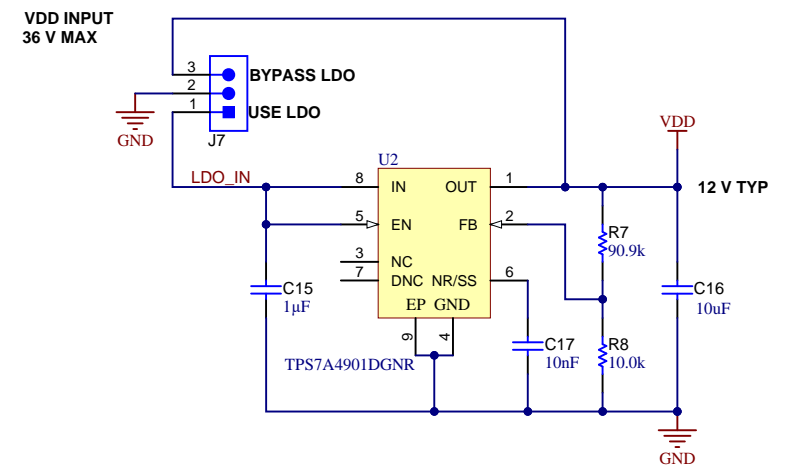
**HIGH VOLTAGE
LOW VOLTAGE**



AEF CIRCUIT



OPTIONAL EXTERNAL LDO



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Orderable: TPSF12C1EVM-FILTER	Designed for:	Mod. Date: 6/30/2023
TID #:	Project Title: TPSF12C1EVM-FILTER	
Number: SR005	Rev: A	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 1 of 1
Drawn By: T. Jacobs	File: SR005A.SchDoc	Size: A3
Engineer: Tim Hegarty	Contact:	

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