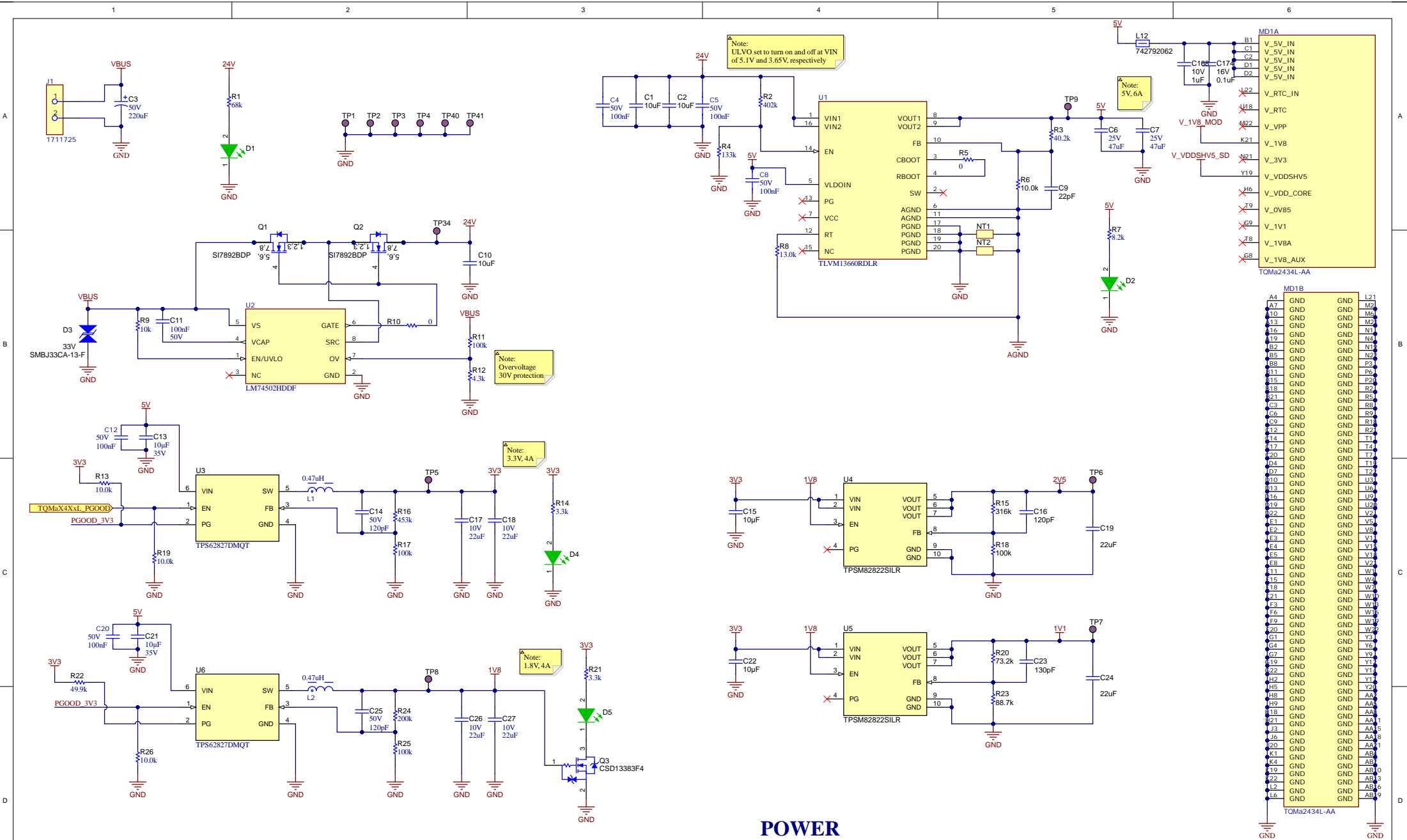


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Orderable: N/A	Designed for: Public Release	Mod. Date: 12/20/2024
TID #: TIDA-010948	Project Title: TIDA-010948 Control Board	
Number: TIDA-010948 CB Rev: 1.0	Sheet Title:	
SVN Rev: Not in version control	Assembly Variant: Variant of TIDA-010948-CB	Sheet: 1 of 11
Drawn By: Chen Gao	File: TIDA-010948-CB-BlockDiagram.SchDoc	Size: B
Engineer: Chen Gao	Contact: http://www.ti.com/support	

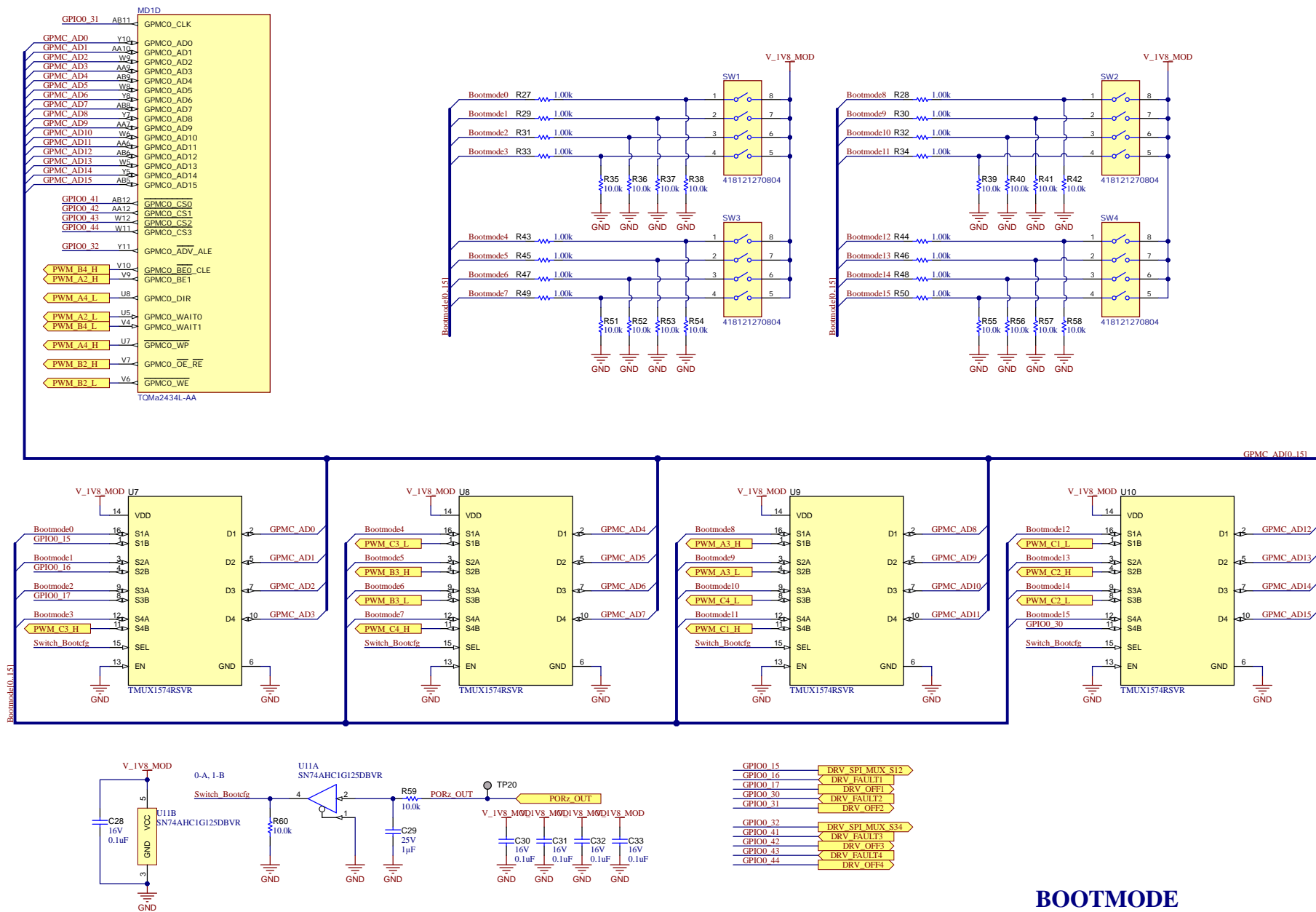


POWER

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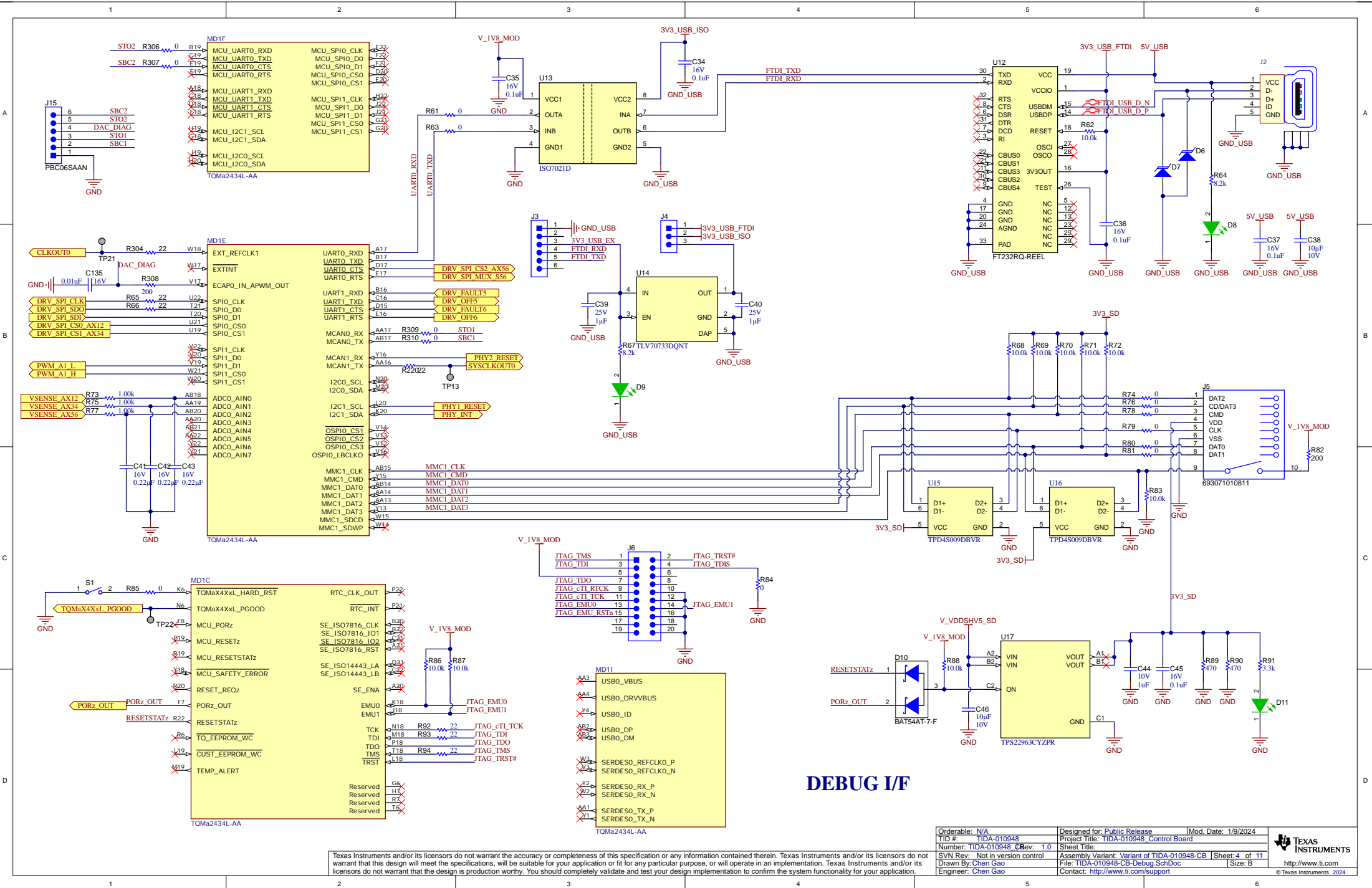
Orderable: N/A	Designed for: Public Release	Mod. Date: 1/11/2024
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Number: TIDA-010948	Rev: 1.0	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: Variant of TIDA-010948-CB	Sheet 2 of 11
Drawn By: Chen Gao	File: TIDA-010948-CB-Power.SchDoc	Size: B
Engineer: Chen Gao	Contact: http://www.ti.com/support	





BOOTMODE

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TID #: TIDA-010948	Project Title: TIDA-010948 Control Board	
Number: TIDA-010948 CB Rev: 1.0	Sheet Title:	
SVN Rev: Not in version control	Assembly Variant: Variant of TIDA-010948-CB	Sheet: 3 of 11
Drawn By: Chen Gao	File: TIDA-010948-CB-Bootmode SchDoc	Size: B
Engineer: Chen Gao	Contact: http://www.ti.com/support	

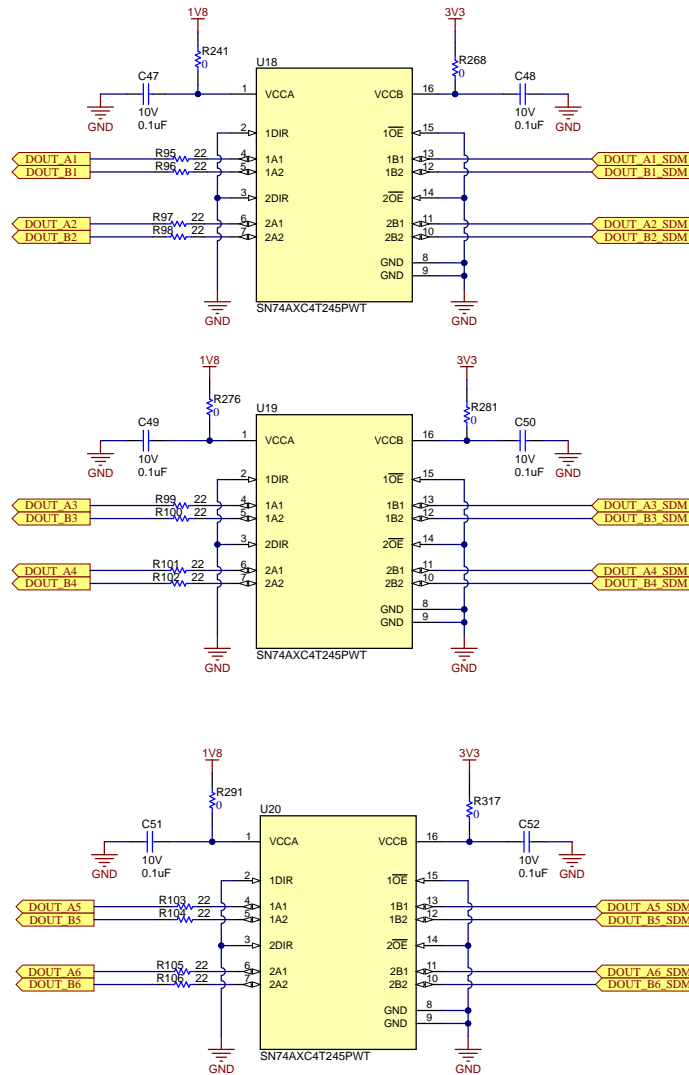


DEBUG I/F

Orderable: N/A	Designed for: Public Release	Mod. Date: 1/9/2024
TID #: TIDA-010948	Project Title: TIDA-010948 Control Board	
Number: TIDA-010948-08 Rev: 1.0	Sheet Title:	
SVN Rev: Not in version control	Assembly Variant: Variant of TIDA-010948-CB	Sheet 4 of 11
Drawn By: Chen Gao	File: TIDA-010948-CB-Debug_SchDoc	Size: B
Engineer: Chen Gao	Contact: http://www.ti.com/support	© Texas Instruments 2024

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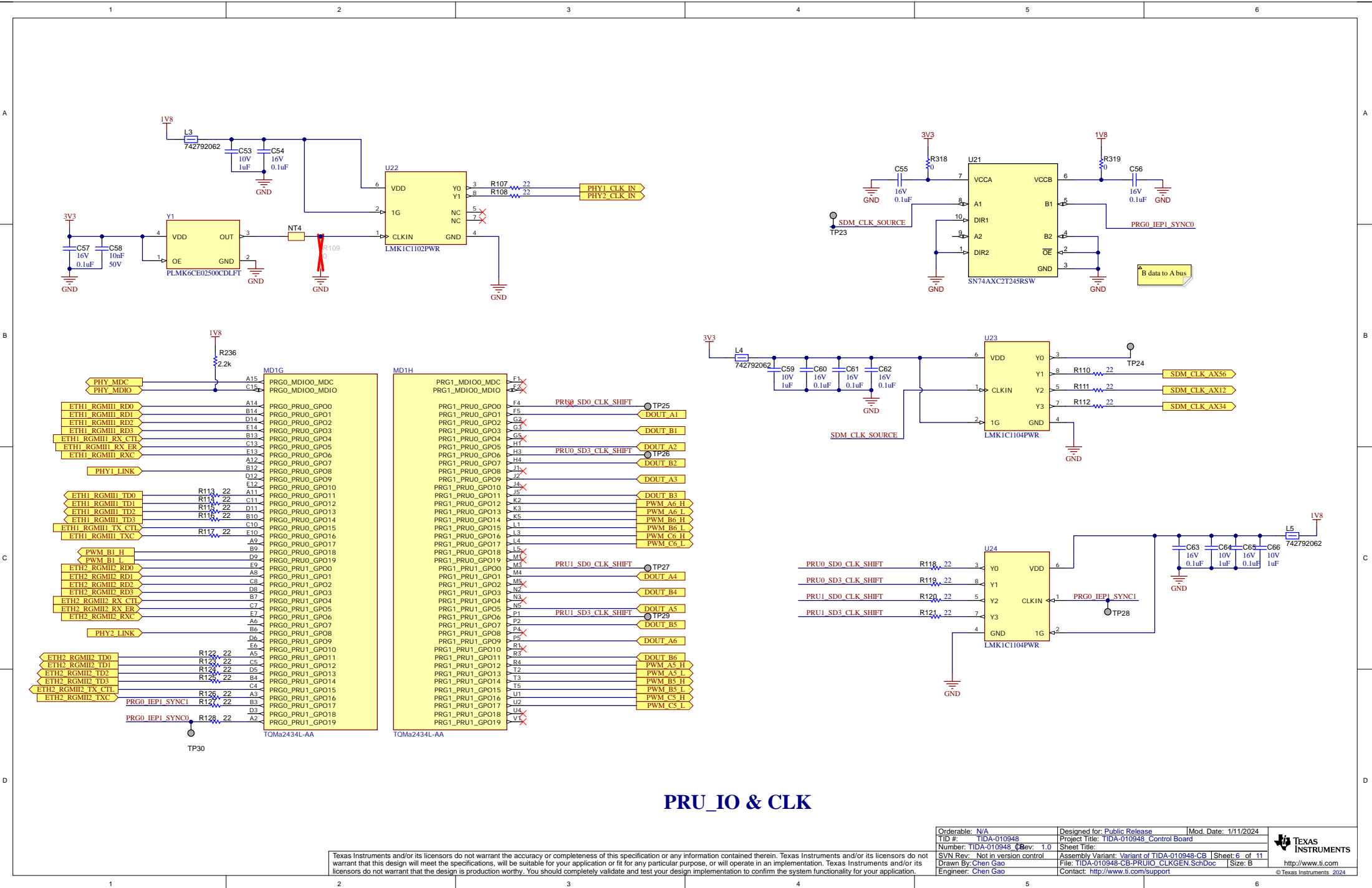
SDM LEVEL SHIFTER

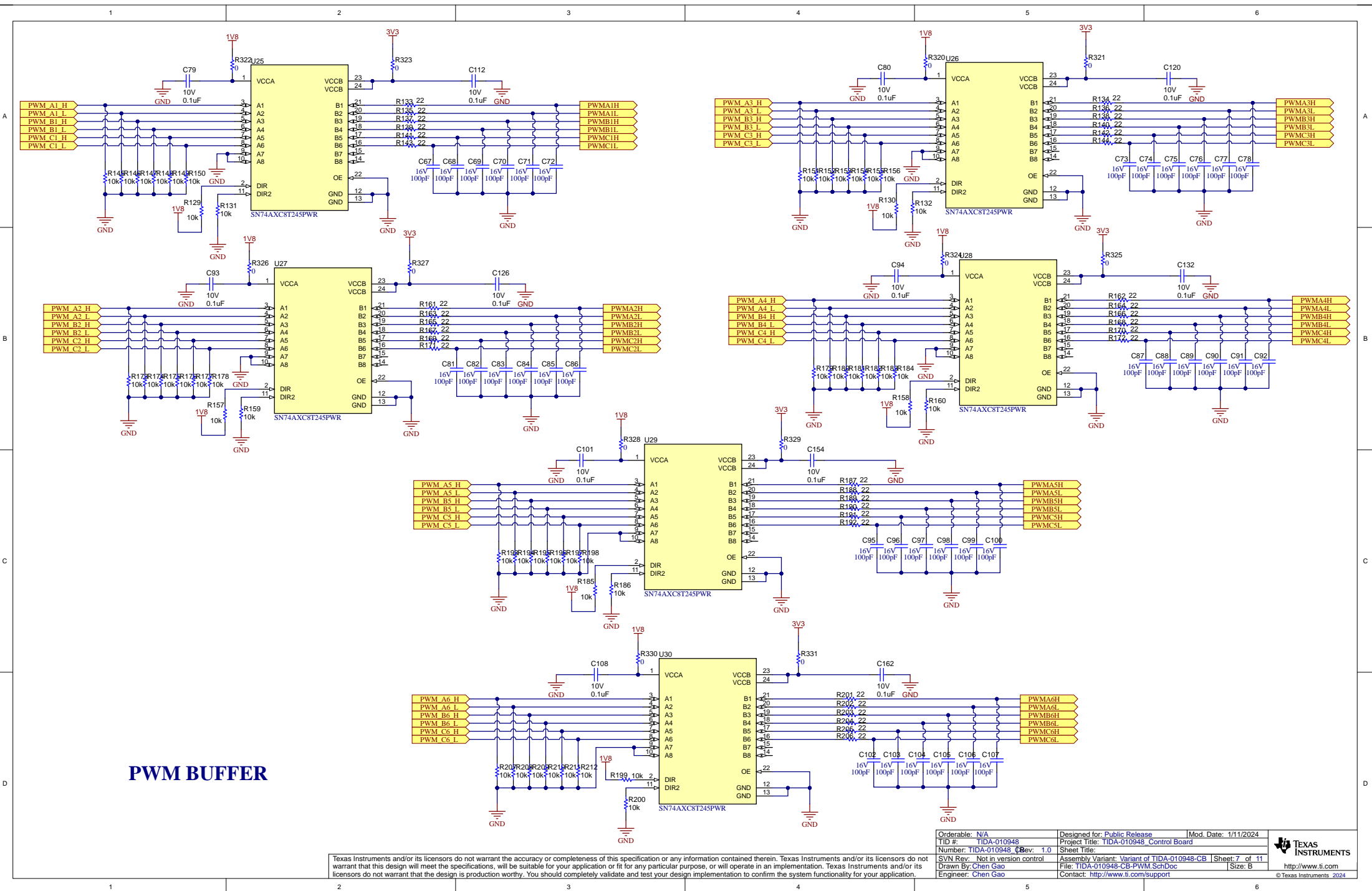


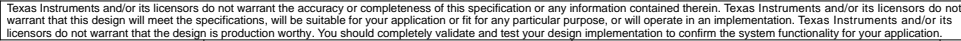
A
B data to A bus

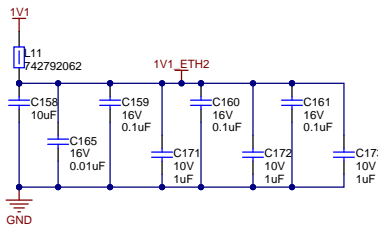
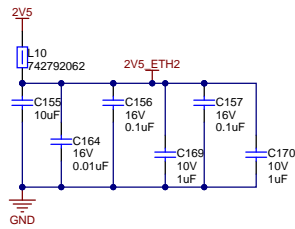
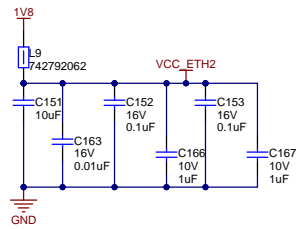
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TID #: TIDA-010948	Project Title: TIDA-010948 Control Board	
Number: TIDA-010948-CB Rev: 1.0	Sheet Title:	
SVN Rev: Not in version control	Assembly Variant: Variant of TIDA-010948-CB	Sheet 5 of 11
Drawn By: Chen Gao	File: TIDA-010948-CB-SDFM.SchDoc	Size: B
Engineer: Chen Gao	Contact: http://www.ti.com/support	



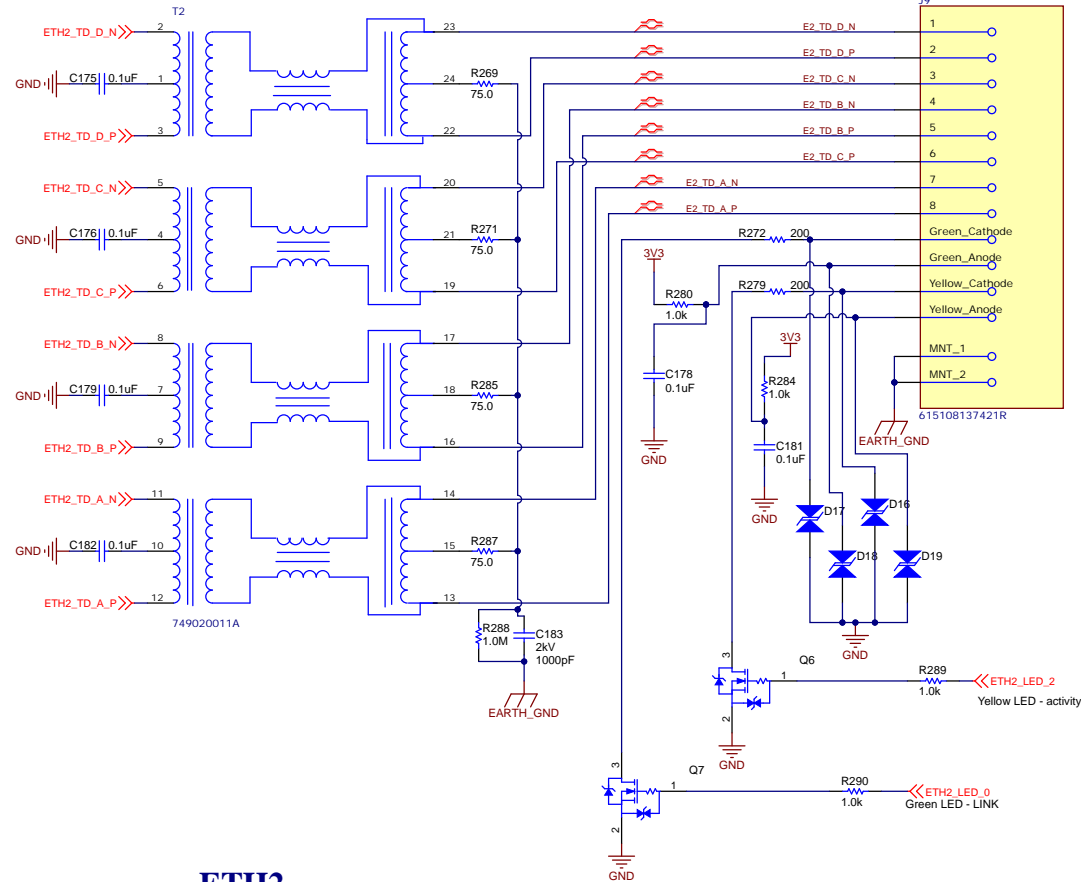
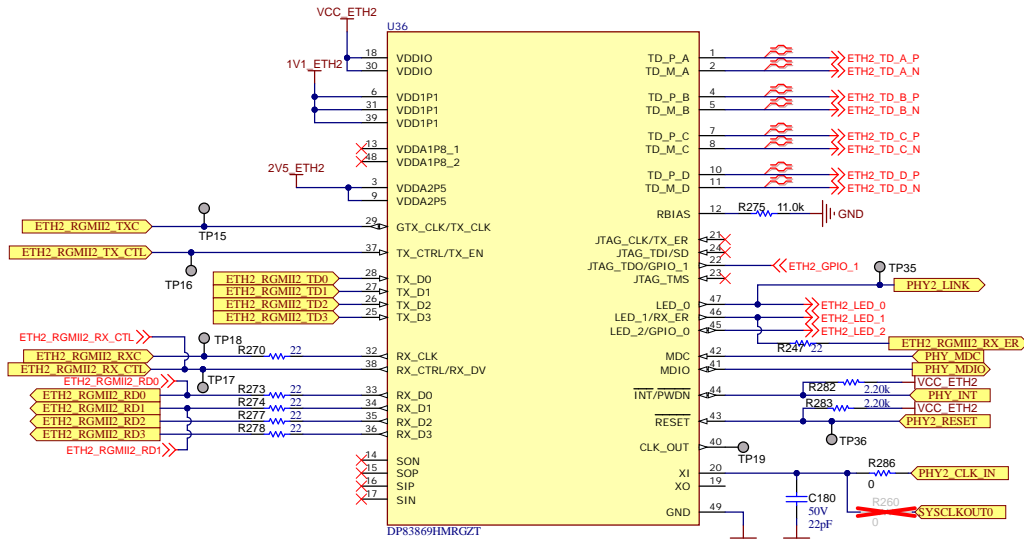
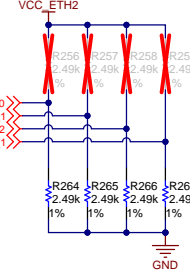
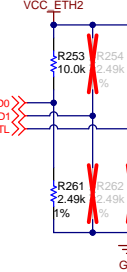






Mode 1
Mode 0
Mode 1

ETH2 Default:
Address 0x1;
AutoNeg Enable;
10/100/1000;
Mirror Enable

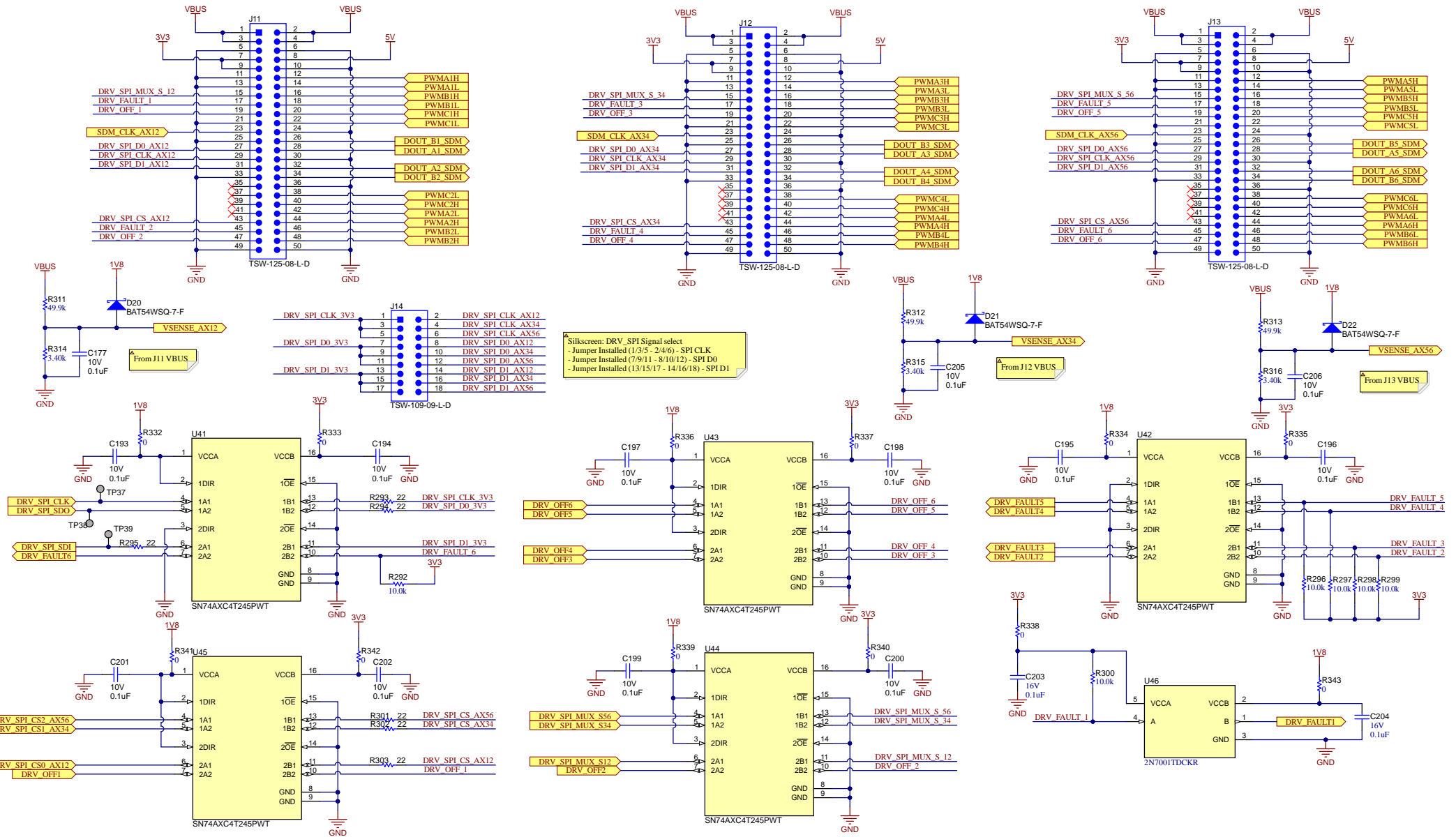


ETH2

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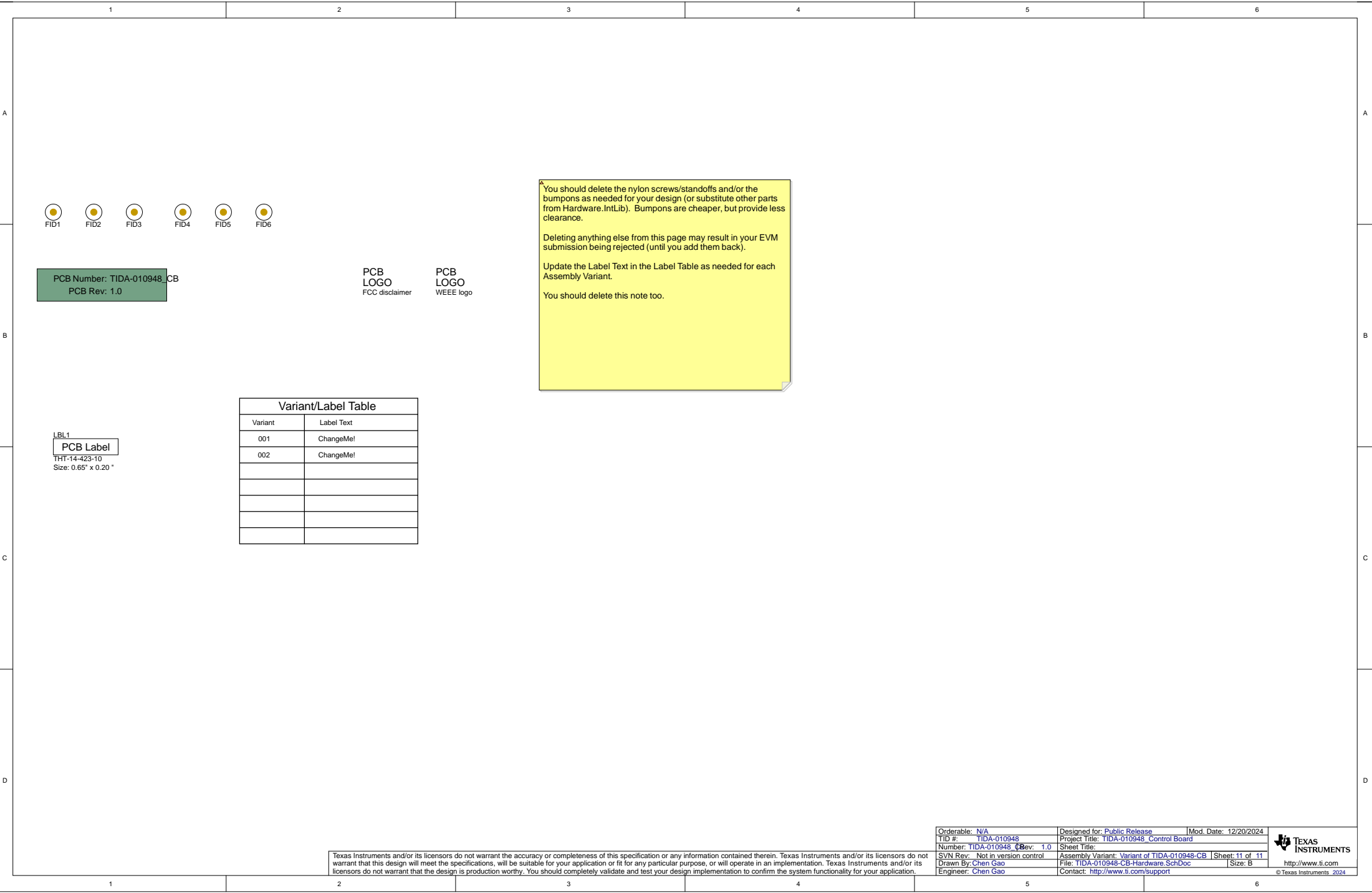
Orderable: N/A	Designed for: Public Release	Mod. Date: 12/27/2023
TID #: TIDA-010948	Project Title: TIDA-010948 Control Board	
Number: TIDA-010948	Rev: 1.0	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: Variant of TIDA-010948-CB	Sheet: 9 of 11
Drawn By: Chen Gao	File: TIDA-010948-CB-ethernet2_SchDoc	Size: B
Engineer: Chen Gao	Contact: http://www.ti.com/support	

Connector to 3 BackPlane Daughter Board



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TID #: TIDA-010948	Project Title: TIDA-010948 Control Board	
Number: TIDA-010948 Rev: 1.0	Sheet Title:	
SVN Rev: Not in version control	Assembly Variant: Variant of TIDA-010948-CB	Sheet: 10 of 11
Drawn By: Chen Gao	File: TIDA-010948-CB-Interface_SchDoc	Size: B
Engineer: Chen Gao	Contact: http://www.ti.com/support	



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TID #: TIDA-010948	Project Title: TIDA-010948_Control Board		
Number: TIDA-010948_CB Rev: 1.0	Sheet Title:		
SVN Rev: Not in version control	Assembly Variant: Variant of TIDA-010948-CB	Sheet: 11 of 11	
Drawn By: Chen Gao	File: TIDA-010948-CB-Hardware.SchDoc	Size: B	
Engineer: Chen Gao	Contact: http://www.ti.com/support		

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