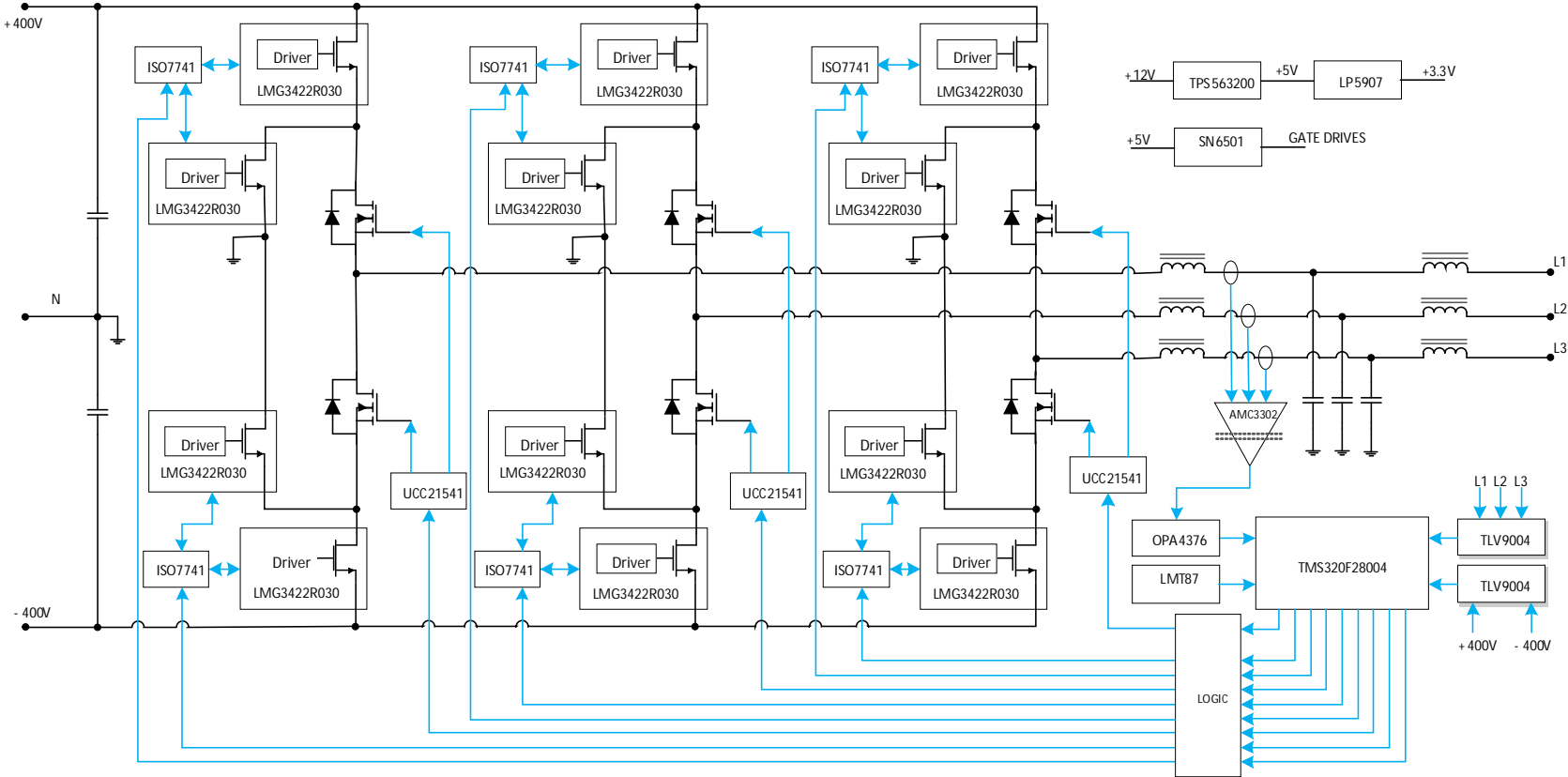
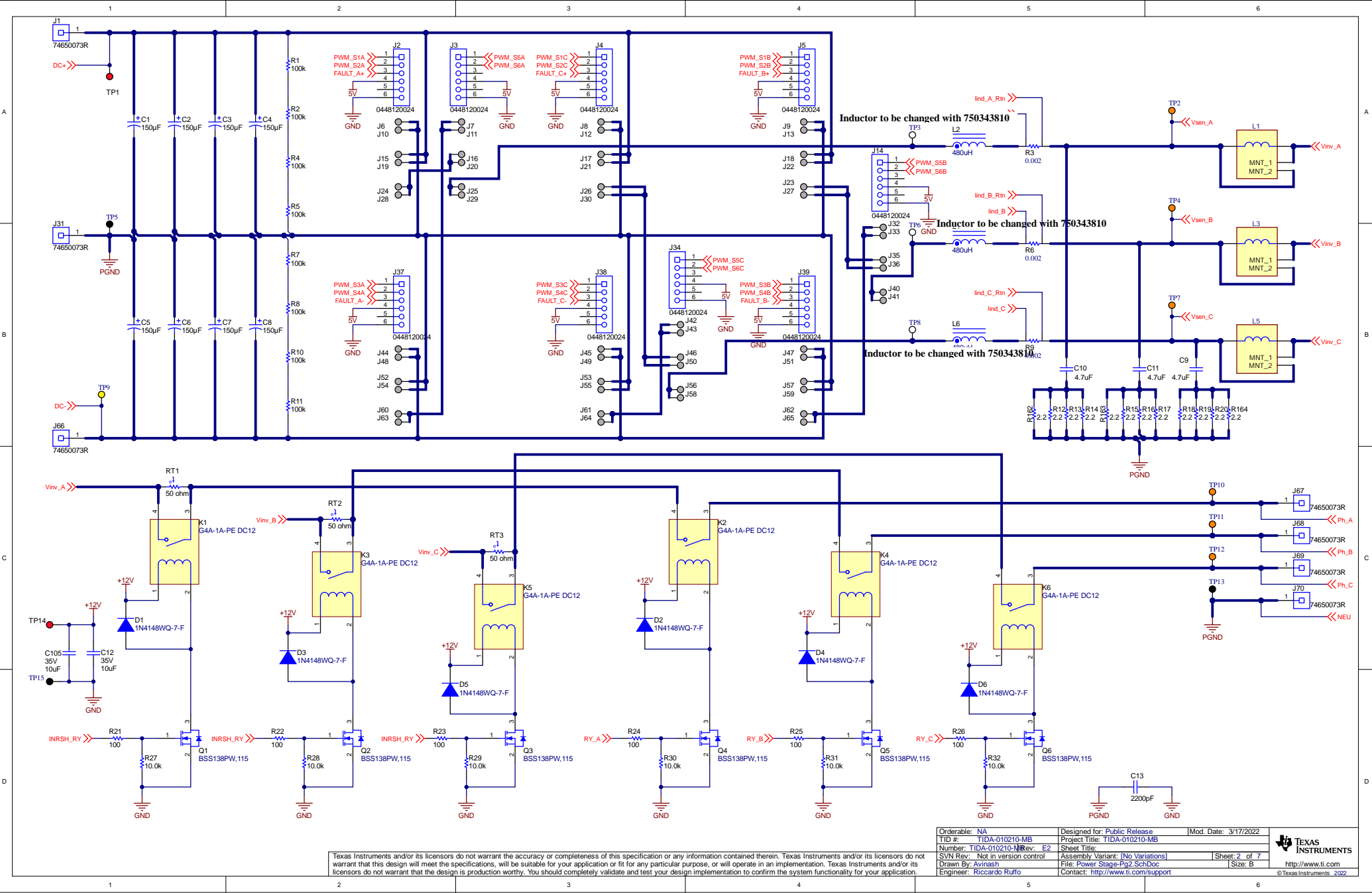


Revision History				
Rev	ECN #	Approved Date	Approved by	Notes
N/A	N/A	N/A	N/A	N/A



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Orderable: NA	Designed for: Public Release	Mod. Date: 3/17/2022
TID #: TIDA-010210-MB	Project Title: TIDA-010210-MB	
Number: TIDA-010210-MB Rev: E2	Sheet Title:	
SVN Rev: Not in version control	Assembly Variant: [No Variations]	Sheet: 1 of 7
Drawn By: Avinash	File: Block Diagram-Pg1.SchDoc	Size: B
Engineer: Riccardo Ruffo	Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>	

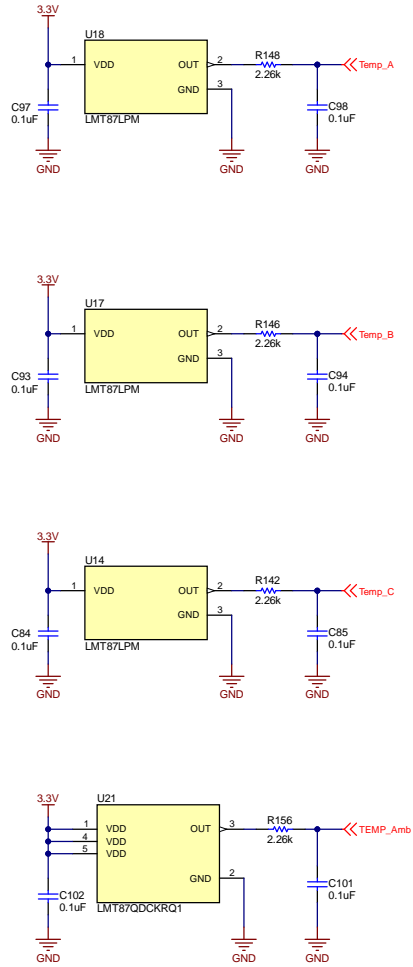


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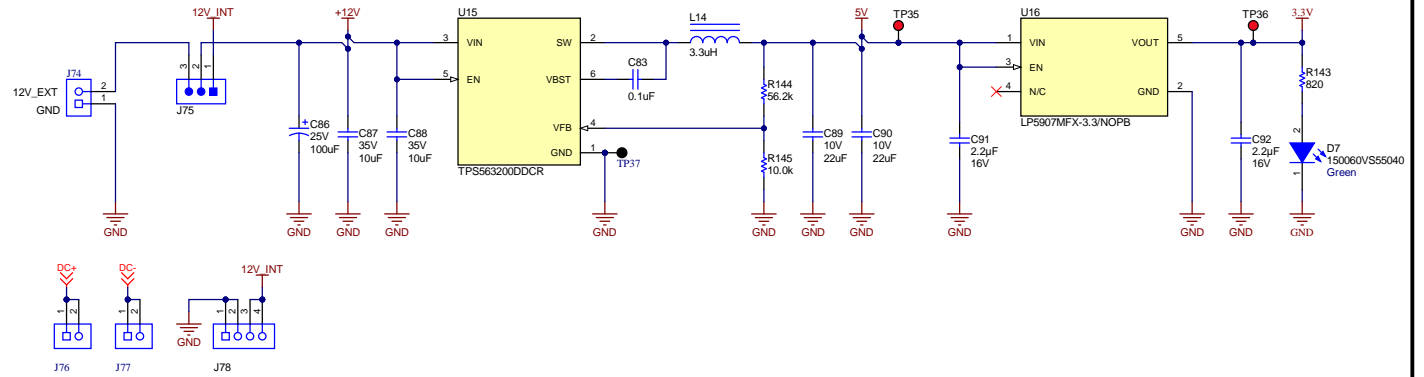
Orderable: NA	Designed for: Public Release	Mod. Date: 3/17/2022
TID #: TIDA-010210-MB	Project Title: TIDA-010210-MB	
Number: TIDA-010210-MB Rev: E2	Sheet Title:	
SVN Rev: Not in version control	Assembly Variant: [No Variations]	Sheet: 2 of 7
Drawn By: Avinash	File: Power Stage-Pg2.SchDoc	Size: B
Engineer: Riccardo Ruffo	Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>	

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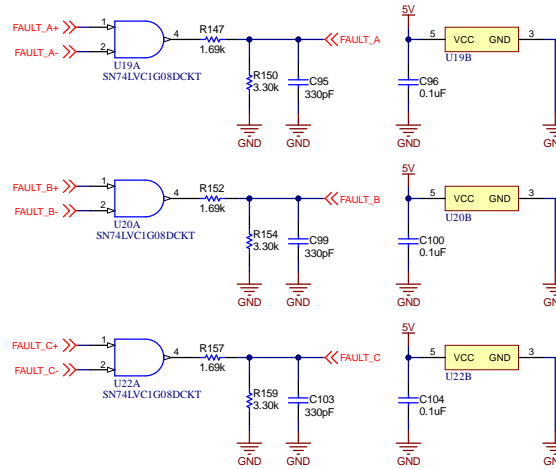
## TEMP SENSING



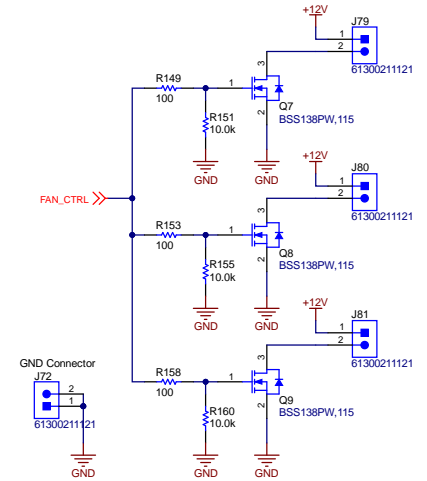
## NON-ISOLATED POWER SUPPLY



## FAULT DETECTION



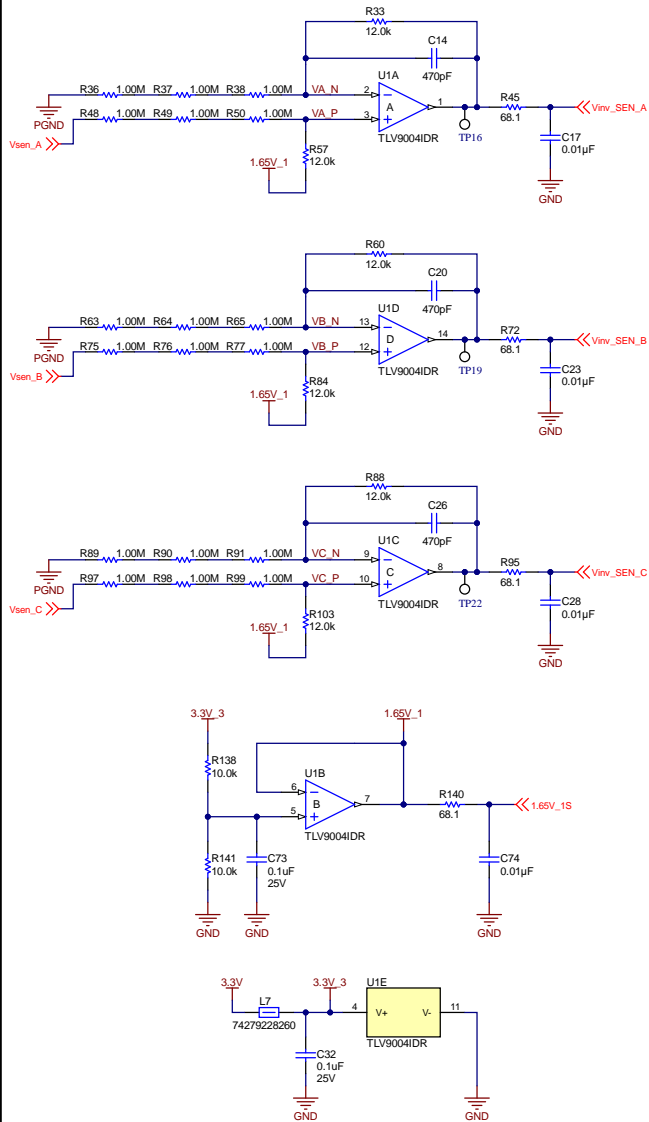
## FAN CONTROL



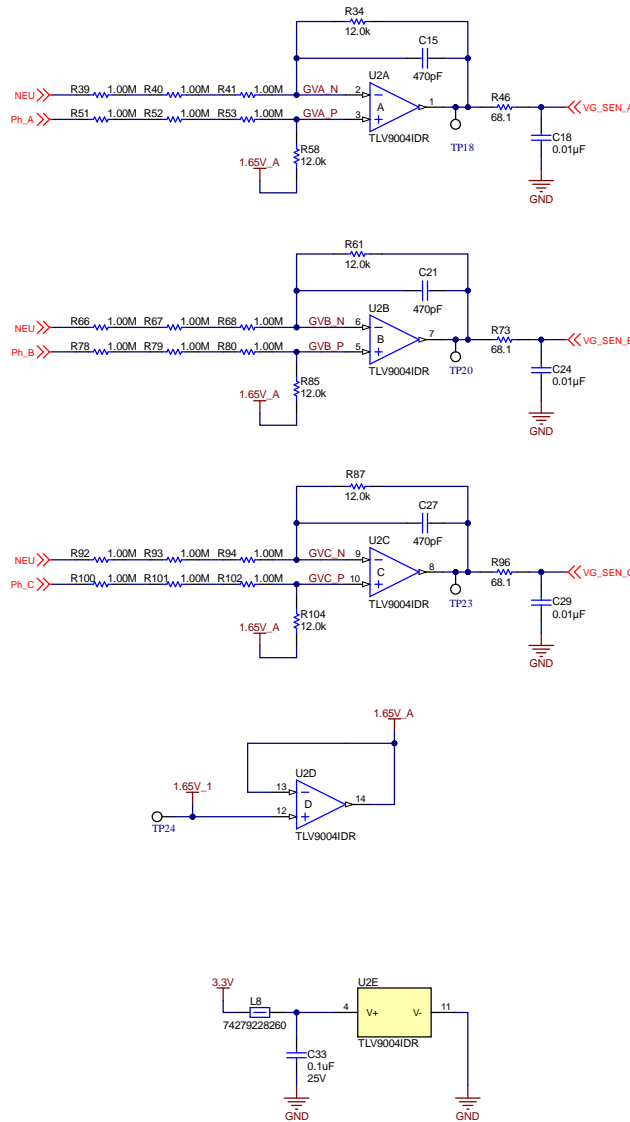
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Orderable: NA	Designed for: Public Release	Mod. Date: 7/19/2021
TID #: TIDA-010210-MB	Project Title: TIDA-010210-MB	
Number: TIDA-010210-MB Rev: E2	Sheet Title:	
SVN Rev: Not in version control	Assembly Variant: [No Variations]	Sheet: 3 of 7
Drawn By: Avinash	File: Power Supply and Protection-Pg3.SchDoc	Size: B
Engineer: Riccardo Ruffo	Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>	

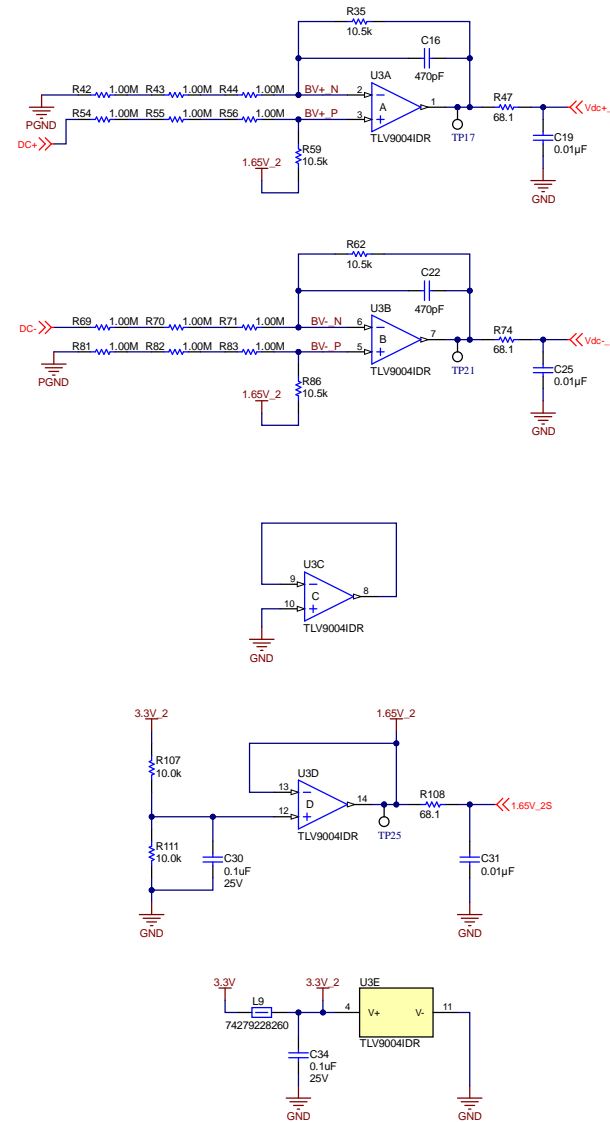
# INVERTER OUTPUT VOLTAGE SENSING



# GRID VOLTAGE SENSING

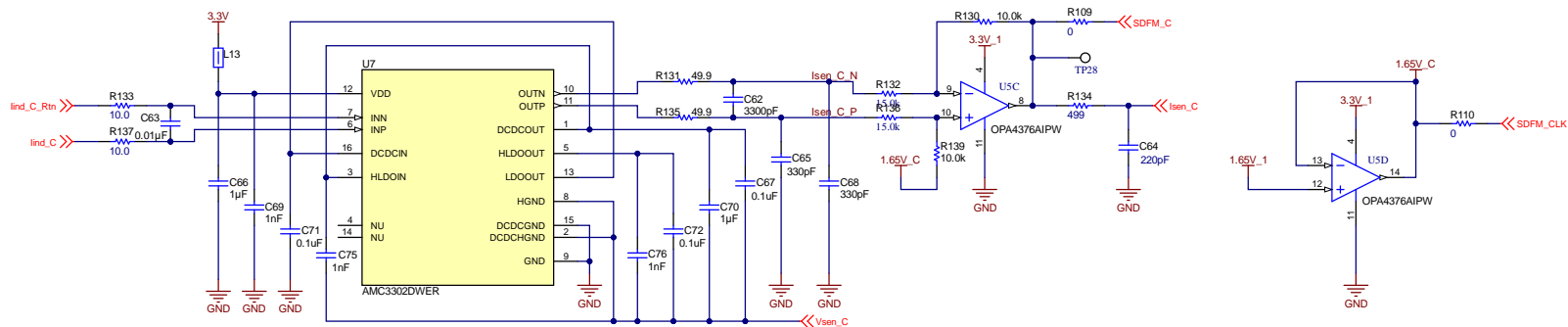
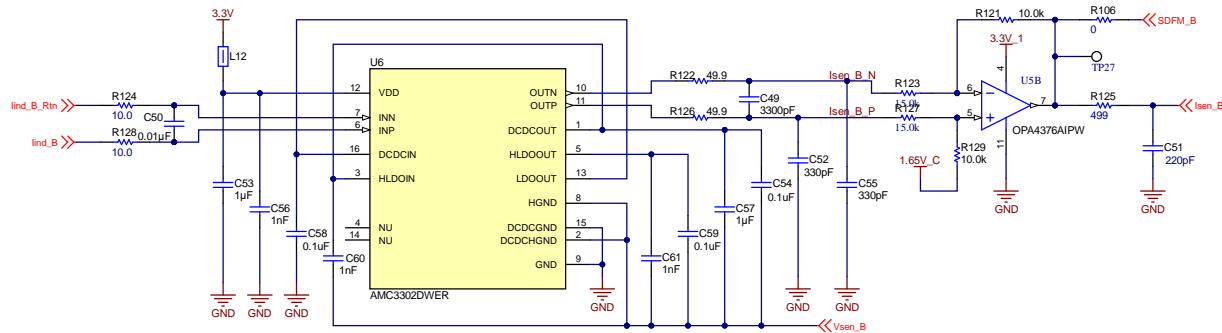
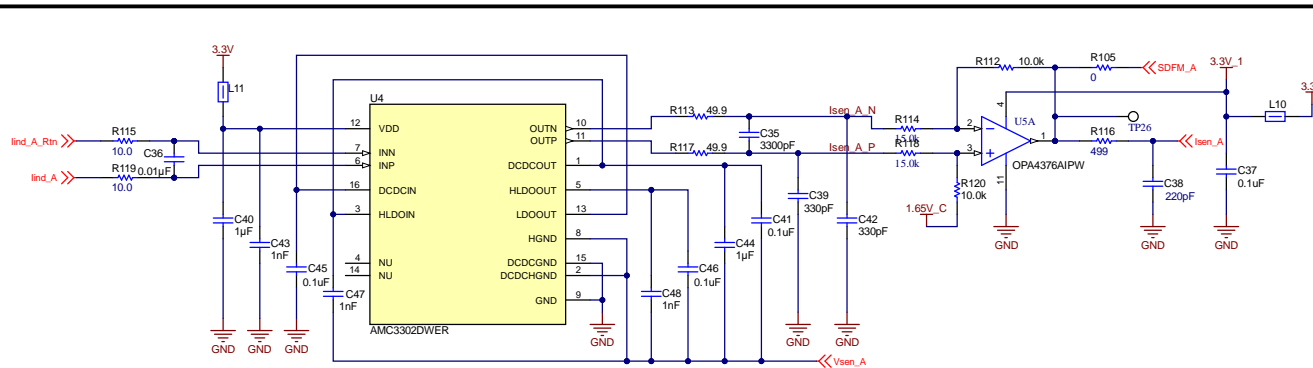


# BUS VOLTAGE SENSING



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Orderable: NA	Designed for: Public Release	Mod. Date: 3/17/2022
TID #: TIDA-010210-MB	Project Title: TIDA-010210-MB	
Number: TIDA-010210-MB Rev: E2	Sheet Title:	
SVN Rev: Not in version control	Assembly Variant: [No Variations]	Sheet: 4 of 7
Drawn By: Avinash	File: Voltage Sensing-Pg4.SchDoc	Size: B
Engineer: Riccardo Ruffo	Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>	



Orderable: NA	Designed for: Public Release	Mod. Date: 3/17/2022
TID #: TIDA-010210-MB	Project Title: TIDA-010210-MB	
Number: TIDA-010210-MB Rev. E2	Sheet Title: Inductor Current Sensing Interface Circuit	
SVN Rev: Not in version control	Assembly Variant: [No Variations]	Sheet: 5 of 7
Drawn By: Avinash	File: Current Sensing-Pg5.SchDoc	Size: B
Engineer: Riccardo Ruffo	Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>	

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PCB Number: TIDA-010210-MB  
PCB Rev: E2

PCB  
LOGO  
Pb-Free Symbol

PCB  
LOGO  
FCC disclaimer

PCB  
LOGO  
WEEE logo



LOGO6



LOGO7



LOGO8



LOGO9



LOGO10



LOGO11



LOGO12

PCB  
LOGO  
Logo4

PCB  
LOGO  
Logo5



LOGO13



LOGO16



LOGO17

LBL1

PCB Label

THT-14-423-10

ZZ1

Label Assembly Note

This Assembly Note is for PCB labels only

Variant/Label Table	
Variant	Label Text
001	ChangeMe!
002	ChangeMe!

ZZ2

Assembly Note

These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3

Assembly Note

These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4

Assembly Note

These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

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Orderable: NA	Designed for: Public Release	Mod. Date: 3/17/2022
TID #: TIDA-010210-MB	Project Title: TIDA-010210-MB	
Number: TIDA-010210-MB Rev: E2	Sheet Title:	
SVN Rev: Not in version control	Assembly Variant: [No Variations]	Sheet: 7 of 7
Drawn By: Avinash	File: Hardware-Pg7_SchDoc	Size: B
Engineer: Riccardo Ruffo	Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>	

