Date: 15-Nov-2022

# Mapping to DIM100 pinout with Adapter card

## F28P65X

HSEC Pinout: 2.17

| Exp Kit lbl | DIM pin | | | DIM pin | Exp Kit lbl |
|---|---|---|---|---|---|
| | 1 | | | 51 | |
| | 2 | | | 52 | |
| | 3 | 12V | 12V | 53 | |
| | 4 | | | 54 | |
| | 5 | | | 55 | |
| | 6 | | | 56 | |
| B0 | 7 | B0, VDAC | A0, DACA_OUT, CMP1P_I1 | 57 | A0 |
| | 8 | | | 58 | |
| B1 | 9 | B1, DACC_OUT | A1, CMP1N_I1 | 59 | A1 |
| | 10 | | | 60 | |
| B2 | 11 | B2, CMP3P_I0 | A2, CMP1P_I0 | 61 | A2 |
| | 12 | | | 62 | |
| B3 | 13 | B3, CMP3N_I0 | A3, CMP1N_I0 | 63 | A3 |
| | 14 | | | 64 | |
| B4 | 15 | B4, GPIO215, CMP5P/N_I1 | A4, CMP2P_I0 | 65 | A4 |
| | 16 | - | VREFHIABC * | 66 | |
| B5 | 17 | B5, GPIO216, CMP5P_I2 | A5, CMP2N_I0 | 67 | A5 |
| 58 | 18 | GPIO105 | GPIO106 | 68 | 59 |
| B6 | 19 | C6, GPIO203, CMP10P_I1 | A14/B14/C14, CMP4P_I0 | 69 | A6 |
| 60 | 20 | GPIO125 | GPIO45 | 70 | 61 |
| B7 | 21 | C4, GPIO205, CMP10N_I1 | A15/B15/C15, CMP4N_I0 | 71 | A7 |
| 62 | 22 | GPIO97 | GPIO99 | 72 | 63 |
| 00 | 23 | GPIO0 | GPIO1 | 73 | 01 |
| 02 | 24 | GPIO2 | GPIO3 | 74 | 03 |
| 04 | 25 | GPIO4 | GPIO5 | 75 | 05 |
| 06 | 26 | GPIO6 | GPIO7 | 76 | 07 |
| | 27 | | 5V | 77 | |
| 08 | 28 | GPIO8 | GPIO9 | 78 | 09 |
| 10 | 29 | GPIO10 | GPIO11 | 79 | 11 |
| 48 | 30 | GPIO12 | GPIO13 | 80 | 49 |
| 84 | 31 | GPIO14 | GPIO15 | 81 | 85 |
| 86 | 32 | GPIO48 | 5V | 82 | |
| 12 | 33 | GPIO40 | GPIO41 | 83 | 13 |
| 15 | 34 | GPIO98 | GPIO96 | 84 | 14 |
| 24 | 35 | GPIO24 | GPIO25 | 85 | 25 |
| 26 | 36 | GPIO26 | GPIO27 | 86 | 27 |
| | 37 | | 5V | 87 | |
| 16 | 38 | GPIO16 | GPIO17 | 88 | 17 |
| 18 | 39 | GPIO18 | GPIO35 | 89 | 19 |
| 20 | 40 | GPIO20 | GPIO21 | 90 | 21 |
| 22 | 41 | GPIO22 | GPIO23 | 91 | 23 |
| 87 | 42 | GPIO39 | 5V | 92 | |
| 28 | 43 | GPIO28## | GPIO29## | 93 | 29 |
| 30 | 44 | GPIO30 | GPIO31** | 94 | 31 |
| 32 | 45 | GPIO32 | GPIO33 | 95 | 33 |
| 34 | 46 | GPIO34** | 5V | 96 | |
| | 47 | | GPIO222/TDI | 97 | |
| | 48 | TCK | GPIO223/TDO | 98 | |
| | 49 | TMS | - | 99 | |
| | 50 | - | - | 100 | |

*Switches may be altered to connect this HSEC pin to the MCU's VREFHI inputs

** Connected to LED

#Boot mode pin

## Connect through isolation to embedded debug probe