

TMDSCSKCC

TABLE OF CONTENTS

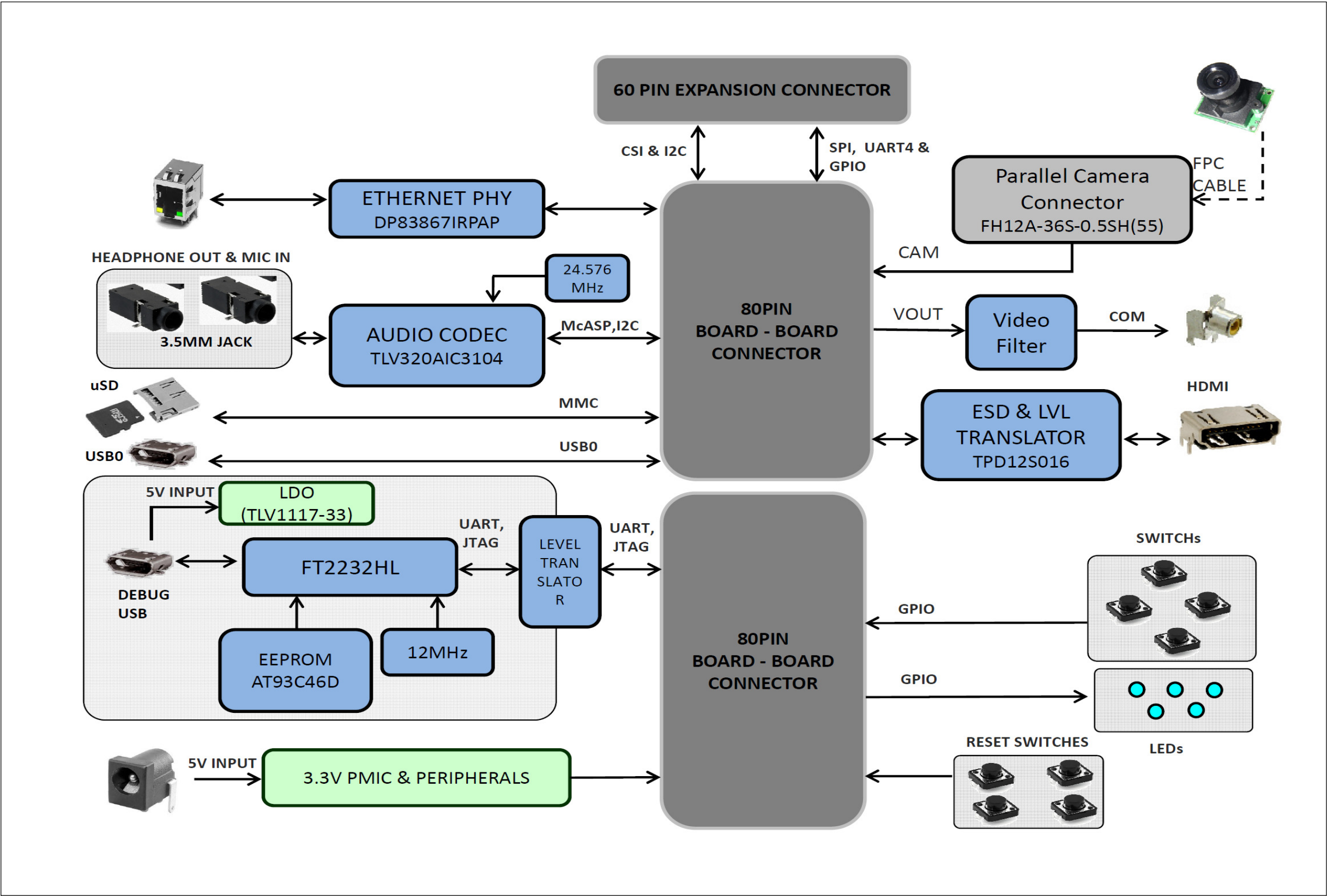
SL.No.	CONTENTS	PAGE NO
1	TABLE OF CONTENTS	1
2	REVISION HISTORY	2
3	BLOCK DIAGRAM	3
4	I2C TREE DIAGRAM	4
5	POWER FLOW DIAGRAM	5
6	POWER CALCULATION	6
7	BOARD TO BOARD & EXPANSION CONNECTOR	7
8	CAMERA & SWITCHES	8
9	AUDIO CODEC & LEDS	9
10	COMPOSITE VIDEO & HDMI	10
11	ETHERNET PHY	11
12	FT2232 JTAG / DEBUG	12
13	MICROSD CARD & USB	13
14	POWER SUPPLY	14
15	OVERVOLTAGE PROTECTION	15
16	HARDWARE SCHEMATICS	16

REV	B
VERSION	4.0

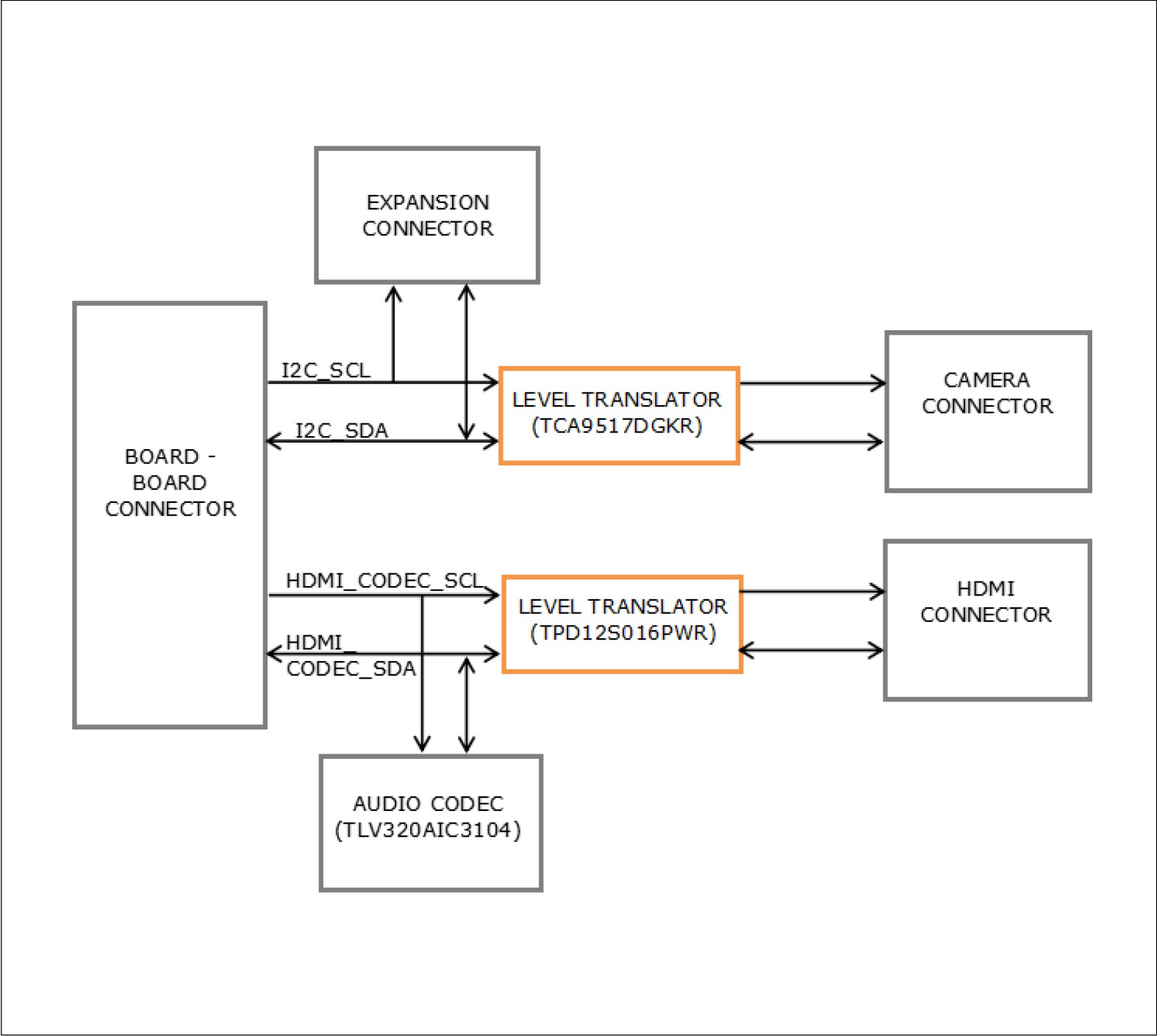
REVISION HISTORY

VER #	DATE	DESCRIPTION OF CHANGES	DESIGNED BY	REVEIWED BY	APPROVED BY
0.1	21/09/2016	UPDATED PRDN_REV A SCHEMATIC FROM SOURCE REV A SCHEMATIC AS PER CHANGELIST DOCUMENT	MISTRAL DESIGN TEAM	AJIT M B	AJIT M B
1.0	21/09/2016	Reviewd & Baselined	MISTRAL DESIGN TEAM	AJIT M B	AJIT M B
1.1	06/01/2017	SD card Part# removed from hardware schematics page	MISTRAL DESIGN TEAM	AJIT M B	AJIT M B
2.0	06/01/2017	Baselined	MISTRAL DESIGN TEAM	AJIT M B	AJIT M B
2.1	09/01/2017	Power cord Part# added in hardware schematics page	MISTRAL DESIGN TEAM	AJIT M B	AJIT M B
3.0	09/01/2017	Baselined	MISTRAL DESIGN TEAM	AJIT M B	AJIT M B
3.1	18/01/2017	Schematics renamed from PRDN_REVA to REVB	MISTRAL DESIGN TEAM	AJIT M B	AJIT M B
4.0	18/01/2017	Baselined	MISTRAL DESIGN TEAM	AJIT M B	AJIT M B

BLOCK DIAGRAM



I2C TREE DIAGRAM

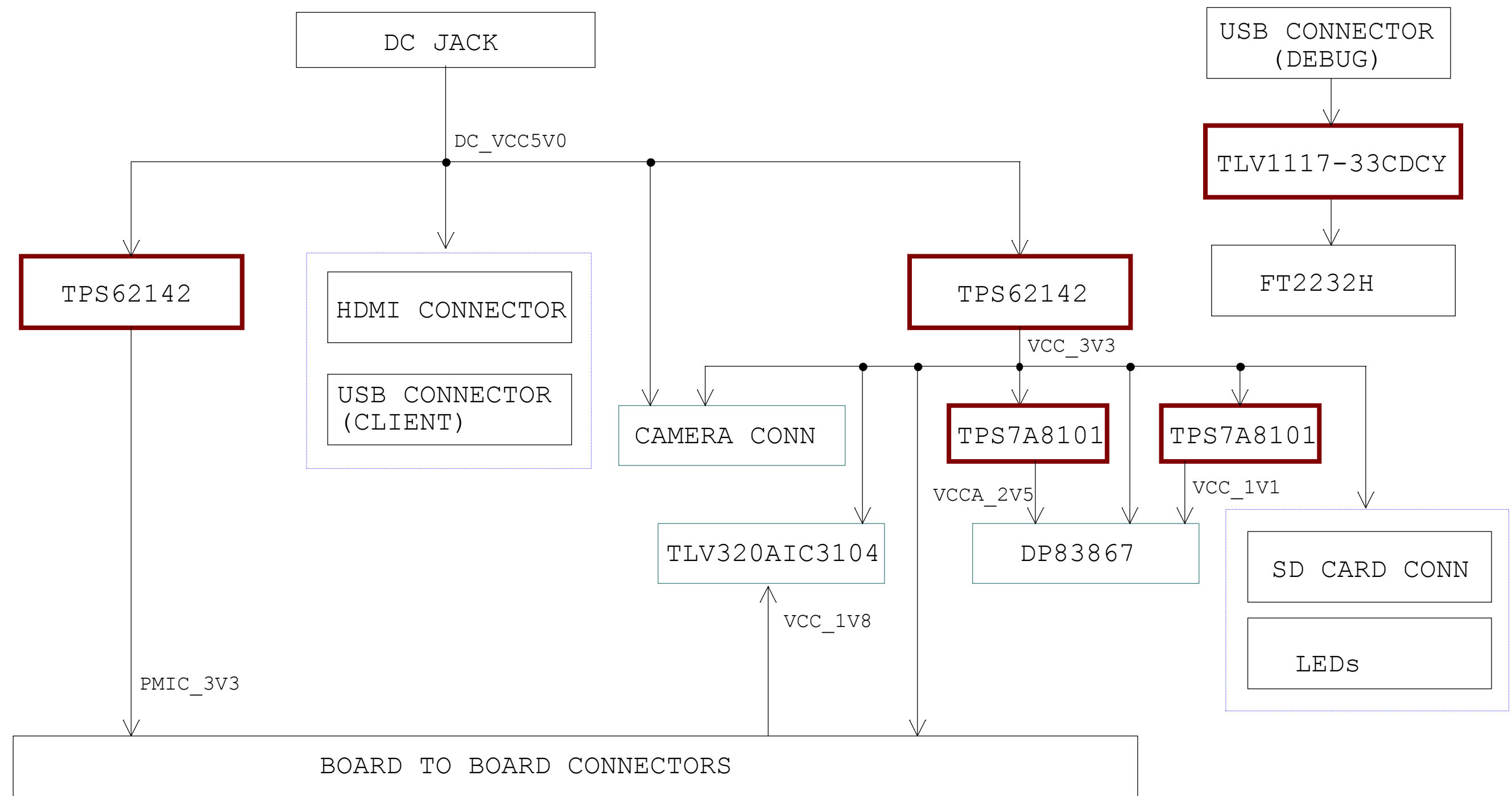


I2C ADDRESS MAPPING

	INTERFACES	ADDRESS (7-BIT)	ADDRESS (8-BIT)
I2C	AUDIO CODEC (TLV320AIC3104)	0x18	0x30(W),0x31(R)
	CAMERA	0x10 (CAMERA SENSOR)	0x20(W),0x21(R) (CAMERA SENSOR)
		0x2D (LVDS MODULE)	0x5A(W),0x5B(R) (LVDS MODULE)

NOTE: PHY ADDRESS : 0X19h

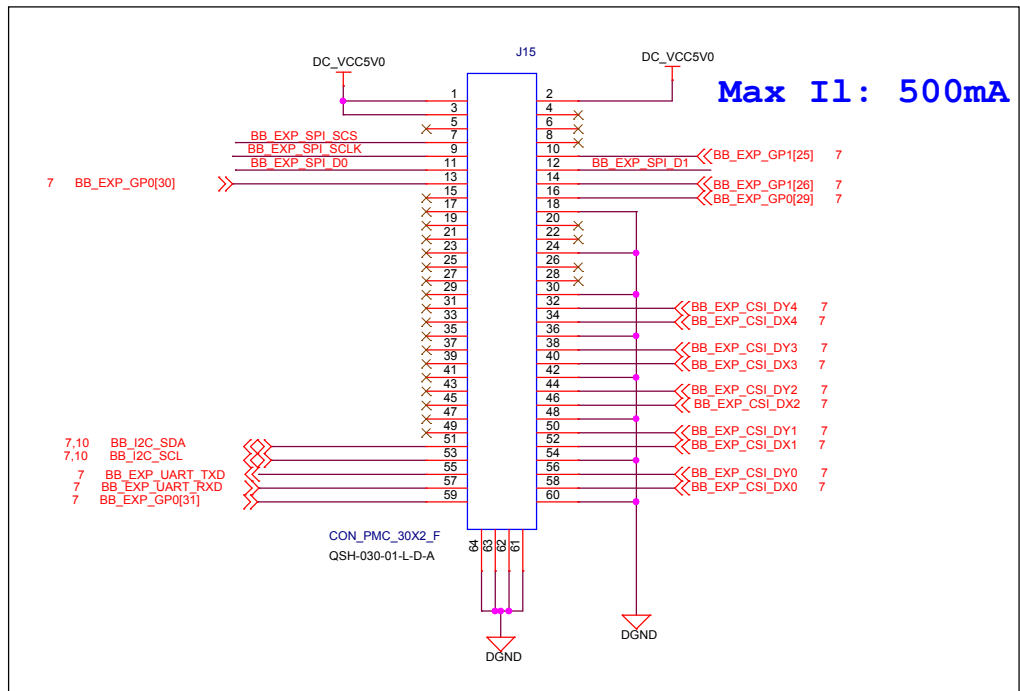
POWER FLOW DIAGRAM



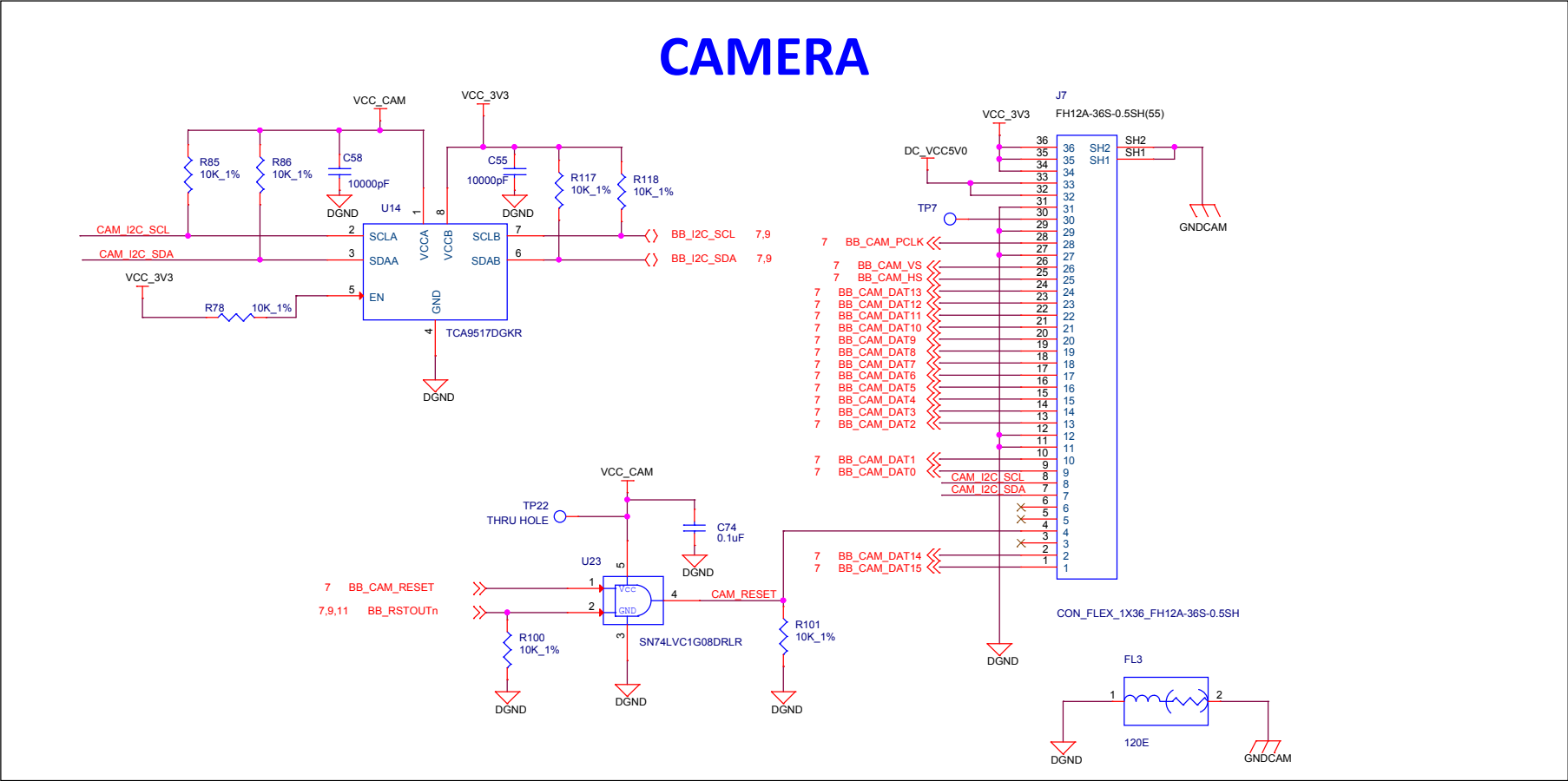
NOTE:

- REGULATORS
- DEVICES
- Multiple powered device

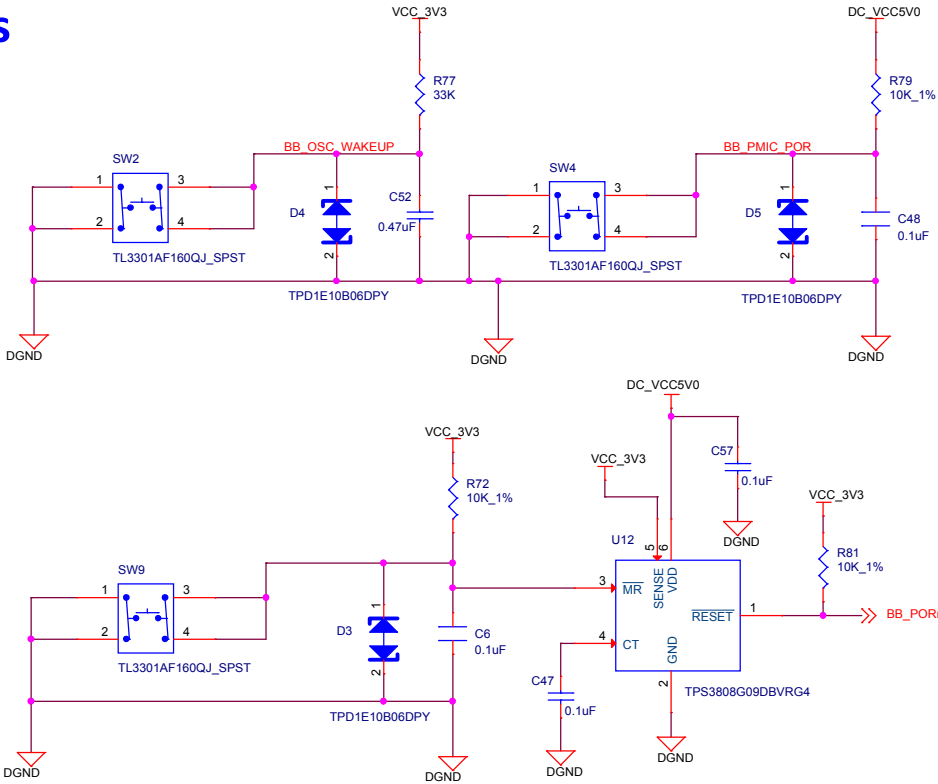
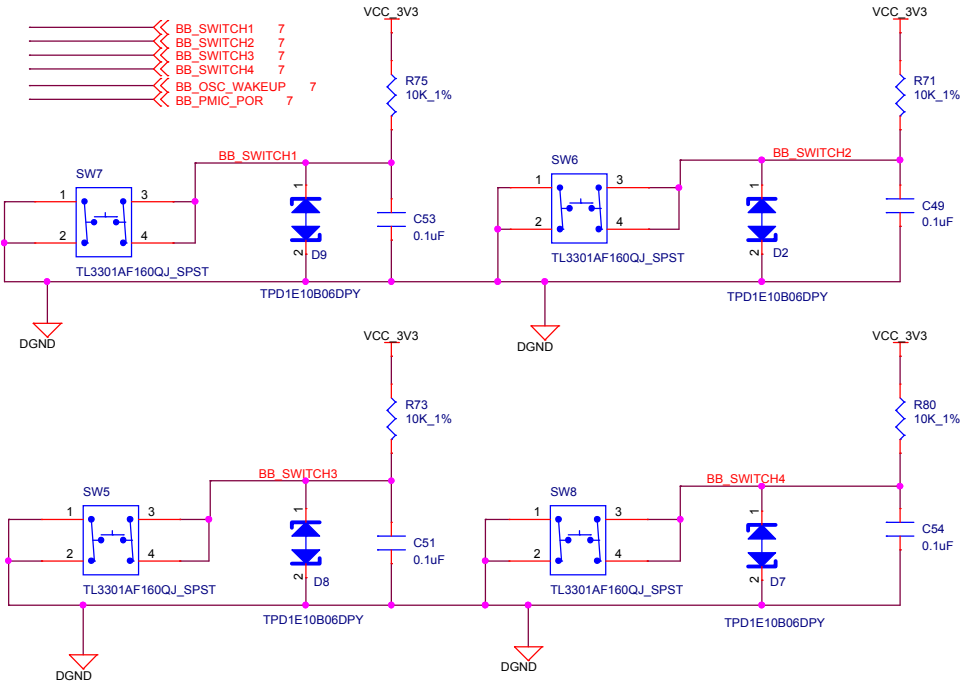
EXPANSION CONNECTOR

[illegible]

CAMERA



SWITCHs



AUDIO CODEC

AUDIO CODEC

I2C ADDRESS : 0X18

ISOLATE GROUNDS
AND CONNECT AT
SINGLE LOCATION
IN THE GROUND PLANE

7 BB_HDMI_CODEC_SDA
7 BB_HDMI_CODEC_SCL
7 BB_AIC_MCLK
7 BB_AIC_BCLK
7 BB_AIC_WCLK
7 BB_AIC_DIN
7 BB_AIC_DOUT

TMDSCSKCC



Title	AUDIO CODEC & LEDs
-------	--------------------

Size	Document Number
------	-----------------

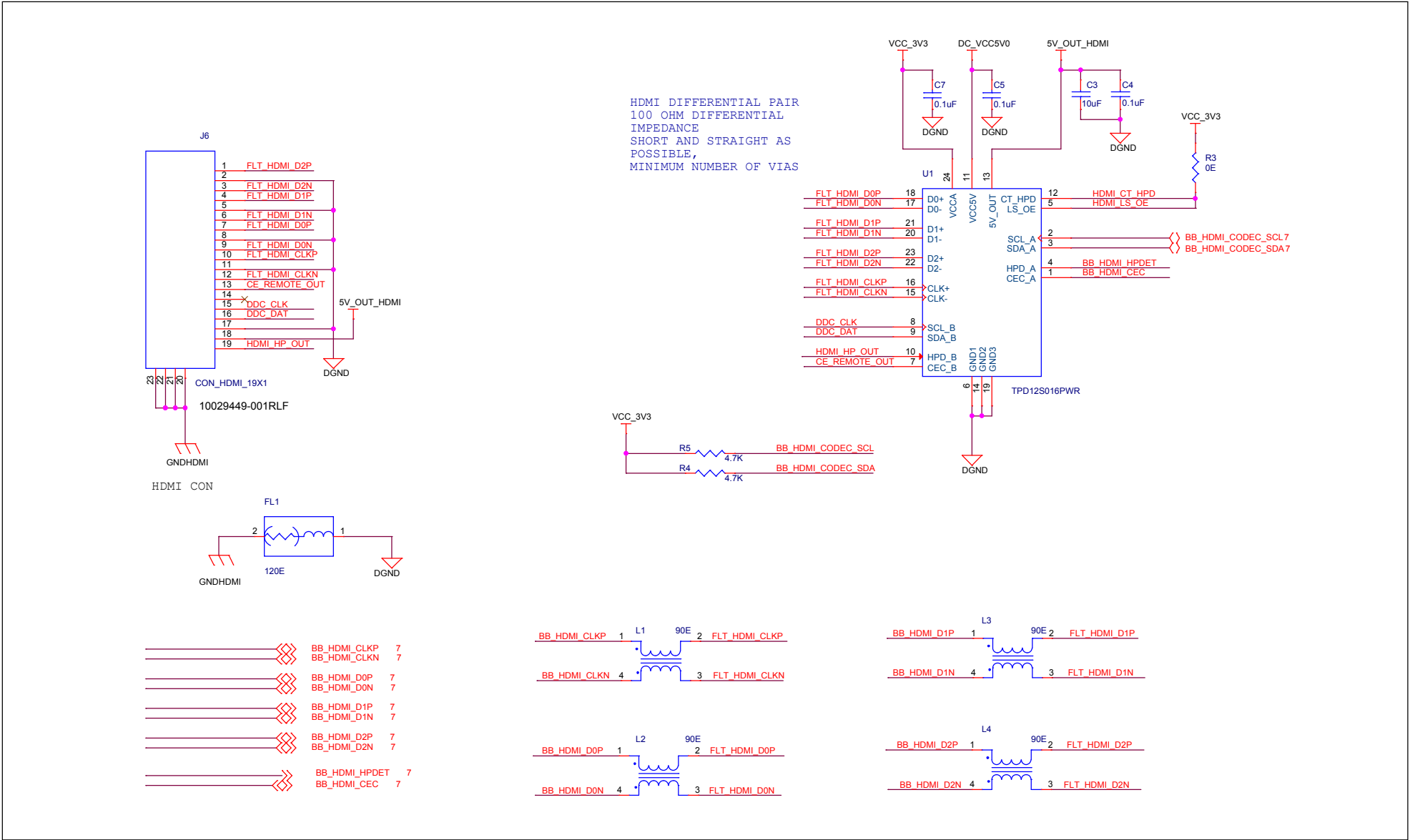
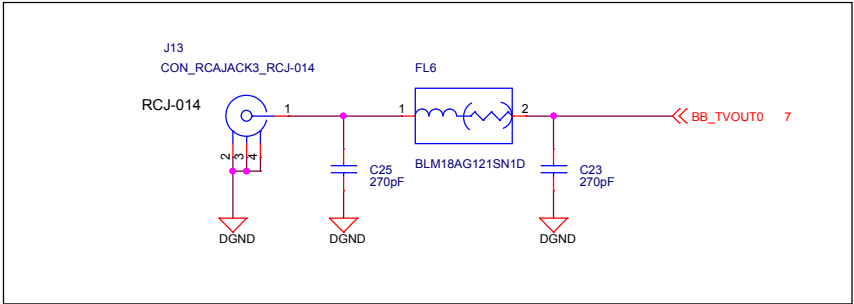
MS_TI_CSK_TMDSCSKCC_SCH_REVB

c		

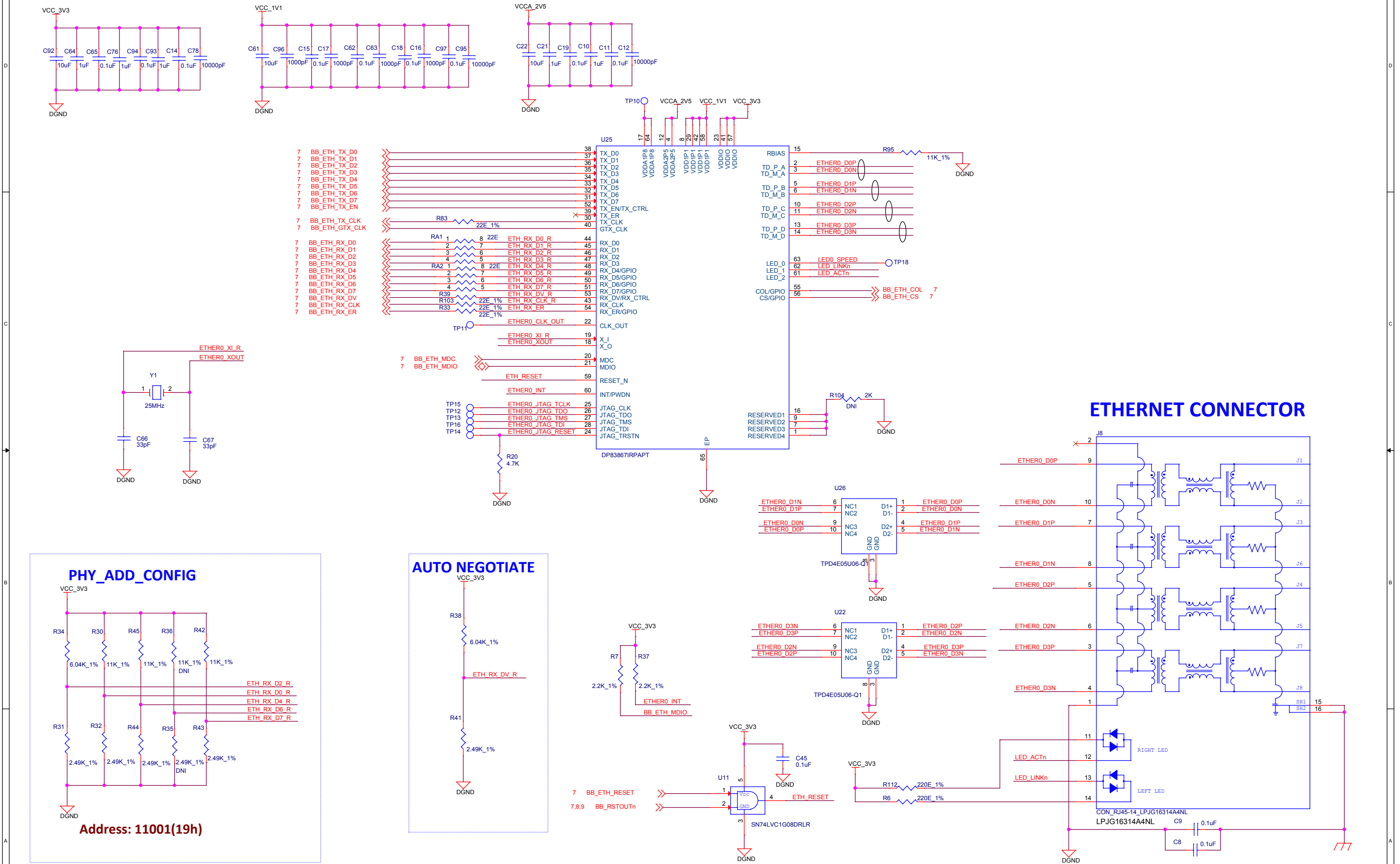
Rev

REV
P

COMPOSITE VIDEO & HDMI



ETHERNET PHY

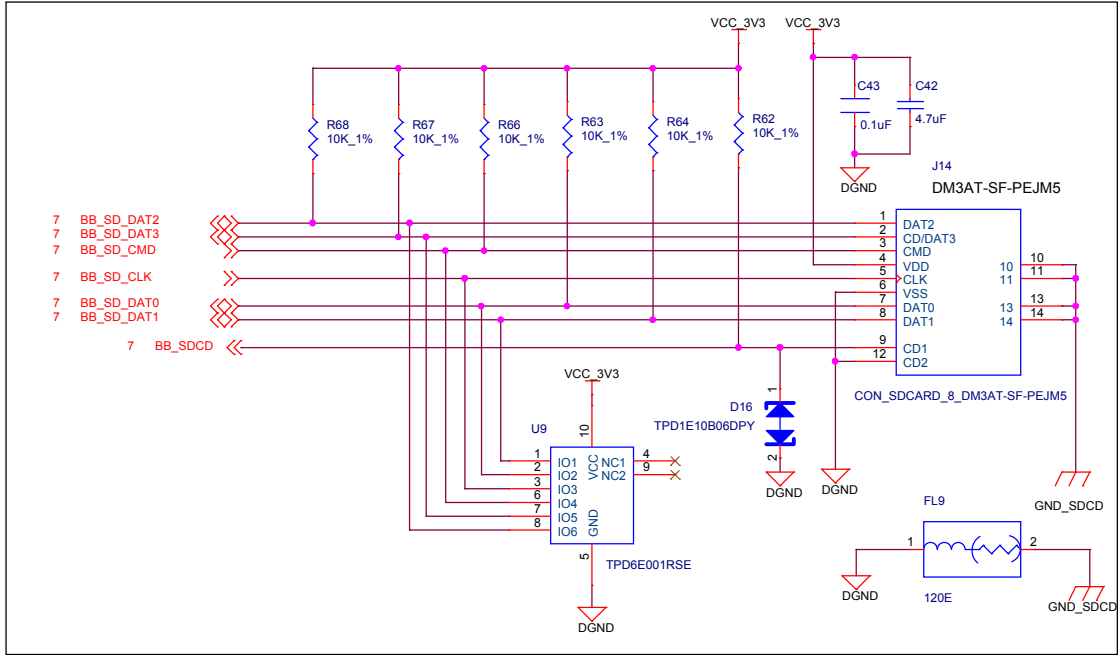
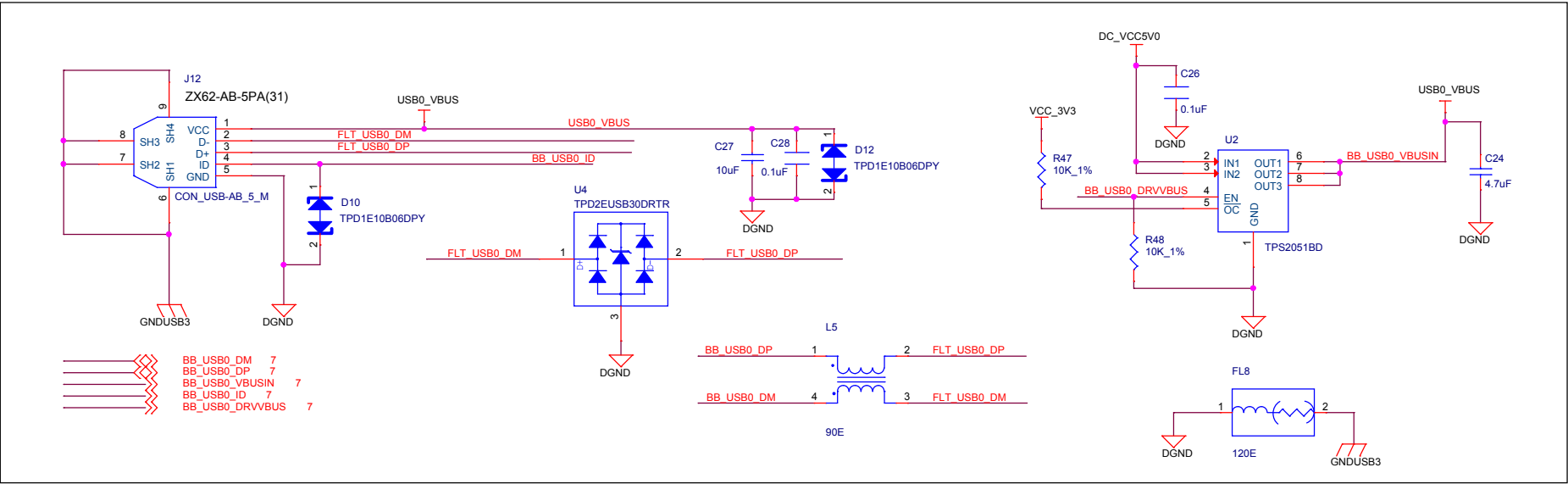


ETHERNET CONNECTOR

FT2232 JTAG/DEBUG



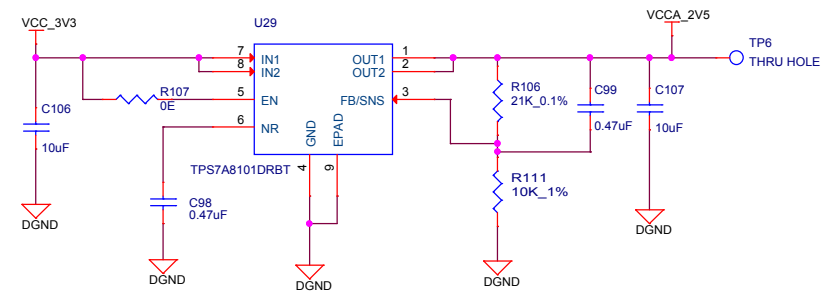
MicroSD Card & USB



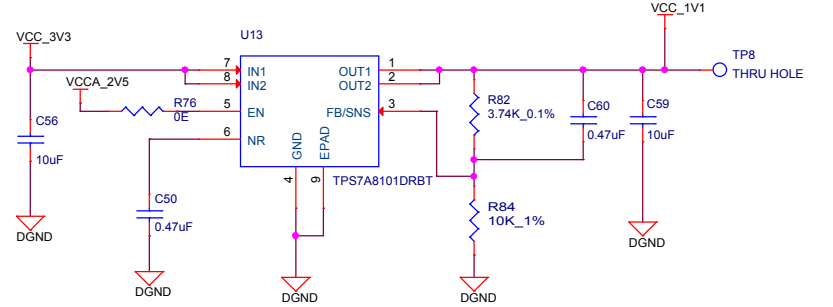
POWER SUPPLY

ETHERNET POWER

3.3 V TO 2.5 V LDO

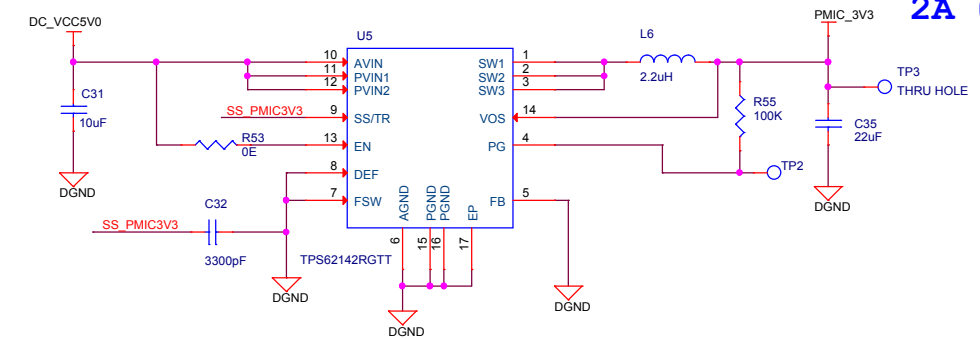


3.3 V TO 1.1 V LDO



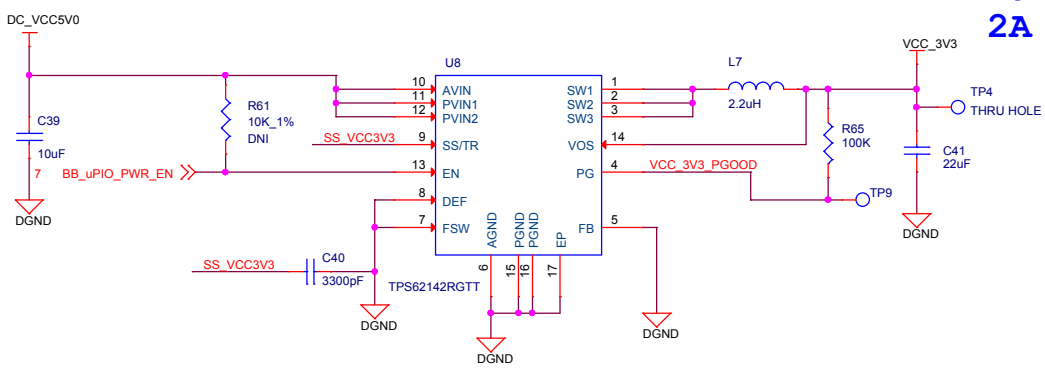
3.3V DC FOR PMIC INPUT

Max I1:
2A @ 3.3V

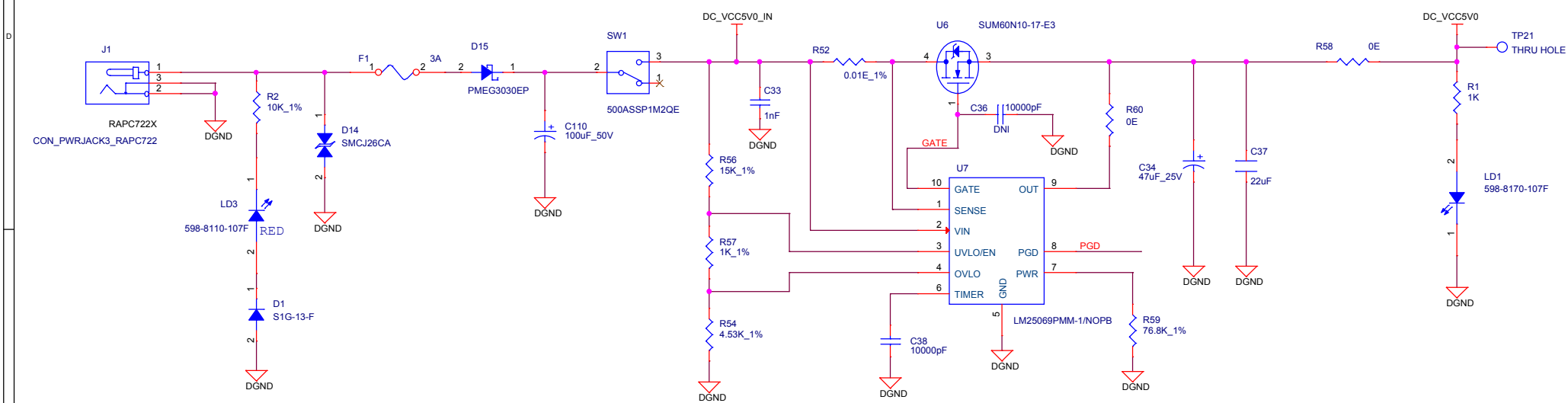


3.3V DC FOR PERIPHERALS

Max I1:
2A @ 3.3V

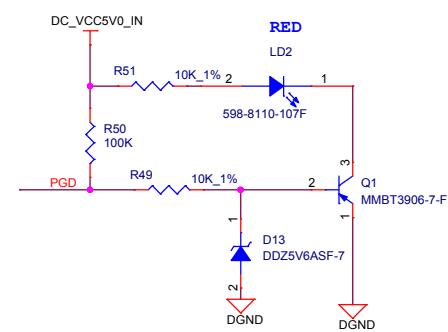


OVERVOLTAGE PROTECTION



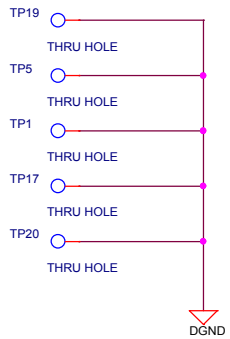
Condition	LED Status (LD3)
Reverse Voltage	ON

Fault Indication



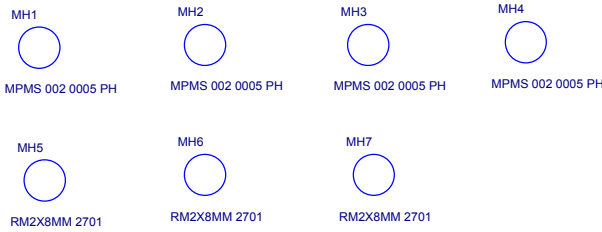
Condition	LED Status (LD2)
DC_VCC5V0_IN between 4.5 to 5.25V	OFF
DC_VCC5V0_IN above 5.25V or below 4.5V	ON

Note:-
When fault is indicated ,set to proper voltage and power cycle the board.

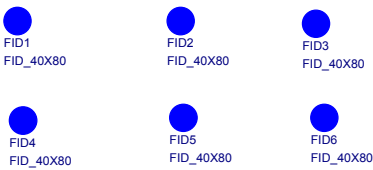


HARDWARE SCHEMATICS

MOUNTING HOLES



FIDUCIALS



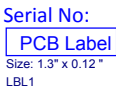
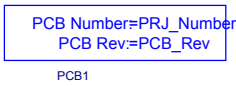
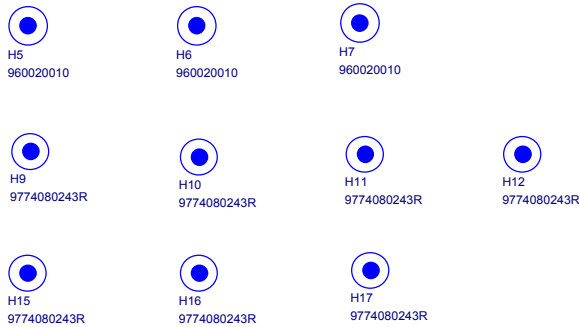
CAMERA MODULE & FPC CABLE



LOGOs & LABELs



STANDOFFs



KIT CONTENT

USB CABLE



ETHERNET CABLE



MicroSD CARD READER



POWER CORD



Label Assembly Note

ZZ1
The boards and components must be baked before assembly

Label Assembly Note

ZZ2
Provide serial numbers to the assembled boards for identification

Label Assembly Note

ZZ3
Please carry out the cold points check verification and provide the report for each assembled board

Label Assembly Note

ZZ4
The assembled board are wrapped in ESD Covers(individual) and packed securely before shipment.

Label Assembly Note

ZZ5
All MSL components should be baked as per JEDEC standard

Label Assembly Note

ZZ6
PCB should be baked at 120 degree for 8 hours

Label Assembly Note

ZZ7
These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

Label Assembly Note

ZZ8
These assemblies are ESD sensitive, ESD precautions shall be observed.

Label Assembly Note

ZZ9
These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

Project :

TMDSCSKCC

Designed for TI by Mistral Solutions Pvt Ltd



Title

HARDWARE SCHEMATICS

Size

C

Document Number

MS_TI_CSK_TMDSCSKCC_SCH_REVB

Rev

B

Date:

Wednesday, January 18, 2017

Sheet

16

of

16