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D

Revision History

Rev	ECN #	Approved Date	Approved by	Notes
N/A	N/A	N/A	N/A	N/A

Place Block Diagram here (if appropriate) or delete this text box.  
If using a block diagram from another tool, save the picture as a .bmp file.  
Then, use menu Place|Drawing Tools|Graphic to insert the .png/.svg/.bmp file on the schematic.

Orderable: <a href="#">DRV8145H-Q1EVM</a>	Designed for: <a href="#">Public Release</a>	Mod. Date: <a href="#">9/27/2022</a>	 <div>TEXAS INSTRUMENTS</div> <div><a href="http://www.ti.com">http://www.ti.com</a></div> <div>© Texas Instruments 2020</div>
TID #: <a href="#">N/A</a>	Project Title: <a href="#">DRV8145S/H-Q1EVM (VQFN)</a>		
Number: <a href="#">MD044</a>	Rev: <a href="#">A</a>	Sheet Title:	
SVN Rev:	Assembly Variant: <a href="#">002</a>	Sheet: <a href="#">1</a> of <a href="#">3</a>	
Drawn By:	File: <a href="#">CoverSheet.SchDoc</a>	Size: B	
Engineer: <a href="#">Michael Erdahl</a>	Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>		

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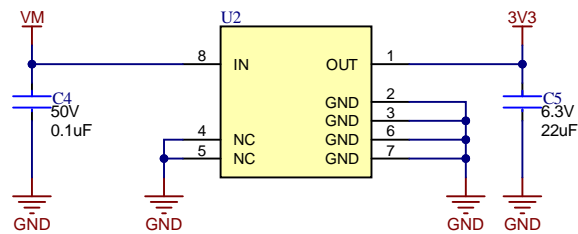
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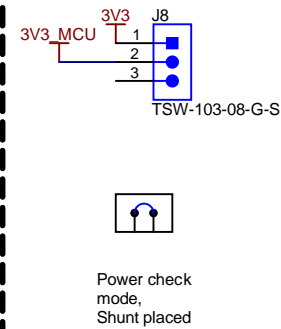
6

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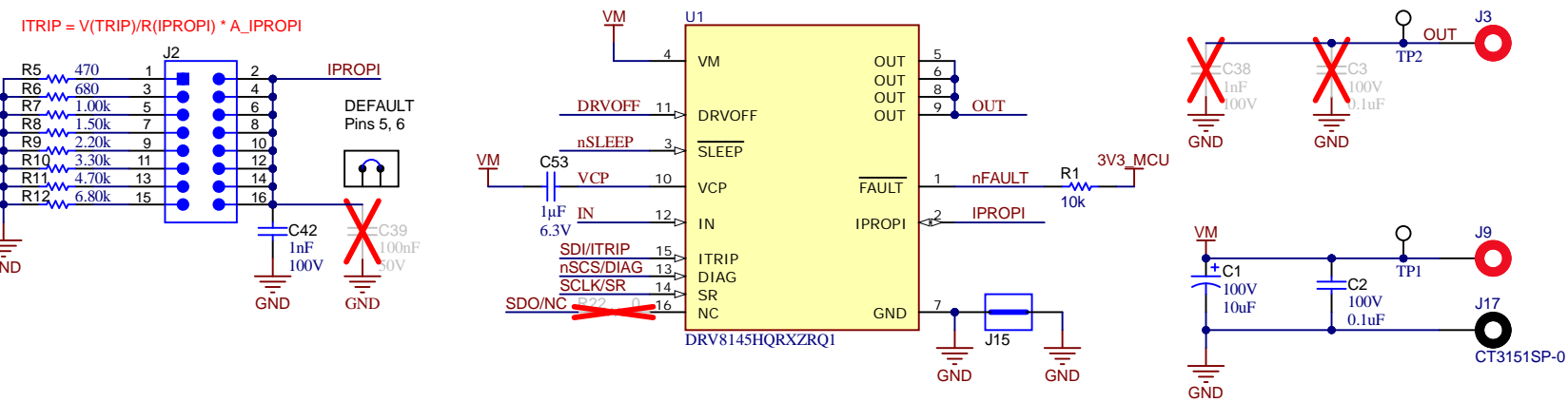
### 3.3V LDO



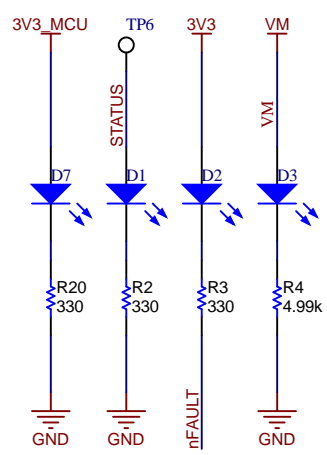
### Power Check



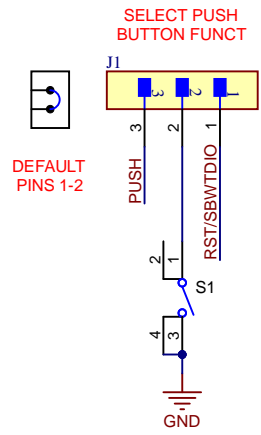
### DRV8144S/H-Q1 VQFN



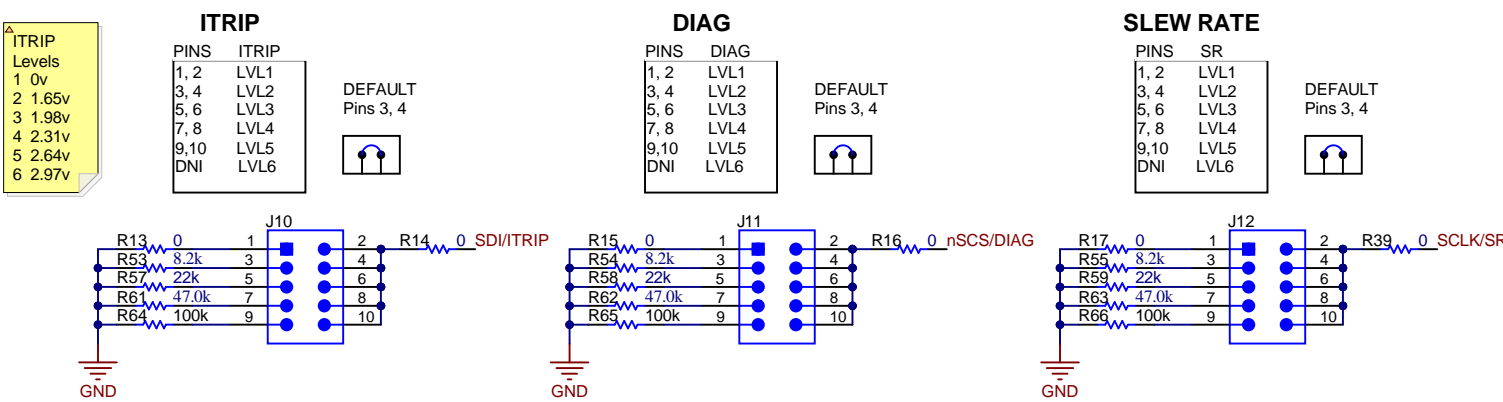
### LEDS



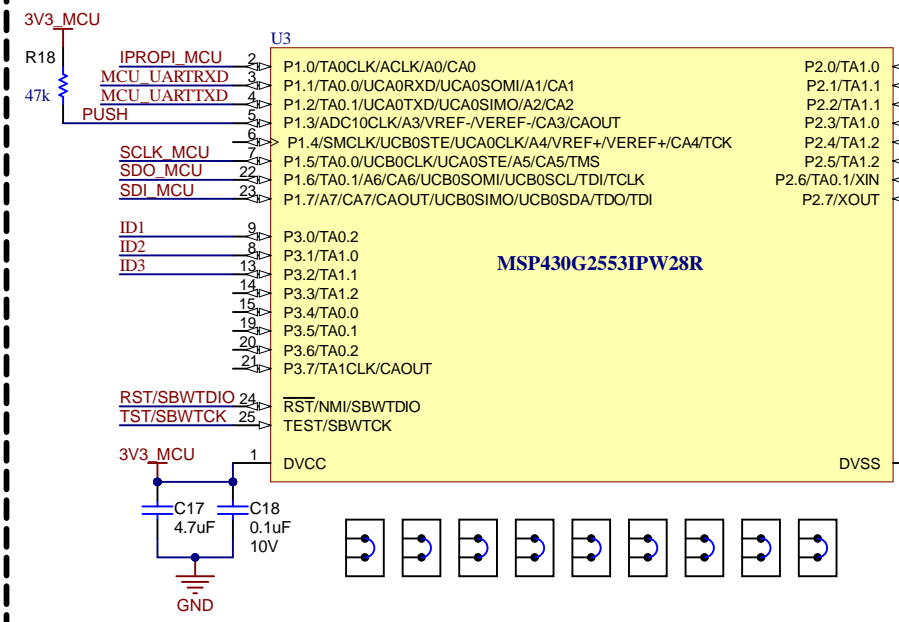
### BUTTON



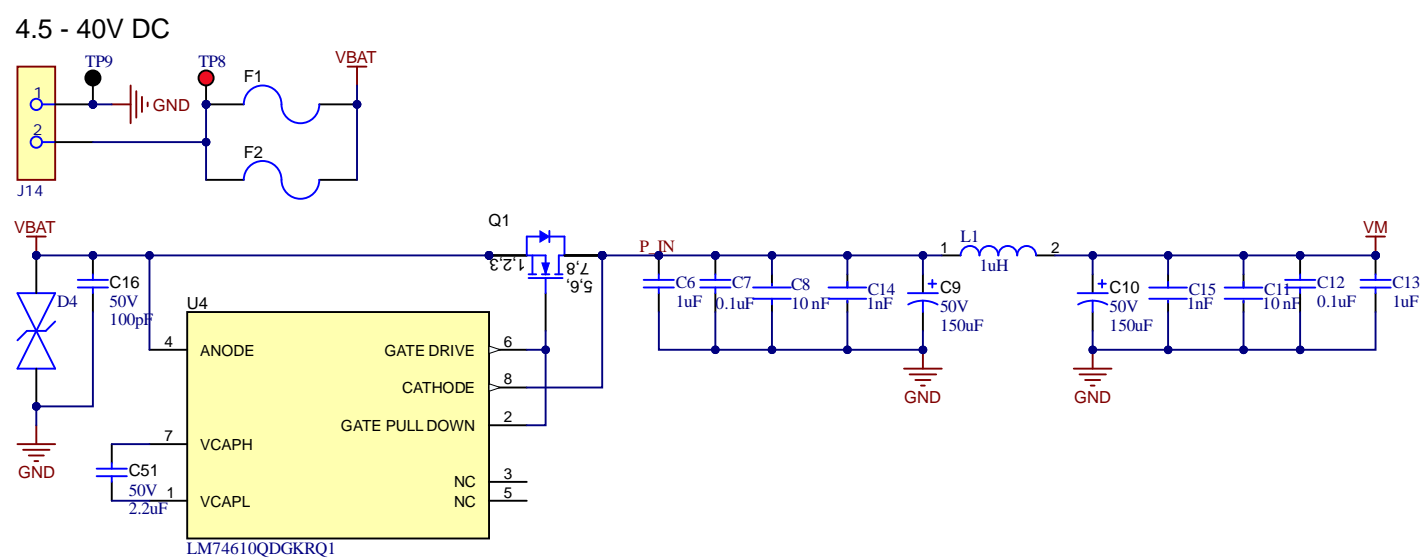
### ANALOG CONTROL SIGNALS (H-variant only)



### MSP430G2553

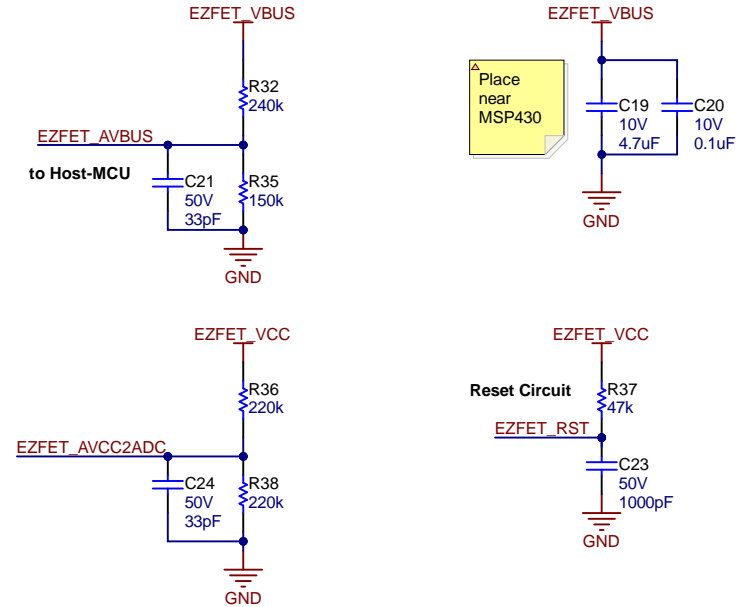
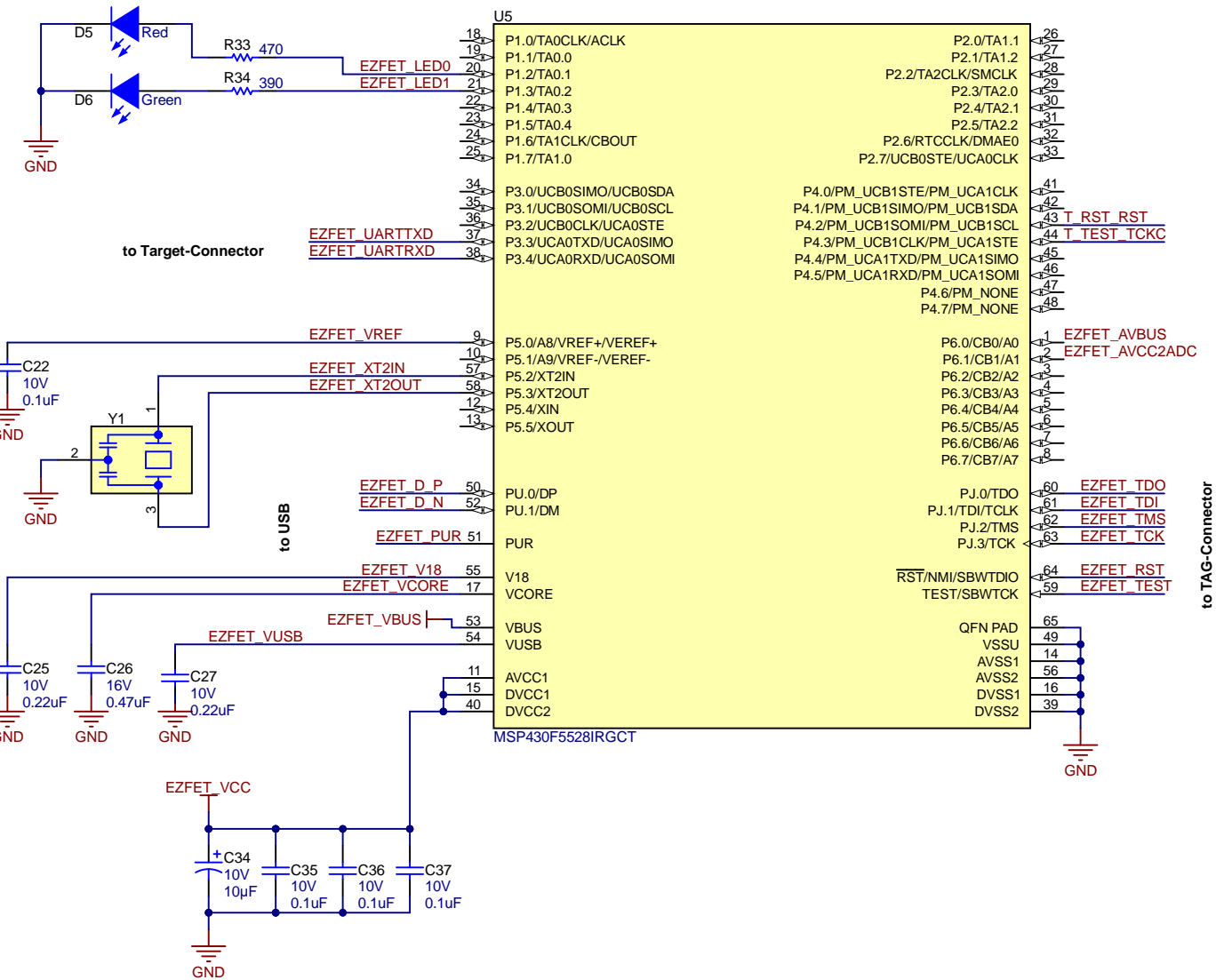


### MAIN INPUT SUPPLY REVERSE PROTECTION & FILTER

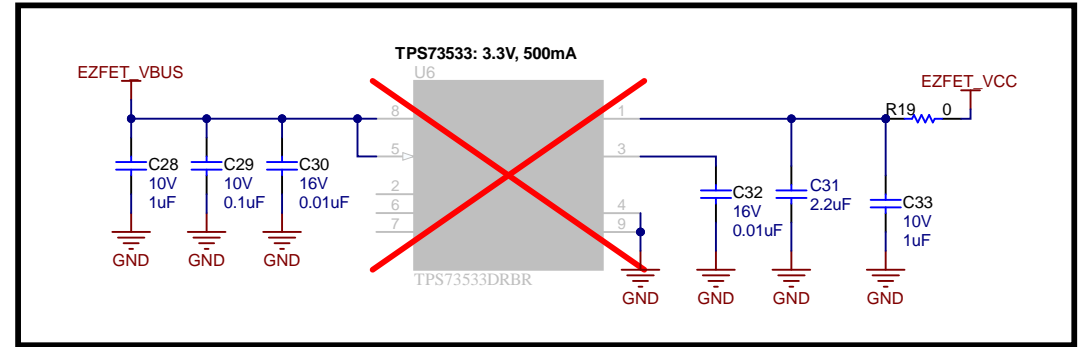


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## Host MCU for Emulation



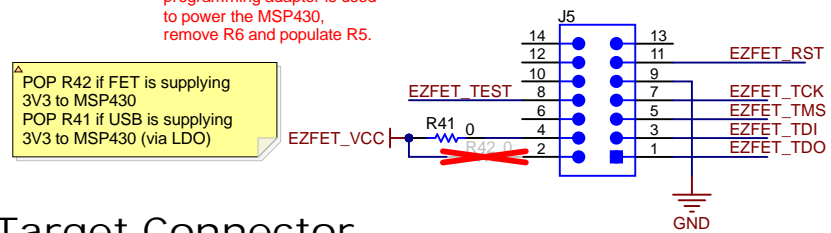
## 3.3V Power (EZFET\_VCC)



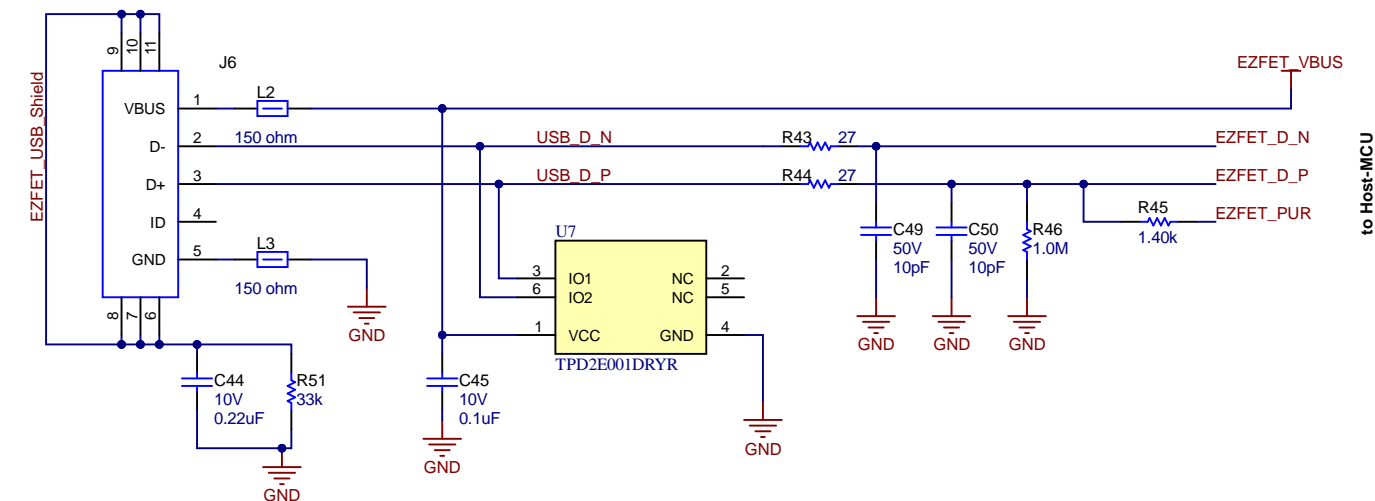
## TAG-Connector (Host Debug)

DNPed R5. If debug or programming adapter is used to power the MSP430, remove R6 and populate R5.

### Host-MCU Debug Connector

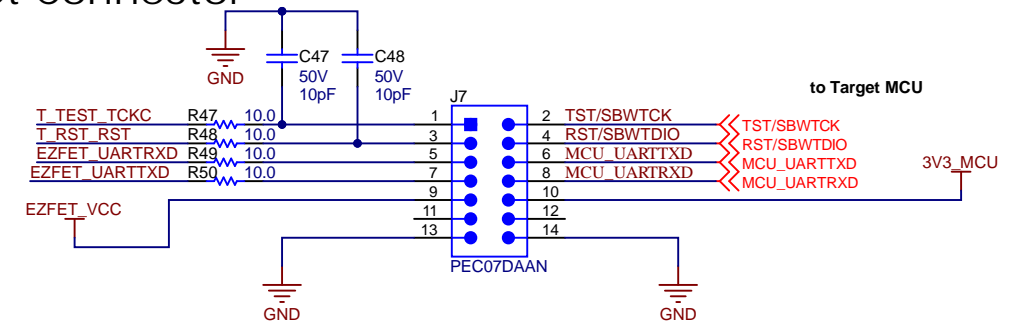


## USB-I nterface



## Target Connector

to Host-MCU  
to Target  
to Host-MCU



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Orderable: DRV8145H-Q1EVM	Designed for: Public Release	Mod. Date: 8/12/2022
TID #: N/A	Project Title: DRV8145S/H-Q1EVM (VQFN)	
Number: MD044	Rev: A	Sheet Title:
SVN Rev: 9497b7228a77c0994b878b0b00144da2402	Sheet: 2 of 3	
Drawn By:	File: ezFET.SchDoc	Size: B
Engineer: Michael Erdahl	Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>	

H1  
SJ-5303 (CLEAR)

H2  
SJ-5303 (CLEAR)

H3  
SJ-5303 (CLEAR)

H4  
SJ-5303 (CLEAR)

FID1

FID2

FID3

PCB Number: MD044

PCB Rev: A

PCB  
LOGO

Texas Instruments

CE Mark

PCB  
LOGO

FCC disclaimer

PCB  
LOGO

WEEE logo

CAUTION HOT SURFACE

PCB  
LOGO

CAUTION. READ USER GUIDE BEFORE USE

LBL1

PCB Label

THT-14-423-10

Size: 0.65" x 0.20 "

ZZ1

Label Assembly Note

This Assembly Note is for PCB labels only

ZZ2

Assembly Note

These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3

Assembly Note

These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4

Assembly Note

These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

Variant/Label Table	
Variant	Label Text
001	DRV8243S-Q1EVM
002	DRV8243H-Q1EVM

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