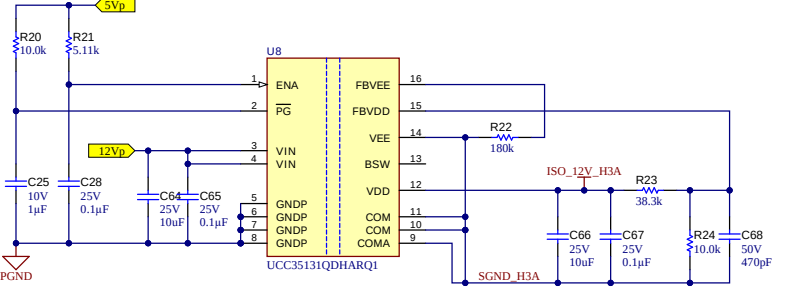
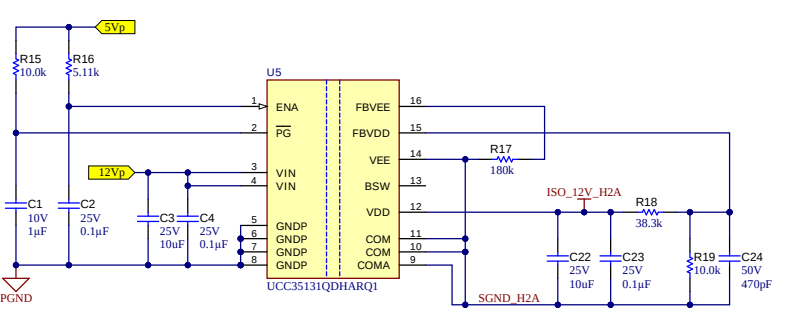
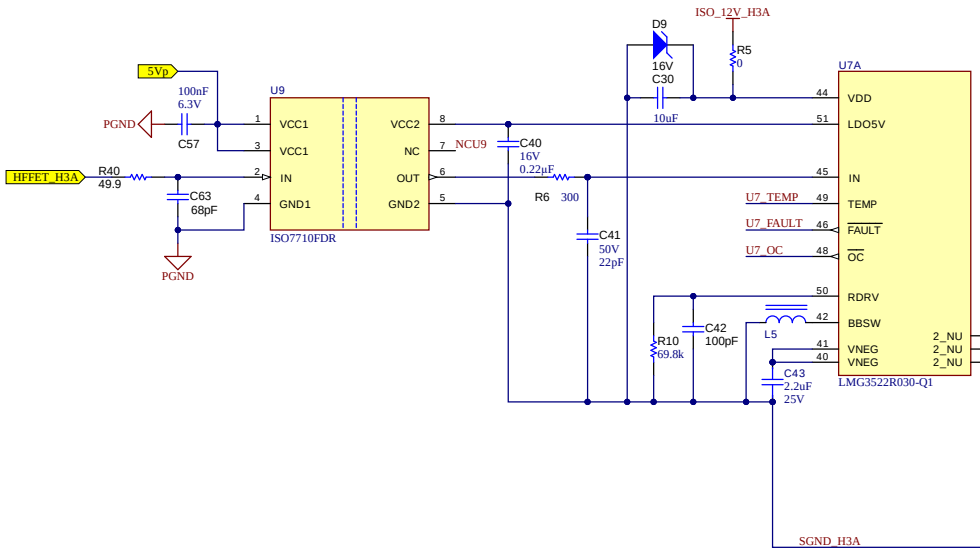
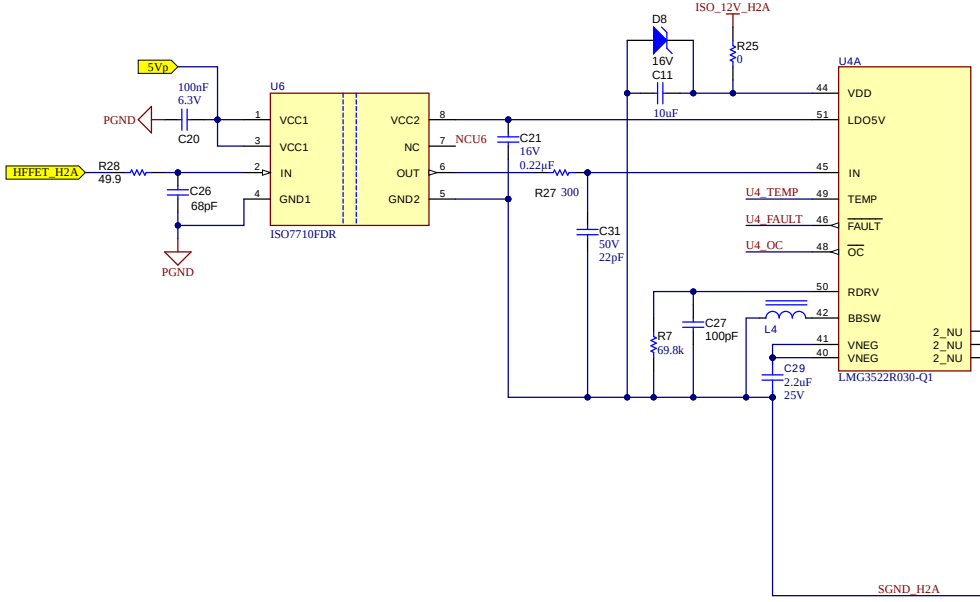
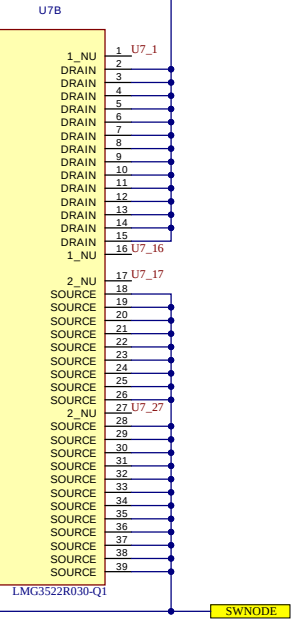
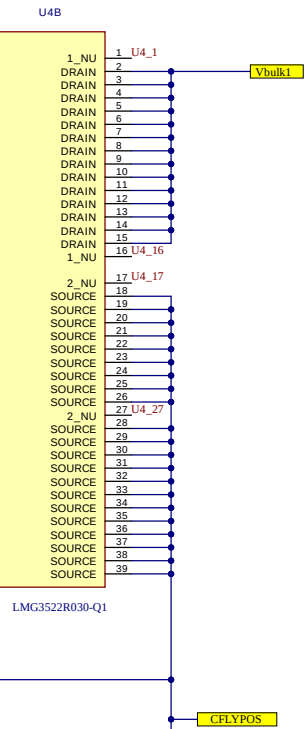
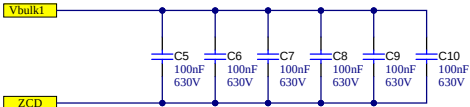
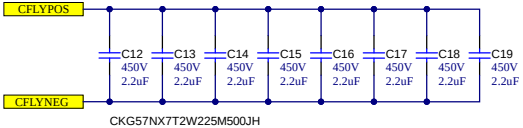
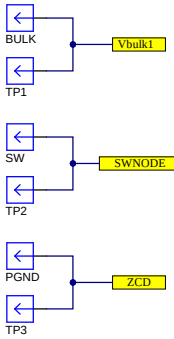
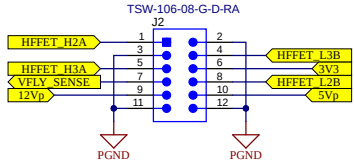
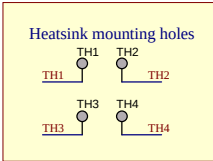


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Orderable: ChangeMe in variant	Designed for: Public Release	Mod. Date: 3/18/2026
TID #: N/A	Project Title: PMP41166 GaN Card	
Number: PMP41166_GdRev: D	Sheet Title:	
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 1 of 3
Drawn By:	File: PMP41166D_Multi_Level_GaN_Card1.SchDoc	Size: A2
Engineer: B. Sun / E. Chen	Contact: http://www.ti.com/support	© Texas Instruments 2025



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TID #: N/A	Project Title: PMP41166 GaN Card	
Number: PMP41166_Gd	Rev: D	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 2 of 3
Drawn By:	File: PMP41166D_Multi_Level_GaN_Card2_SchDoc	Size: A2
Engineer: B. Sun / E. Chen	Contact: http://www.ti.com/support	

