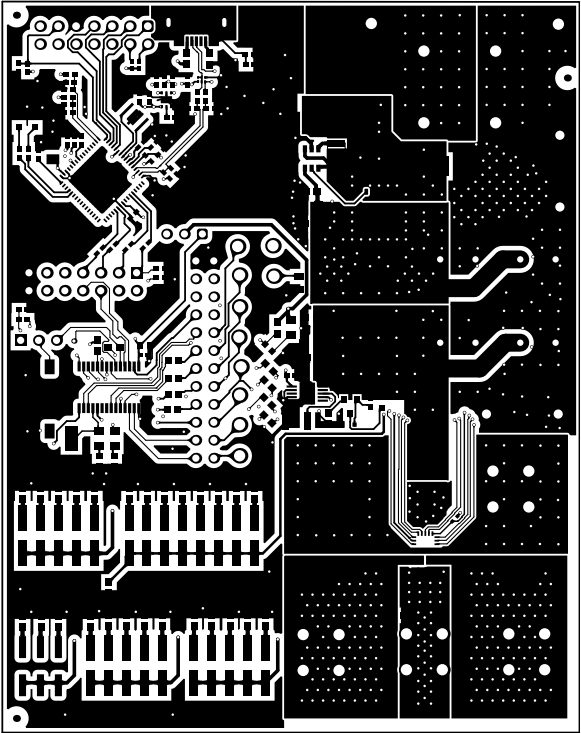
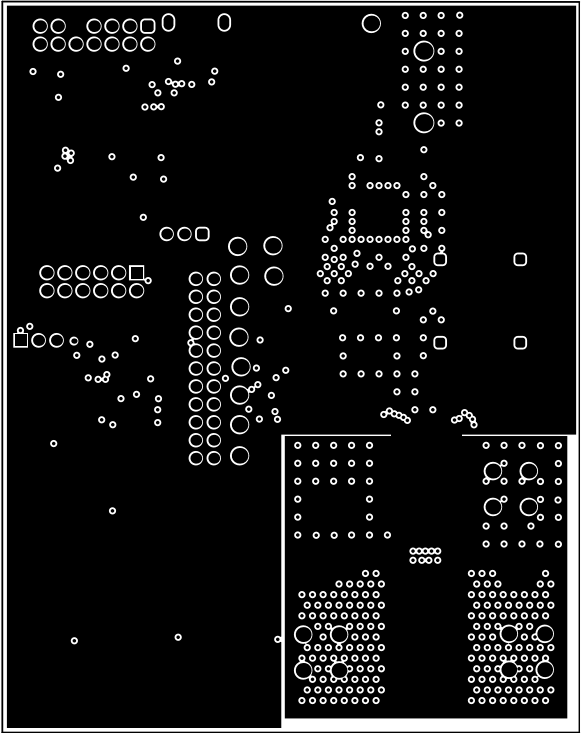


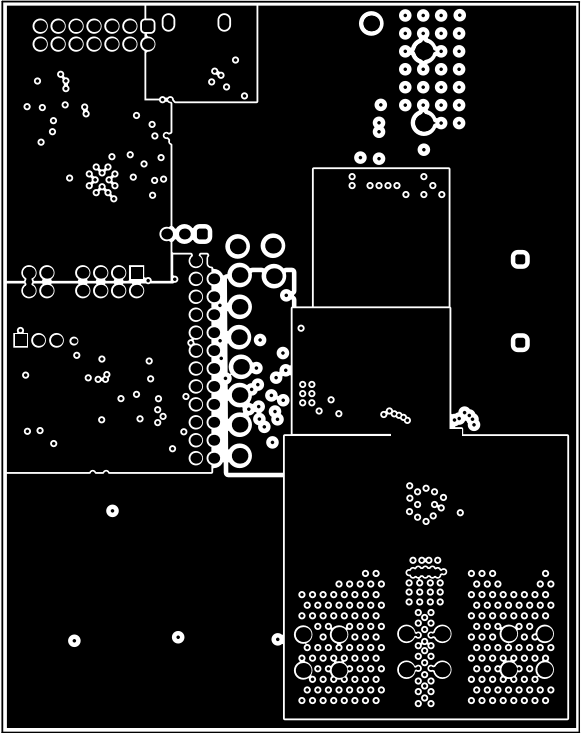
ALL NETWORK VIEWED FROM TOP SIDE	BOARD #	PC009	REV	E1	SUN REV	Not in version control
LAYER NAME = Top Solder	TID #	N/A				
PLOT NAME = Top Solder Mask	GENERATED	6/26/2024	2:00:12 PM	TEXAS INSTRUMENTS		



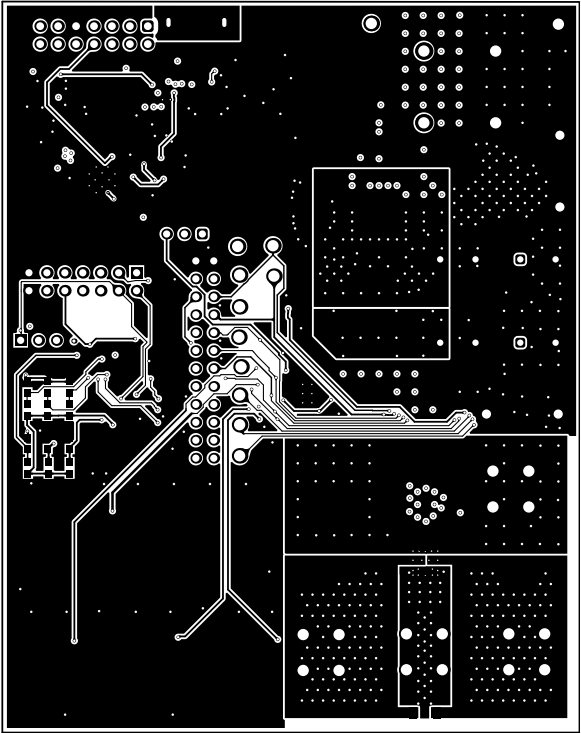
ALL NETWORK VIEWED FROM TOP SIDE	BORD #	PC009	REV	E1	SUN REV	Not in version control
LAYER NAME = Top Layer	TID #	N/A				
PLOT NAME = Top Layer	GENERATED	4/26/2024	2:00:14 PM	TEXAS INSTRUMENTS		



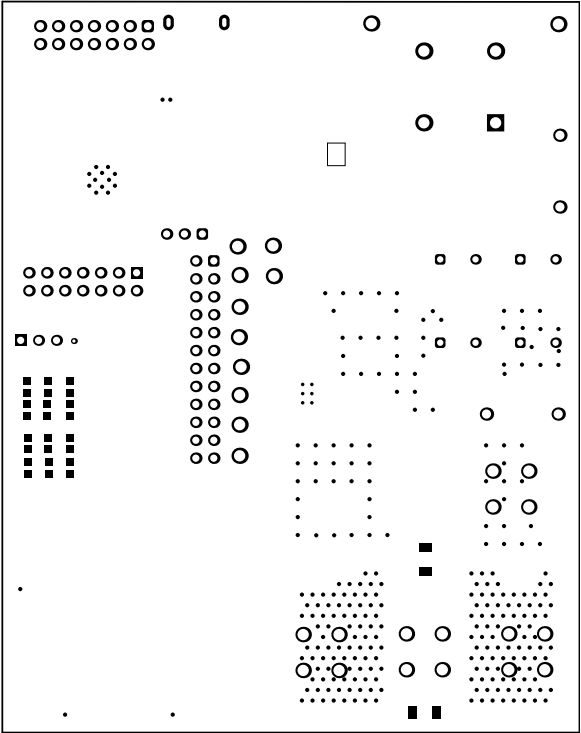
ALL NETWORK VIEWED FROM TOP SIDE	BORD #	PC009	REV	ET	SUN REV	Not in version control
LAYER NAME =	TID #	N/A				
PLOT NAME =	Signal 1	GENERATED	6/26/2024	2:00:14 PM	TEXAS INSTRUMENTS	



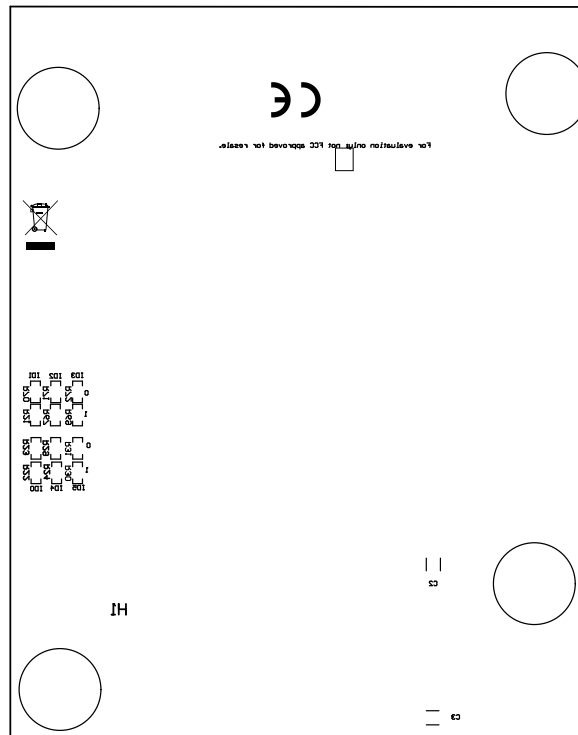
ALL NETWORK VIEWED FROM TOP SIDE	BORD #	PC009	REV	E1	SUN REV	Not in version control
LAYER NAME *	TID #	N/A				
PLOT NAME *	Signal 2	GENERATED *	6/26/2024	2:00:18 PM	TEXAS INSTRUMENTS	



ALL NETWORK VIEWED FROM TOP SIDE	BORD #	PC099	REV	ET	SUN REV	Not in version control
LAYER NAME = Bottom Layer	TID #	N/A				
PLOT NAME = Bottom Layer	GENERATED	4/26/2024	2:00:18 PM	TEXAS INSTRUMENTS		



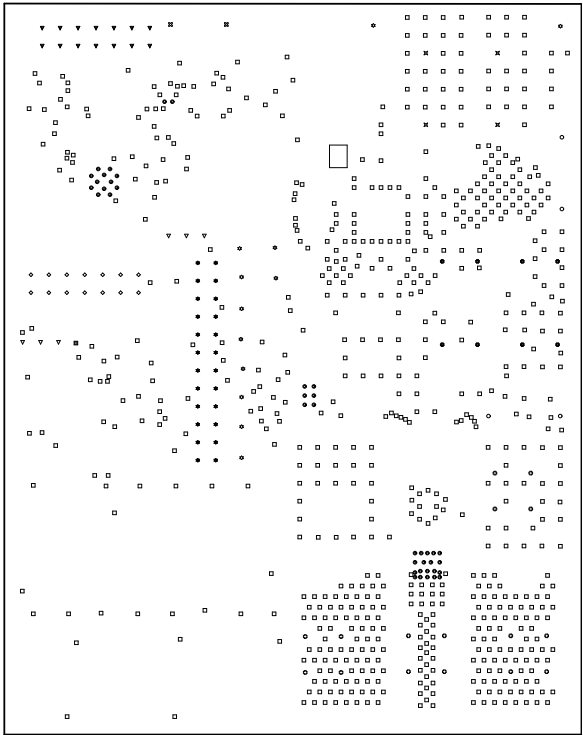
ALL NETWORK VIEWED FROM TOP SIDE	BOARD #	PCB099	REV	E1	SUN REV	Not in version control
LAYER NAME = Bottom Solder	TID #	N/A				
PLOT NAME = Bottom Solder Mask	GENERATED	4/26/2024	2:00:14 PM	TEXAS INSTRUMENTS		



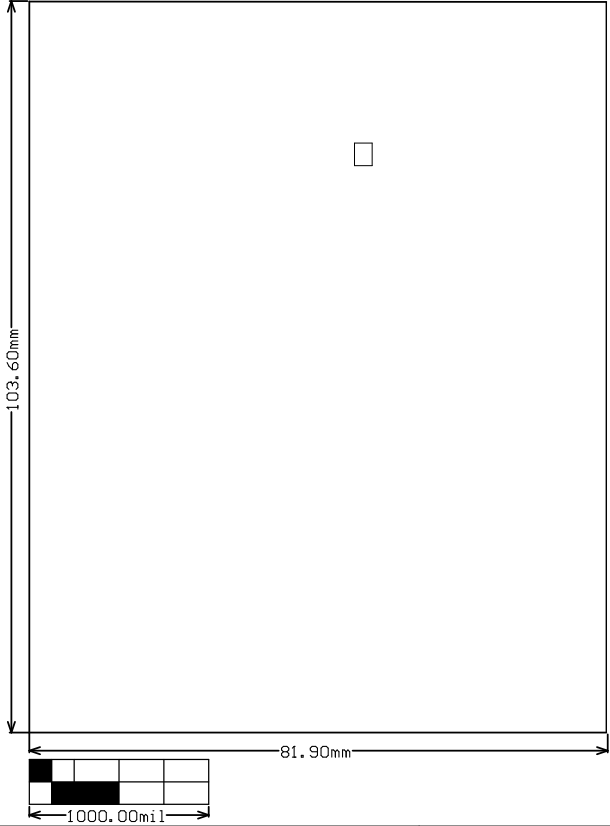
ALL NETWORK VIEWED FROM TOP SIDE	BOARD #: Y0009	REV: EI	SUN REV: Not in version control
LAYER NAME = Bottom Overlay	TID #: N/A		
PLOT NAME = Bottom Overlay	GENERATED : 6/26/2024 2:00:16 PM		TEXAS INSTRUMENTS

Symbol	Quantity	Finished Hole Size	Plated	Hole Type	Drill Layer Pair	Hole Tolerance
⊙	39	7.87mil (0.200mm)	PTH	Round	Top Layer - Bottom Layer	
□	692	13.78mil (0.350mm)	PTH	Round	Top Layer - Bottom Layer	
▣	1	17.72mil (0.450mm)	PTH	Round	Top Layer - Bottom Layer	
⊕	8	35.43mil (0.900mm)	PTH	Round	Top Layer - Bottom Layer	
◇	14	40.00mil (1.016mm)	PTH	Round	Top Layer - Bottom Layer	
✱	24	40.16mil (1.020mm)	PTH	Round	Top Layer - Bottom Layer	+/-3.15mil
▽	6	40.16mil (1.020mm)	PTH	Round	Top Layer - Bottom Layer	+/-1.97mil
▼	14	43.31mil (1.100mm)	PTH	Round	Top Layer - Bottom Layer	
○	4	51.18mil (1.300mm)	PTH	Round	Top Layer - Bottom Layer	
⊗	4	62.99mil (1.600mm)	PTH	Round	Top Layer - Bottom Layer	+3.94mil/-0.00mil
⊕	16	62.99mil (1.600mm)	PTH	Round	Top Layer - Bottom Layer	
✱	12	63.00mil (1.600mm)	PTH	Round	Top Layer - Bottom Layer	
⊗	2	23.62mil (0.600mm)	PTH	Slot	Top Layer - Bottom Layer	
	836 Total					

Slot definitions : Routed Path Length = Calculated from tool start centre position to tool end centre position.
Hole Length = Routed Path Length + Tool Size = Slot length as defined in the PCB layout



ALL NETWORK USED FROM TOP SIDE	BOARD #:	PCB099	REV:	E1	SUN REV:	Not in version control
LAYER NAME =	Drill Drawing	TID #:	N/A			
PLOT NAME =	Drill Drawing	GENERATED :	6/26/2024	2:00:17 PM	TEXAS INSTRUMENTS	



ALL NETWORK VIEWED FROM TOP SIDE	BOARD #	PC099	REV	E1	SUN REV	Not in version control
LAYER NAME =	T2 Board Dimensions					
PLOT NAME =	Board Dimensions					
GENERATED	4/26/2024		2:00:18 PM		TEXAS INSTRUMENTS	