TPS62876 Buck Converter Evaluation Module



ABSTRACT

This user's guide describes the characteristics, operation, and use of Tl's evaluation module (EVM) for the TPS6287x-Q1 devices. The TPS62876EVM-198 is designed to help the user easily evaluate and test the operation and functionality of the TPS62876-Q1 buck converters. The TPS62876EVM-198 can be used to evaluate the TPS62874-Q1, TPS62875-Q1, TPS62876-Q1, and TPS62877-Q1. The EVM converts 2.7-V to 6.0-V input voltage to a regulated 0.75-V output voltage. The output current can go up to 25-A for the TPS62876EVM-198. This user's guide includes setup instructions for the hardware, a printed-circuit board (PCB) layout, a schematic diagram, and a bill of materials (BOM).

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1 Warning and Caution



Caution

Caution Hot surface.
Contact may cause burns.
Do not touch!

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2 Introduction

The TPS62876-Q1 device is a high-frequency, synchronous step-down converter optimized for a small solution size and high efficiency. The device focuses on high-efficiency, step-down conversion over a wide output current range. At medium to heavy loads, the converter operates in PWM mode and automatically enters *power save mode* operation at light load to maintain high efficiency over the entire load-current range. The device is available in a 4.05-mm × 3.05-mm, 24-pin VQFN package.

2.1 Performance Specification

Table 2-1 provides a summary of the TPS62876EVM-198 performance specifications.

Table 2-1. Perform	ance Specific	ation Summary
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Specification		Test Conditions	MIN	TYP	MAX	Unit
Input voltage			2.7		6.0	V
Output voltage setpoint				0.75		V
Output current	TPS62876EVM-198		0		25.0	Α

2.2 Modifications

The printed-circuit board (PCB) for this EVM is designed to accommodate the different output current versions of this integrated circuit (IC). On the EVM, additional output capacitors can be added, the default output voltage can be changed, and the switching frequency can be modified.

2.2.1 Input and Output Capacitors

Footprints for additional input capacitors and additional output capacitors are provided. These capacitors are not required for proper operation, but can be used to reduce the input and output voltage ripple and to improve the load transient response. For proper operation, the total output capacitance must remain within the recommended range described in the TPS62876-Q1 Automotive, Low Input Voltage, 25-A, Stackable, Synchronous Buck Converter data sheet.

2.2.2 Output Voltage Setting

The output voltage of U1 is set to a default of 0.75 V. Other default voltages can be set using appropriate values for the resistors R5 and R6. During operation, the output voltage can be changed using the I²C interface. For more details, see the *TPS62876-Q1 Automotive*, *Low Input Voltage*, *25-A*, *Stackable*, *Synchronous Buck Converter* data sheet.

2.2.3 Control Loop Compensation

C46, C47, and R1 are used for compensating the control loop. If the output capacitors have been changed adjustments, of the component values in the compensation network can be necessary. For more details, see the TPS62876-Q1 Automotive, Low Input Voltage, 25-A, Stackable, Synchronous Buck Converter data sheet.

2.2.4 Switching Frequency Setting

The switching frequency is set to a default of 2.25 MHz. By changing the values of R3 and R4, the default switching frequency can be changed. For more details, see the *TPS62876-Q1 Automotive*, *Low Input Voltage*, *25-A*, *Stackable*, *Synchronous Buck Converter* data sheet.

2.2.5 I²C Interface

Output voltage, output voltage ramp time, soft-start time, and various control features can be controlled through the I²C interface. IC status information is also available. For more details, see the *TPS62876-Q1 Automotive*, *Low Input Voltage*, *25-A*, *Stackable*, *Synchronous Buck Converter* data sheet.

RUMENTS Setup www.ti.com

3 Setup

This section describes how to correctly use the TPS62876EVM-198.

3.1 Connector Descriptions

MH1 - VIN Positive input voltage connection from the input supply for the EVM J1-1 - VINsense, J1-2 - GNDsense Input voltage sense connections, measure the input voltage at this point.

MH2 - GND Input return connection from the input supply for the EVM

MH3 - VOUT Positive output voltage connection

J8-1 - VOUTsense, J8-2 -

GNDsense

Output voltage sense connections, measure the output voltage at this point.

MH4 - GND Output return connection

J3 - EN EN pin jumper. Place the supplied jumper across ON and EN to turn on the IC. Place the jumper

across OFF and EN to turn off the IC.

J4 - MODE/SYNC MODE/SYNC pin jumper. Place the supplied jumper across VIN and MODE/SYNC to force the

device in fixed frequency PWM operation at all load currents. Place the jumper across MODE/SYNC and GND to enable power save mode. Connect a clock signal to MODE/SYNC referenced to GND to

synchronize the switching frequency to the clock signal.

J5 - PG The PG output appears on pin 1 of this header with a convenient ground on pin 2.

J7-SYNC OUT At the SYNC OUT output, the switching frequency is provided at pin 1 with a convenient ground on

pin 2.

J6 - 12CI²C connection configured for being used with the USB2ANY interface.

3.2 Hardware Setup

To operate the EVM, set jumpers J3 and J4 to the desired positions per Connector Descriptions. Connect the input supply to MH1 and MH2, between VIN and GND, and connect the load to MH3 and MH4 between VOUT and GND.

To evaluate the I²C features a USB2ANY interface can be connected to J6. For this interface, a software GUI is available here.

4 TPS62876EVM-198 Test Results

The TPS62876EVM-198 was used to take the typical characteristics data in the TPS62876-Q1 data sheet. See the TPS62876-Q1 Automotive, Low Input Voltage, 25-A, Stackable, Synchronous Buck Converter data sheet for the performance of this EVM.

5 Board Layout

This section provides the TPS62876EVM-198 board layout. The gerber files are available on the TPS62876EVM-198 tool page.

www.ti.com Board Layout

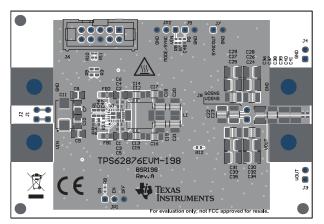


Figure 5-1. Top Silk

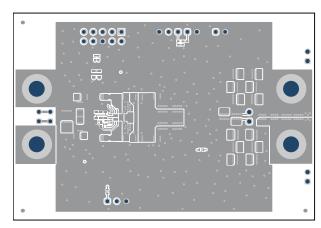


Figure 5-2. Top Layer

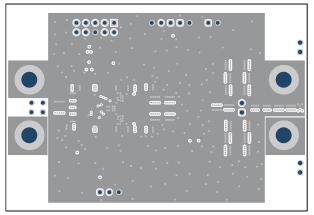


Figure 5-3. Layer 2

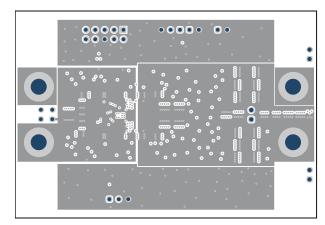


Figure 5-4. Layer 3

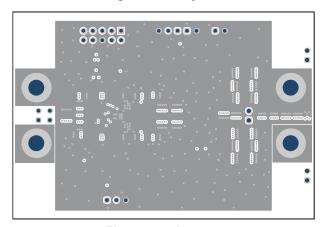


Figure 5-5. Layer 4

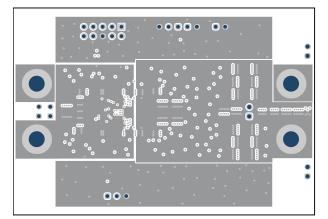
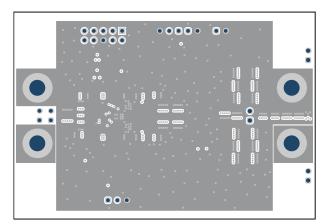


Figure 5-6. Layer 5

Schematic and Bill of Materials www.ti.com



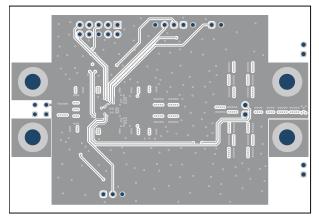


Figure 5-7. Layer 6

Figure 5-8. Layer 7

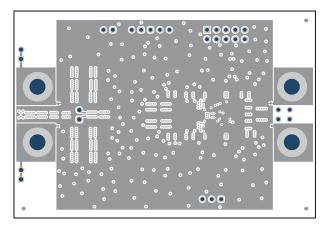


Figure 5-9. Bottom Layer

6 Schematic and Bill of Materials

This section includes the TPS62876EVM-198 schematics and bills of materials.



6.1 Schematic

Figure 6-1 shows the schematic of TPS62876EVM-198.

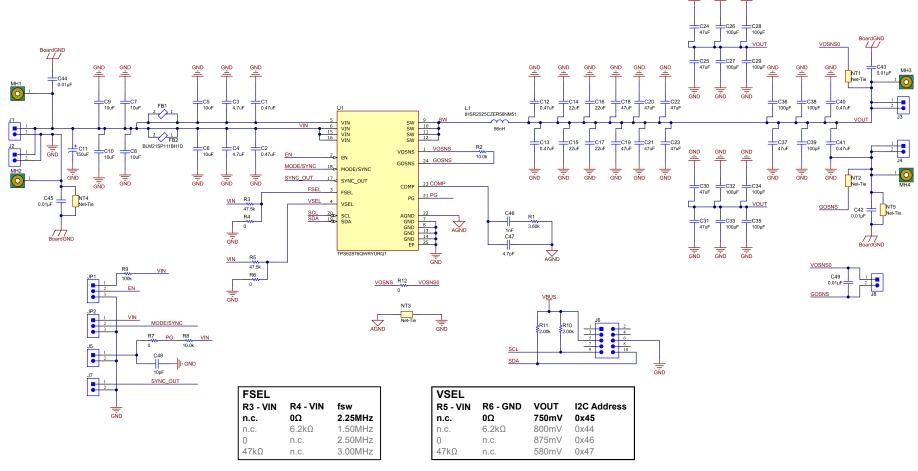


Figure 6-1. TPS62876EVM-198 Schematic



6.2 Bill of Materials

Table 6-1 lists the BOM for the TPS62876EVM-198.

Table 6-1. TPS62876EVM-198 BOM

Qty	Ref Des	Value	Description	Size	Part Number	MFR
6	C1, C2, C12, C13, C40, C41	0.47 μF	Ceramic Capacitor, 10 V, X7S	0402	GCM155C71A474KE36D	any
2	C3, C4	4.7 µF	Ceramic Capacitor, 10 V, X5R	0603	LMK107BJ475MAHT	Taiyo Yuden
6	C5, C6, C7, C8, C9, C10	10 μF	Ceramic Capacitor, 10 V, X7R	0805	GCM21BR71A106KE22L	MuRata
2	C14, C15	22 µF	Ceramic Capacitor, 10 V, X7R	1206	GCM31CR71A226KE02L	MuRata
8	C18, C19, C22, C23, C24, C25, C30, C31	47 μF	Ceramic Capacitor, 6.3 V, X7R	1210	GCM32ER70J476ME19L	MuRata
3	C26, C32, C36	100 μF	Ceramic Capacitor, 6.3 V, X5R	1210	GRT32ER60J107ME13L	MuRata
1	C11	150 µF	Tantalum Capacitor, 16 V	7.3 × 4.3 mm	T495D157K016ATE125	Kemet
1	C46	1000 pF	Ceramic Capacitor, 50 V, X7R	0603		any
1	C47	4.7 pF	Ceramic Capacitor, 50 V, COG/NPO	0603		any
1	C48	10 pF	Ceramic Capacitor, 50 V, COG/NPO	0402		any
1	L1	56 nH	Shielded Power Inductor	6.65 × 6.65 × 3.0 mm	IHSR2525CZER56NM51	Vishay Dale
1	R1	3.6 kΩ	Resistor 1%, 0.1 W	0603		any
2	R4, R6	0 Ω	Resistor 1%, 0.1 W	0603		any
2	R9	100 kΩ	Resistor 1%, 0.1 W	0402		any
2	R7, R12	0 Ω	Resistor 1%, 0.1 W	0402		any
1	R8	10 kΩ	Resistor 1%, 0.1 W	0402		any
2	R10, R11	2 kΩ	Resistor 1%, 0.1 W	0402		any
1	U1		2.7-V to 6-V Input, 25-A, Fast Transient Synchronous Step-Down Converter with I ² C Interface, Remote Sense, Droop Compensation and Stackability	4.05 × 3.05 mm	TPS62876QWRZVRQ1	Texas Instruments

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3.1.2 For EVMs annotated as FCC – FEDERAL COMMUNICATIONS COMMISSION Part 15 Compliant:

CAUTION

This device complies with part 15 of the FCC Rules. Operation is subject to the following two conditions: (1) This device may not cause harmful interference, and (2) this device must accept any interference received, including interference that may cause undesired operation.

Changes or modifications not expressly approved by the party responsible for compliance could void the user's authority to operate the equipment.

FCC Interference Statement for Class A EVM devices

NOTE: This equipment has been tested and found to comply with the limits for a Class A digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instruction manual, may cause harmful interference to radio communications. Operation of this equipment in a residential area is likely to cause harmful interference in which case the user will be required to correct the interference at his own expense.

FCC Interference Statement for Class B EVM devices

NOTE: This equipment has been tested and found to comply with the limits for a Class B digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates, uses and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following measures:

- Reorient or relocate the receiving antenna.
- Increase the separation between the equipment and receiver.
- · Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.
- Consult the dealer or an experienced radio/TV technician for help.

3.2 Canada

3.2.1 For EVMs issued with an Industry Canada Certificate of Conformance to RSS-210 or RSS-247

Concerning EVMs Including Radio Transmitters:

This device complies with Industry Canada license-exempt RSSs. Operation is subject to the following two conditions:

(1) this device may not cause interference, and (2) this device must accept any interference, including interference that may cause undesired operation of the device.

Concernant les EVMs avec appareils radio:

Le présent appareil est conforme aux CNR d'Industrie Canada applicables aux appareils radio exempts de licence. L'exploitation est autorisée aux deux conditions suivantes: (1) l'appareil ne doit pas produire de brouillage, et (2) l'utilisateur de l'appareil doit accepter tout brouillage radioélectrique subi, même si le brouillage est susceptible d'en compromettre le fonctionnement.

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Under Industry Canada regulations, this radio transmitter may only operate using an antenna of a type and maximum (or lesser) gain approved for the transmitter by Industry Canada. To reduce potential radio interference to other users, the antenna type and its gain should be so chosen that the equivalent isotropically radiated power (e.i.r.p.) is not more than that necessary for successful communication. This radio transmitter has been approved by Industry Canada to operate with the antenna types lated in the user guide with the maximum permissible gain and required antenna impedance for each antenna type indicated. Antenna types not included in this list, having a gain greater than the maximum gain indicated for that type, are strictly prohibited for use with this device.

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- 2. Use EVMs only after User obtains the license of Test Radio Station as provided in Radio Law of Japan with respect to EVMs, or
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 - 4.3 Safety-Related Warnings and Restrictions:
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