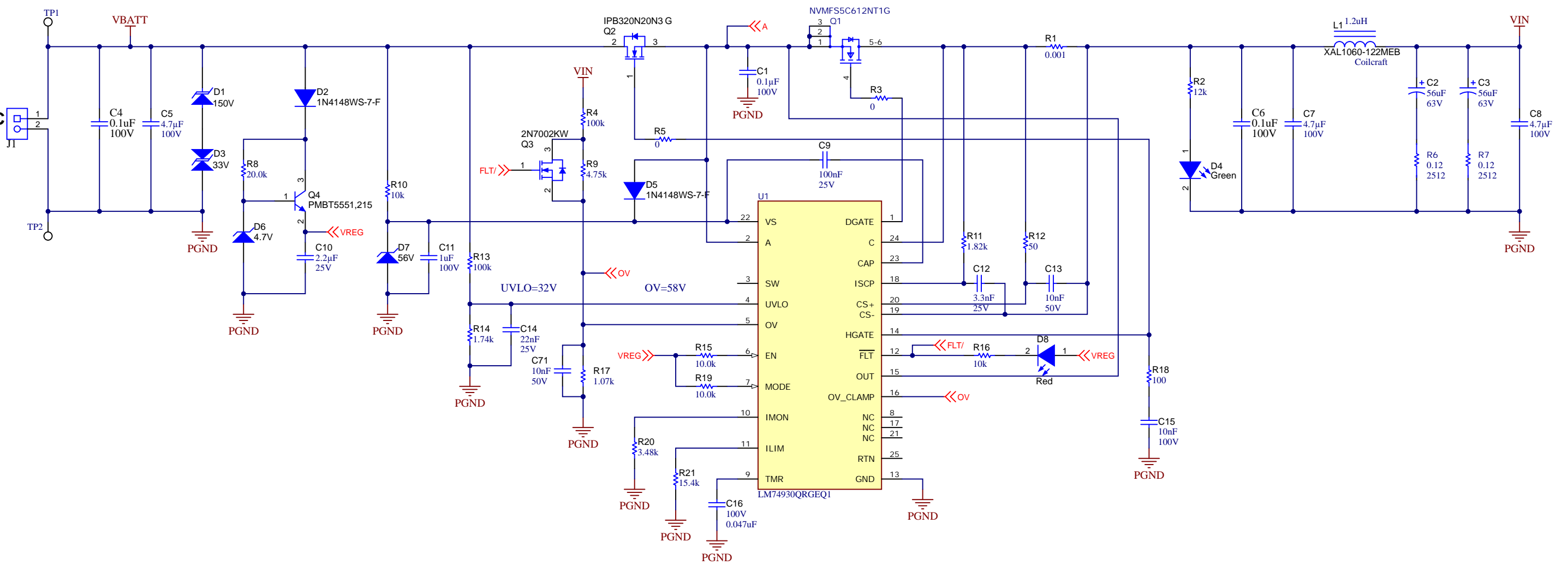


INPUT:
36-54VDC



PRELIMINARY

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| Orderable: ChangeMe in variant | Designed for: Public Release | Mod. Date: 9/24/2024 |
| TID #: N/A | Project Title: Change in menu Project Project Options Parameter | |
| Number: XXX### | Rev: E1 | Sheet Title: LM25143-Q1 Two Phases |
| SVN Rev: Not in version control | Assembly Variant: 001 | Sheet: 1 of 2 |
| Drawn By: | File: PMP23468-SH1_SchDoc | Size: B |
| Engineer: Enter name of project lead | Contact: http://www.ti.com/support | © Texas Instruments 2018 |



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| Orderable: ChangeMe in variant | Designed for: Public Release | Mod. Date: 9/23/2024 |
| TID #: N/A | Project Title: Change in menu Project[Project Options]Parameter | |
| Number: XXX### | Rev: E1 | Sheet Title: LM25143-Q1 Two Phases |
| SVN Rev: Not in version control | Assembly Variant: 001 | Sheet: 2 of 2 |
| Drawn By: | File: PMP23468-SH2.SchDoc | Size: B |
| Engineer: Enter name of project lead | Contact: http://www.ti.com/support | |

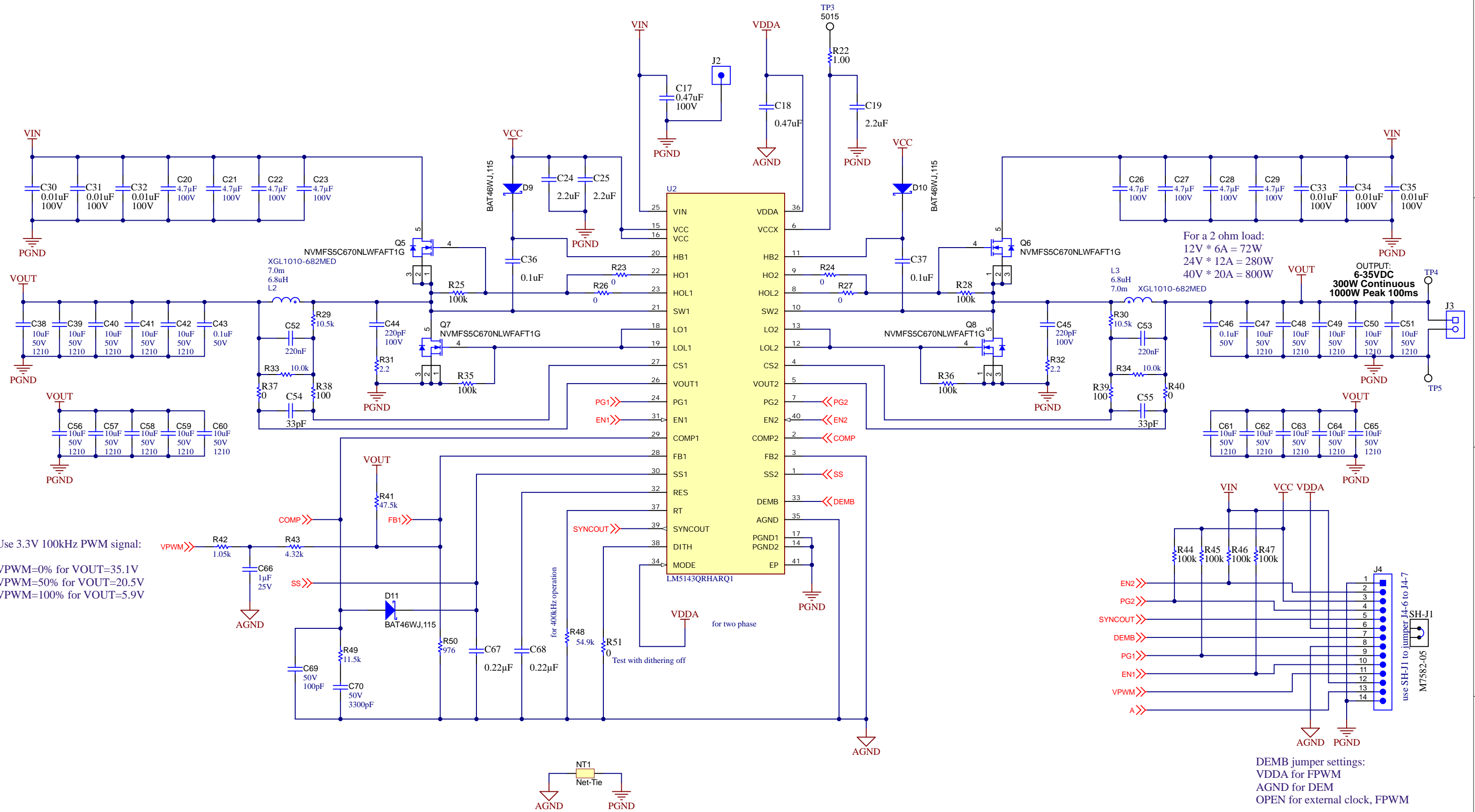


Use 3.3V 100kHz PWM signal:
 VPWM=0% for VOUT=35.1V
 VPWM=50% for VOUT=20.5V
 VPWM=100% for VOUT=5.9V

For a 2 ohm load:
 12V * 6A = 72W
 24V * 12A = 280W
 40V * 20A = 800W

OUTPUT:
 6-35VDC
 300W Continuous
 1000W Peak 100ms

DEMB jumper settings:
 VDDA for FPWM
 AGND for DEM
 OPEN for external clock, FPWM



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