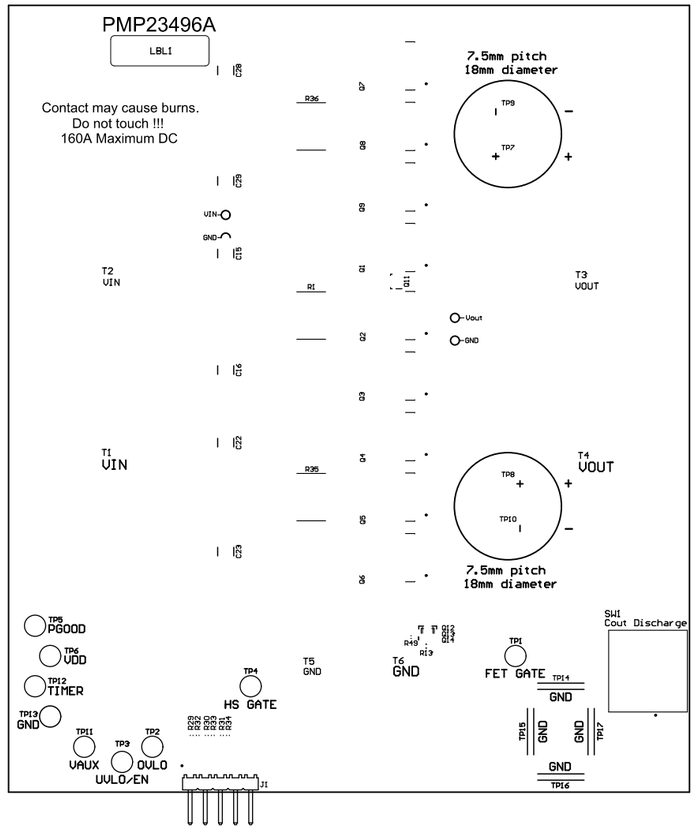
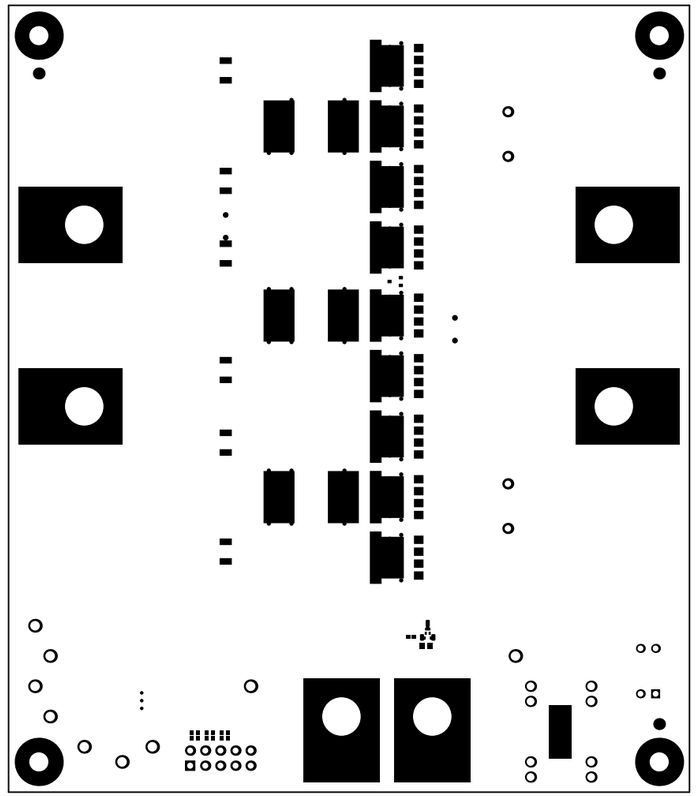
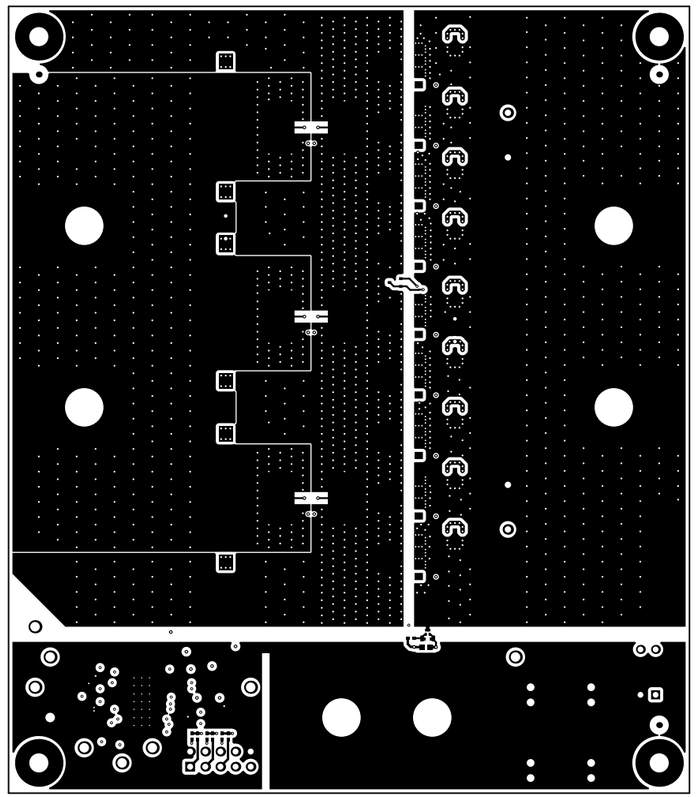


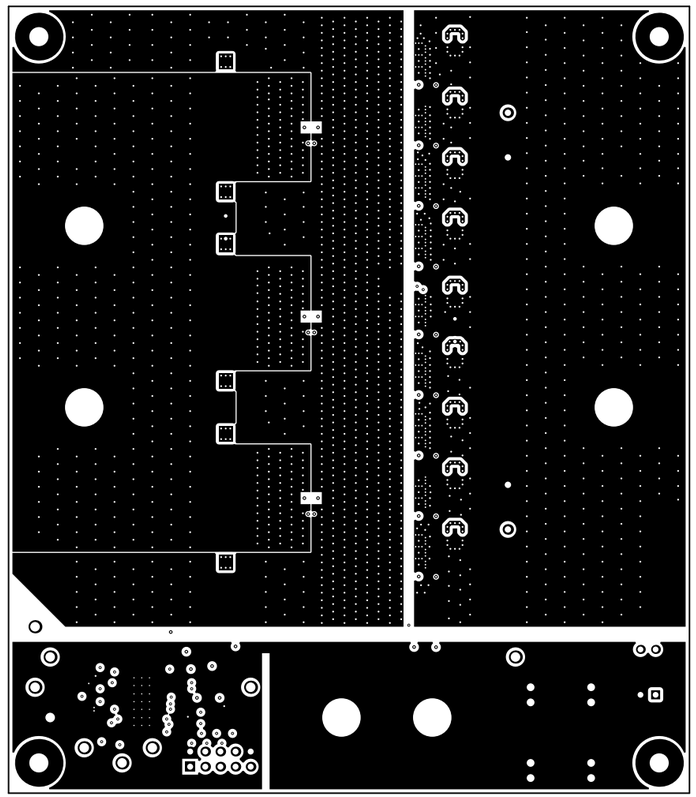
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PMP23496	REV: A	SVN REV: Not in version control
LAYER NAME = Top Overlay			
PLOT NAME = Top Overlay	GENERATED : 16-07-2024 04:04:11 PM		TEXAS INSTRUMENTS



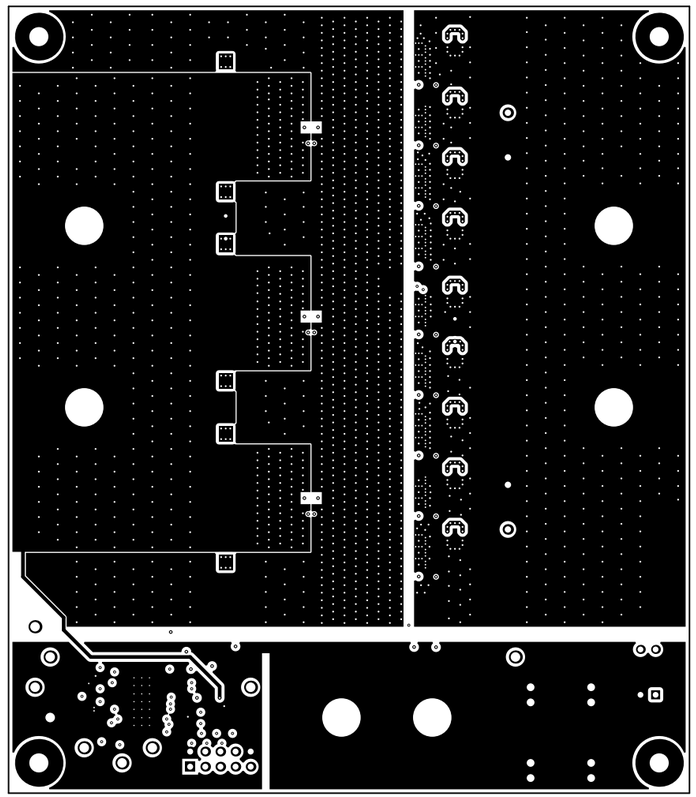
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PMP23496	REV: A	SVN REV: Not in version control
LAYER NAME = **Top Solder**			
PLOT NAME = Top Solder Mask	GENERATED : 16-07-2024 04:04:11 PM		TEXAS INSTRUMENTS



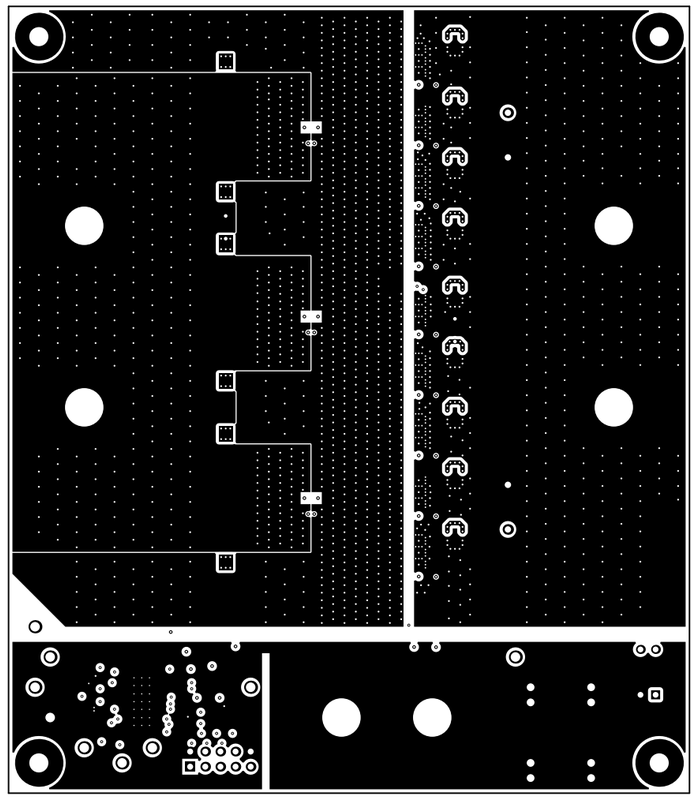
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PMP23496	REV: A	SVN REV: Not in version control
LAYER NAME = Top Layer			
PLOT NAME = Top Layer	GENERATED : 16-07-2024 04:04:11 PM		TEXAS INSTRUMENTS



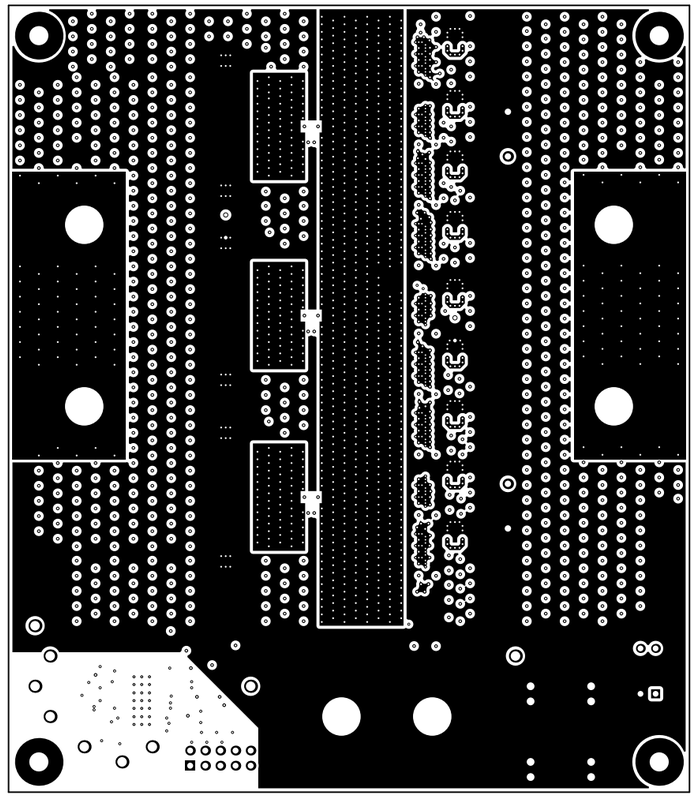
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PMP23496	REV: A	SVN REV: Not in version control
LAYER NAME =			
PLOT NAME = Signal Layer 1	GENERATED : 16-07-2024 04:04:12 PM		TEXAS INSTRUMENTS



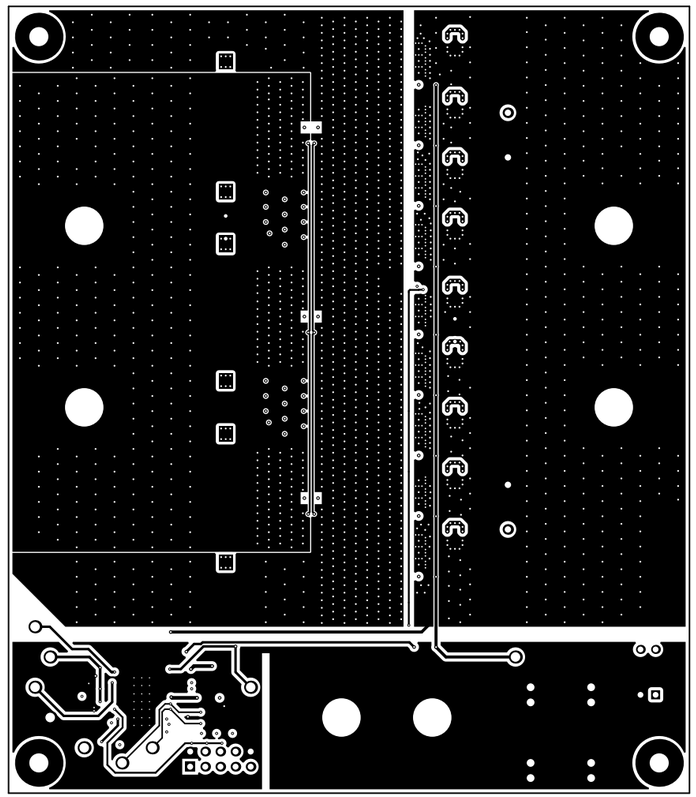
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PMP23496	REV: A	SVN REV: Not in version control
LAYER NAME =			
PLOT NAME = Signal Layer 2	GENERATED : 16-07-2024 04:04:12 PM		TEXAS INSTRUMENTS



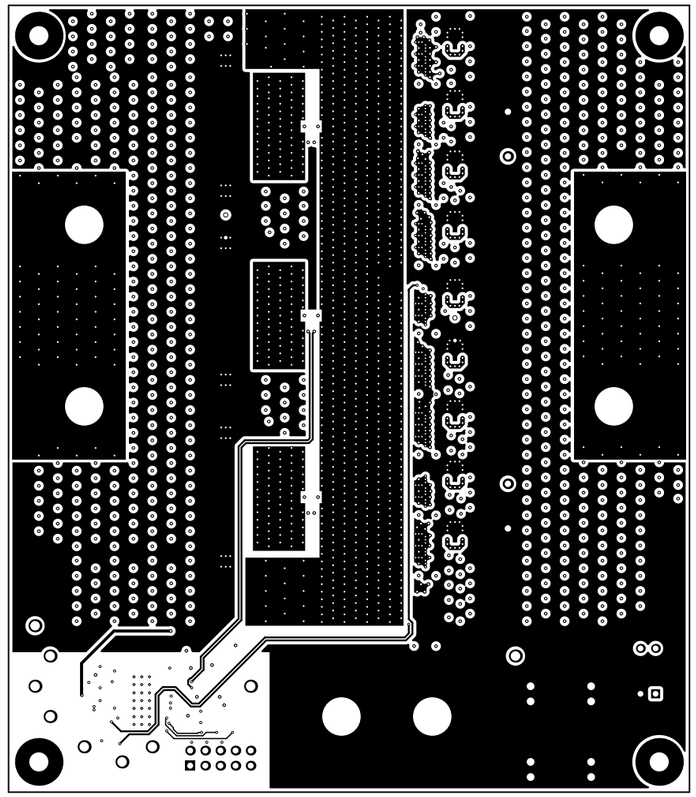
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PMP23496	REV: A	SVN REV: Not in version control
LAYER NAME =			
PLOT NAME = Signal Layer 3	GENERATED : 16-07-2024 04:04:13 PM		TEXAS INSTRUMENTS



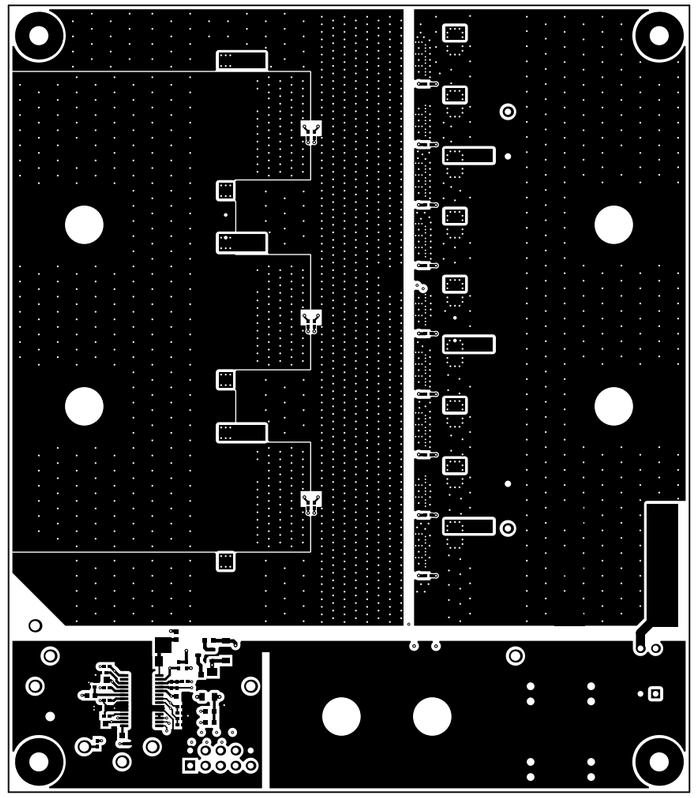
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PMP23496	REV: A	SVN REV: Not in version control
LAYER NAME =			
PLOT NAME = Signal Layer 4	GENERATED : 16-07-2024 04:04:13 PM		TEXAS INSTRUMENTS



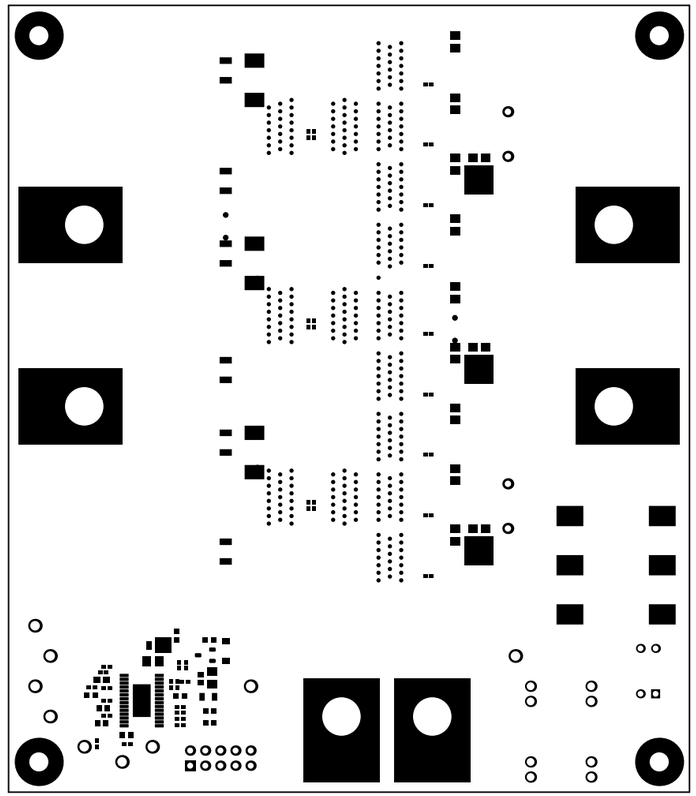
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PMP23496	REV: A	SVN REV: Not in version control
LAYER NAME =			
PLOT NAME = Signal Layer 5	GENERATED : 16-07-2024 04:04:13 PM		TEXAS INSTRUMENTS



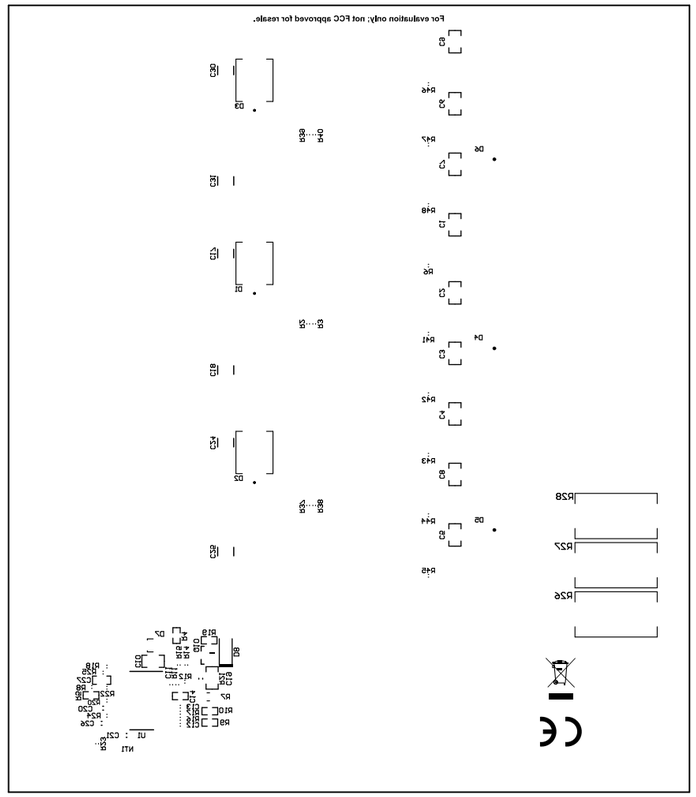
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PMP23496	REV: A	SVN REV: Not in version control
LAYER NAME =			
PLOT NAME = Signal Layer 6	GENERATED : 16-07-2024 04:04:14 PM		TEXAS INSTRUMENTS



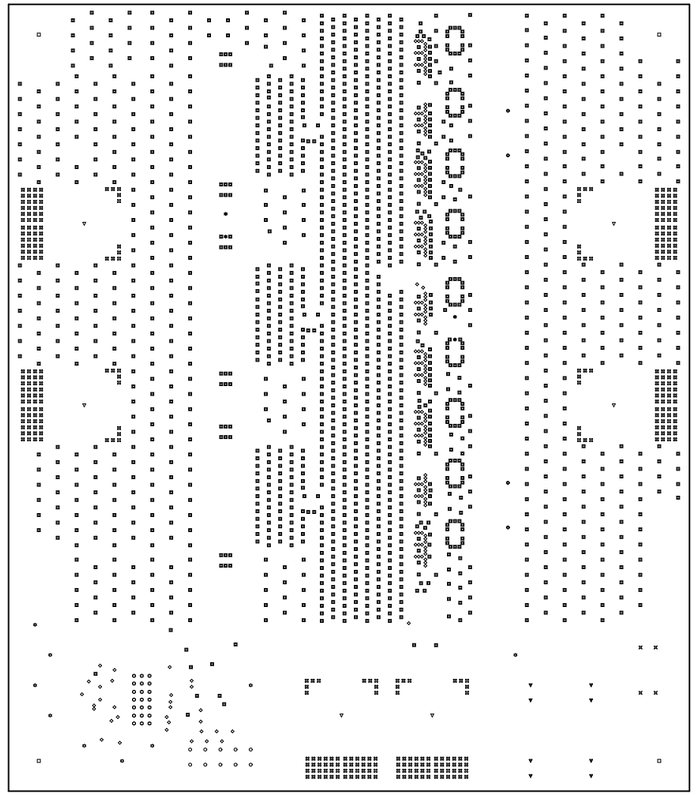
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PMP23496	REV: A	SVN REV: Not in version control
LAYER NAME = Bottom Layer			
PLOT NAME = Bottom Layer	GENERATED : 16-07-2024 04:04:14 PM		TEXAS INSTRUMENTS



ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PMP23496	REV: A	SVN REV: Not in version control
LAYER NAME = Bottom Solder			
PLOT NAME = Bottom Solder Mask	GENERATED : 16-07-2024 04:04:14 PM		TEXAS INSTRUMENTS

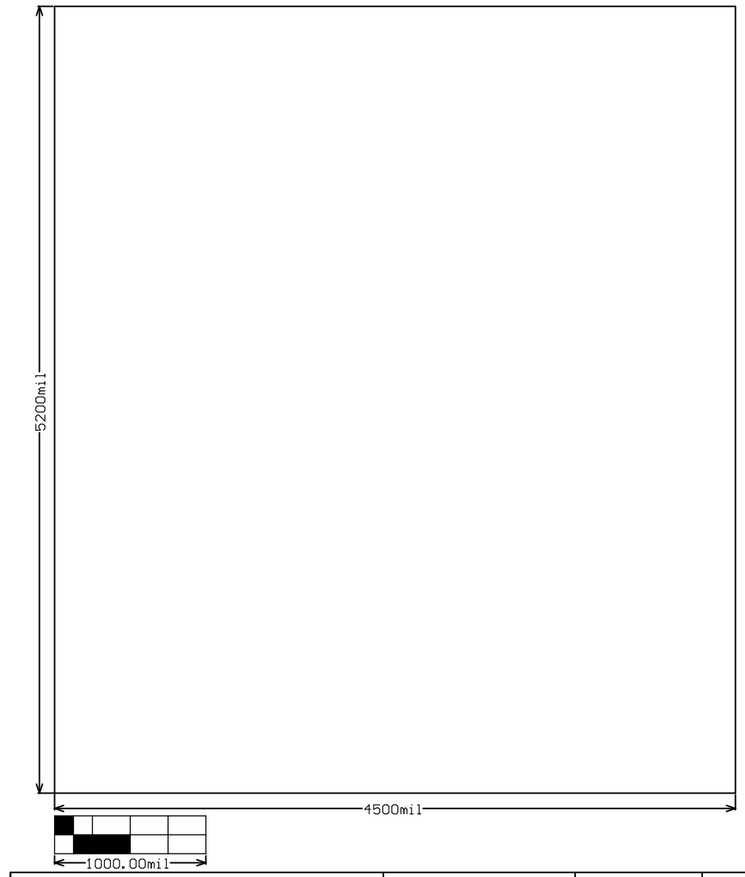


ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PMP23496	REV: A	SVN REV: Not in version control
LAYER NAME = Bottom Overlay			
PLOT NAME = Bottom Overlay	GENERATED : 16-07-2024 04:04:15 PM		TEXAS INSTRUMENTS



Symbol	Quantity	Finished Hole Size	Plated	Hole Type	Drill Layer Pair	Hole Tolerance
	21	7.87mil (0.200mm)	PTH	Round	Top Layer - Bottom Layer	
	187	10.00mil (0.254mm)	PTH	Round	Top Layer - Bottom Layer	
	1840	12.00mil (0.305mm)	PTH	Round	Top Layer - Bottom Layer	
	348	16.00mil (0.406mm)	PTH	Round	Top Layer - Bottom Layer	
	4	24.00mil (0.610mm)	PTH	Round	Top Layer - Bottom Layer	
	4	38.19mil (0.970mm)	PTH	Round	Top Layer - Bottom Layer	
	10	39.37mil (1.000mm)	PTH	Round	Top Layer - Bottom Layer	
	4	42.00mil (1.067mm)	PTH	Round	Top Layer - Bottom Layer	
	8	51.18mil (1.300mm)	PTH	Round	Top Layer - Bottom Layer	
	9	63.00mil (1.600mm)	PTH	Round	Top Layer - Bottom Layer	
	4	125.98mil (3.200mm)	PTH	Round	Top Layer - Bottom Layer	
	6	254.00mil (6.452mm)	PTH	Round	Top Layer - Bottom Layer	
	2445 Total					

ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PMP23496	REV: A	SVN REV: Not in version control
LAYER NAME = Drill Drawing			
PLOT NAME = Drill Drawing	GENERATED : 16-07-2024 04:04:15 PM		TEXAS INSTRUMENTS



ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PMP23496	REV: A	SVN REV: Not in version control
LAYER NAME = M2 Board Dimensions			
PLOT NAME = Board Dimensions	GENERATED : 16-07-2024 04:04:23 PM		TEXAS INSTRUMENTS

IMPORTANT NOTICE AND DISCLAIMER

TI PROVIDES TECHNICAL AND RELIABILITY DATA (INCLUDING DATASHEETS), DESIGN RESOURCES (INCLUDING REFERENCE DESIGNS), APPLICATION OR OTHER DESIGN ADVICE, WEB TOOLS, SAFETY INFORMATION, AND OTHER RESOURCES “AS IS” AND WITH ALL FAULTS, AND DISCLAIMS ALL WARRANTIES, EXPRESS AND IMPLIED, INCLUDING WITHOUT LIMITATION ANY IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE OR NON-INFRINGEMENT OF THIRD PARTY INTELLECTUAL PROPERTY RIGHTS.

These resources are intended for skilled developers designing with TI products. You are solely responsible for (1) selecting the appropriate TI products for your application, (2) designing, validating and testing your application, and (3) ensuring your application meets applicable standards, and any other safety, security, regulatory or other requirements.

These resources are subject to change without notice. TI grants you permission to use these resources only for development of an application that uses the TI products described in the resource. Other reproduction and display of these resources is prohibited. No license is granted to any other TI intellectual property right or to any third party intellectual property right. TI disclaims responsibility for, and you fully indemnify TI and its representatives against any claims, damages, costs, losses, and liabilities arising out of your use of these resources.

TI’s products are provided subject to TI’s Terms of Sale, TI’s General Quality Guidelines, or other applicable terms available either on ti.com or provided in conjunction with such TI products. TI’s provision of these resources does not expand or otherwise alter TI’s applicable warranties or warranty disclaimers for TI products. Unless TI explicitly designates a product as custom or customer-specified, TI products are standard, catalog, general purpose devices.

TI objects to and rejects any additional or different terms you may propose.

Copyright © 2025, Texas Instruments Incorporated

Last updated 10/2025