

COMPONENTS MARKED 'DNP' SHOULD NOT BE POPULATED.  
ASSEMBLY VARIANT: [No Variations]

DESIGN INFORMATION	
MIN. TRACK WIDTH:	8 MIL
MIN. CLEARANCE:	0.2 mm
MIN. VIA PAD SIZE:	24 MIL
MINIMUM ANNULAR RING 0.05mm (2MIL) EXTERNAL PER IPC-D-275 CLASS 2 LEVEL C	
REGISTRATION TOLERANCES: METAL +/- 5 MIL, HOLES +/- 3 MIL HOLE SIZE TOLERANCE (UNLESS OTHERWISE SPECIFIED): +/- 3 MIL	
MATERIAL:	
<input type="checkbox"/> FR-408	<input checked="" type="checkbox"/> FR-4 High Tg
<input type="checkbox"/> OTHER _____	
THICKNESS:	<input checked="" type="checkbox"/> 62 MIL (1.6mm) +/-10%
<input type="checkbox"/> OTHER _____	
TOLERANCE:	<input checked="" type="checkbox"/> ANSI IPC-6012 TYPE 3 CLASS 2
<input type="checkbox"/> OTHER +/- _____	
BOW & TWIST:	<input checked="" type="checkbox"/> ANSI IPC-6012 TYPE 3 CLASS 2
<input type="checkbox"/> OTHER +/- _____	
DRILLING:	
REFERENCE:	<input checked="" type="checkbox"/> AS SHOWN
<input checked="" type="checkbox"/> NC_DRILL FILES	
PTH COPPER THICKNESS:	<input checked="" type="checkbox"/> 20-30 um
<input type="checkbox"/> OTHER _____	
BOARD FINISH:	
SILKSCREEN:	<input checked="" type="checkbox"/> TOP
<input checked="" type="checkbox"/> BOTTOM	
<input type="checkbox"/> OTHER _____	
SILKSCREEN COLOR:	<input checked="" type="checkbox"/> WHITE
<input type="checkbox"/> OTHER _____	
SOLDER RESIST COLOR:	<input checked="" type="checkbox"/> GREEN
<input type="checkbox"/> OTHER _____	
<input checked="" type="checkbox"/> MATTE	
<input type="checkbox"/> SEMI-GLOSS	
SURFACE FINISH:	
<input checked="" type="checkbox"/> IMMERSION GOLD (ENIG)	
<input type="checkbox"/> ENEPIC	
<input type="checkbox"/> IMM. TIN/SILVER OR EQUIV	
<input type="checkbox"/> OTHER _____	
ARRAY/PANEL:	
<input type="checkbox"/> CUT AND TRIM PER M1 BOARD OUTLINE	
<input type="checkbox"/> N.C. ROUTE	
<input checked="" type="checkbox"/> V. SCORE	
CERTIFICATION: MATERIALS AND WORKMANSHIP FOR ALL PCBs TO MEET OR EXCEED THE REQUIREMENTS OF:	
<input checked="" type="checkbox"/> ANSI IPC-A-600F CLASS -> <input type="checkbox"/> 1 <input checked="" type="checkbox"/> 2 <input type="checkbox"/> 3	
<input checked="" type="checkbox"/> RoHS	
<input type="checkbox"/> OTHER PER ORDER	
ALL BOARDS MUST MEET OR EXCEED UL94-V0 REQUIREMENTS. PCB MUST BEAR THE UL94V-0 UL REGISTERED MATERIAL ID NUMBER	
ADDITIONAL REQUIREMENTS:	
MICROSECTION: <input type="checkbox"/> YES	
BARE BOARD ELEC. TEST: <input type="checkbox"/> NONE	
<input checked="" type="checkbox"/> REQUIRED	
<input type="checkbox"/> PER ORDER	
<input type="checkbox"/> XX MIL VIAS REQUIRE NON-CONDUCTIVE FILL AND PLANARIZE	
<input type="checkbox"/> XX MIL VIAS REQUIRE CONDUCTIVE FILL AND PLANARIZE	
<input type="checkbox"/> OUTER XX MIL VIAS REQUIRE 50 OHM SINGLE-ENDED IMPEDANCE	
<input type="checkbox"/> LAYER 2 & 3 (INNER LAYERS) XX MIL WIDE, XX MIL SPACE	
TRACES REQUIRE 100 OHM DIFFERENTIAL IMPEDANCE	



PROJECT TITLE:  
450V-780V Three-level Buck to 390U@1.5A

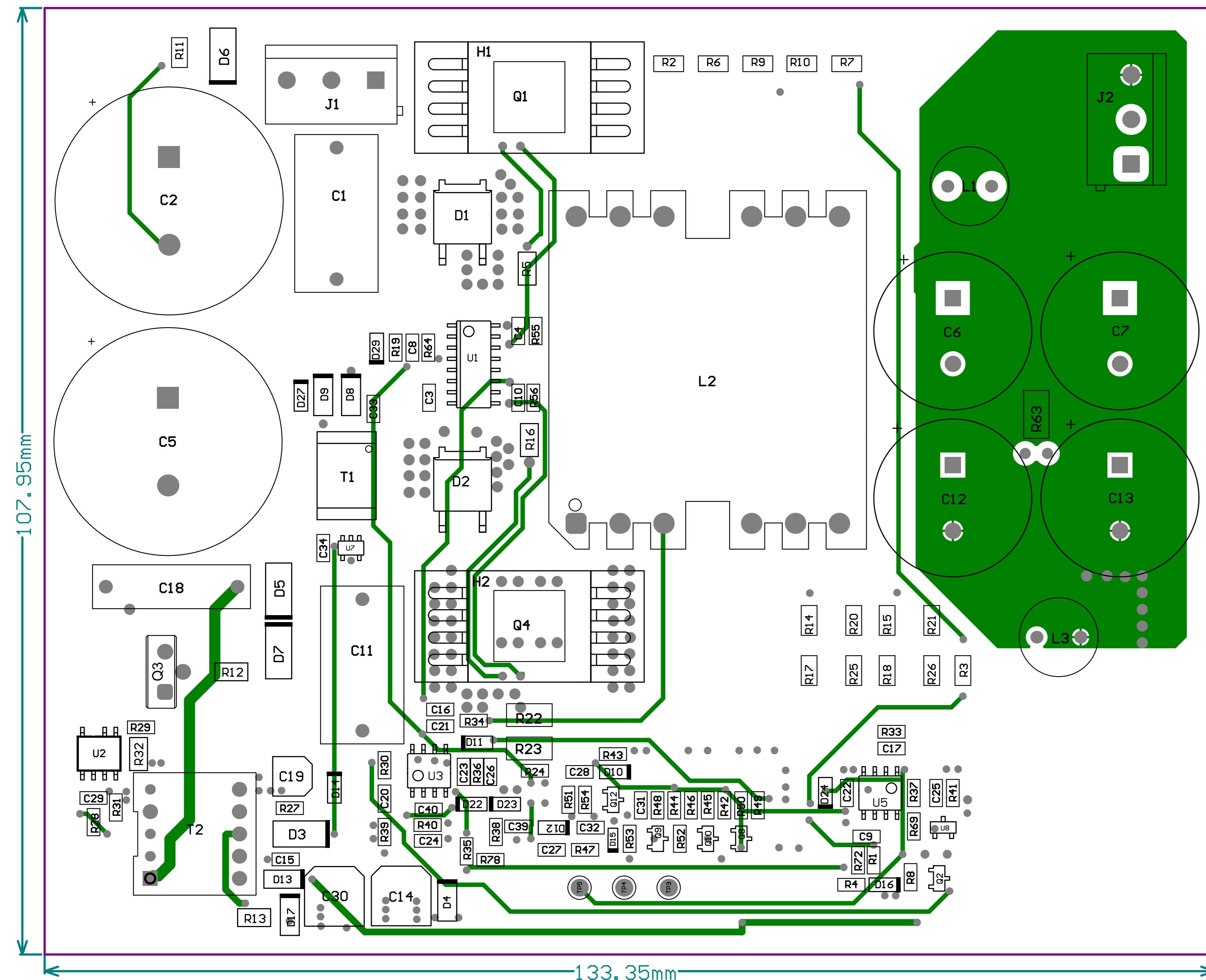
DESIGNED FOR:

FILE NAME:  
PMP30446 Rev\_D PCB.PcbDoc

PCB VIEWED FROM TOP SIDE	BOARD #: PMP30446	REV: D	SUN REV: Not In VersionControl
Plot Name = Top Layer	TID #: .TID	GENERATED : 3/19/2020 5:26:41 PM	TEXAS INSTRUMENTS

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ENGINEER: R. Scibilia	LAYOUT BY: R. Scibilia
SCALE: 1.00	ALTIUM DESIGNER VERSION: 19.1.9.167



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DESIGN INFORMATION	
MIN. TRACK WIDTH:	8 MIL
MIN. CLEARANCE:	0.2 mm
MIN. VIA PAD SIZE:	24 MIL
MINIMUM ANNULAR RING 0.05mm (2MIL) EXTERNAL PER IPC-D-275 CLASS 2 LEVEL C	
REGISTRATION TOLERANCES: METAL +/- 5 MIL, HOLES +/- 3 MIL HOLE SIZE TOLERANCE (UNLESS OTHERWISE SPECIFIED): +/- 3 MIL	
MATERIAL:	
<input type="checkbox"/> FR-408	<input checked="" type="checkbox"/> FR-4 High Tg
<input type="checkbox"/> OTHER _____	
THICKNESS:	<input checked="" type="checkbox"/> 62 MIL (1.6mm) +/-10%
<input type="checkbox"/> OTHER _____	
TOLERANCE:	<input checked="" type="checkbox"/> ANSI IPC-6012 TYPE 3 CLASS 2
<input type="checkbox"/> OTHER +/- _____	
BOW & TWIST:	<input checked="" type="checkbox"/> ANSI IPC-6012 TYPE 3 CLASS 2
<input type="checkbox"/> OTHER +/- _____	
DRILLING:	
REFERENCE:	<input checked="" type="checkbox"/> AS SHOWN <input checked="" type="checkbox"/> NC_DRILL FILES
PTH COPPER THICKNESS:	<input checked="" type="checkbox"/> 20-30 um <input type="checkbox"/> OTHER _____
BOARD FINISH:	
SILKSCREEN:	<input checked="" type="checkbox"/> TOP <input checked="" type="checkbox"/> BOTTOM
<input type="checkbox"/> OTHER _____	
SILKSCREEN COLOR:	<input checked="" type="checkbox"/> WHITE <input type="checkbox"/> OTHER _____
<input type="checkbox"/> OTHER +/- _____	
SOLDER RESIST COLOR:	<input checked="" type="checkbox"/> GREEN <input type="checkbox"/> OTHER _____
<input checked="" type="checkbox"/> MATTE <input type="checkbox"/> SEMI-GLOSS	
SURFACE FINISH:	
<input checked="" type="checkbox"/> IMMERSION GOLD (ENIG)	<input type="checkbox"/> ENEPIG
<input type="checkbox"/> IMM. TIN/SILVER OR EQUIV <input type="checkbox"/> OTHER _____	
ARRAY/PANEL:	
<input type="checkbox"/> CUT AND TRIM PER M1 BOARD OUTLINE	<input checked="" type="checkbox"/> V. SCORE
<input type="checkbox"/> N.C. ROUTE	
CERTIFICATION: MATERIALS AND WORKMANSHIP FOR ALL PCBs TO MEET OR EXCEED THE REQUIREMENTS OF:	
<input checked="" type="checkbox"/> ANSI IPC-A-600F CLASS ->	<input type="checkbox"/> 1 <input checked="" type="checkbox"/> 2 <input type="checkbox"/> 3
<input checked="" type="checkbox"/> RoHS <input type="checkbox"/> OTHER PER ORDER	
ALL BOARDS MUST MEET OR EXCEED UL94-V0 REQUIREMENTS. PCB MUST BEAR THE UL94V-0 UL REGISTERED MATERIAL ID NUMBER	
ADDITIONAL REQUIREMENTS:	
MICROSECTION: <input type="checkbox"/> YES	
BARE BOARD ELEC. TEST: <input type="checkbox"/> NONE <input checked="" type="checkbox"/> REQUIRED <input type="checkbox"/> PER ORDER	
<input type="checkbox"/> XX MIL VIAS REQUIRE NON-CONDUCTIVE FILL AND PLANARIZE	
<input type="checkbox"/> XX MIL VIAS REQUIRE CONDUCTIVE FILL AND PLANARIZE	
<input type="checkbox"/> OUTER XX MIL VIAS REQUIRE 50 OHM SINGLE-ENDED IMPEDANCE	
<input type="checkbox"/> LAYER 2 & 3 (INNER LAYERS) XX MIL WIDE, XX MIL SPACE	
TRACES REQUIRE 100 OHM DIFFERENTIAL IMPEDANCE	



PROJECT TITLE:  
 450V-780V Three-level Buck to 390U@1.5A

DESIGNED FOR:

FILE NAME:  
 PMP30446 Rev\_D PCB.PcbDoc

PCB VIEWED FROM TOP SIDE	BOARD #: PMP30446	REV: D	SUN REV: Not In VersionControl
MPSI Board Dimensions	TID #: .TID		
PLOT NAME = Signal Layer 1	GENERATED : 3/19/2020 5:26:41 PM	TEXAS INSTRUMENTS	

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SCALE: 1.00	ALTIUM DESIGNER VERSION: 19.1.9.167

DESIGN INFORMATION	
MIN. TRACK WIDTH:	8 MIL
MIN. CLEARANCE:	0.2 mm
MIN. VIA PAD SIZE:	24 MIL
MINIMUM ANNULAR RING 0.05mm (2MIL) EXTERNAL PER IPC-D-275 CLASS 2 LEVEL C	
REGISTRATION TOLERANCES: METAL +/- 5 MIL, HOLES +/- 3 MIL	
HOLE SIZE TOLERANCE (UNLESS OTHERWISE SPECIFIED): +/- 3 MIL	
MATERIAL:	
<input type="checkbox"/> FR-408	<input checked="" type="checkbox"/> FR-4 High Tg
<input type="checkbox"/> OTHER _____	
THICKNESS:	<input checked="" type="checkbox"/> 62 MIL (1.6mm) +/-10%
<input type="checkbox"/> OTHER _____	
TOLERANCE:	<input checked="" type="checkbox"/> ANSI IPC-6012 TYPE 3 CLASS 2
<input type="checkbox"/> OTHER +/- _____	
BOW & TWIST:	<input checked="" type="checkbox"/> ANSI IPC-6012 TYPE 3 CLASS 2
<input type="checkbox"/> OTHER +/- _____	
DRILLING:	
REFERENCE:	<input checked="" type="checkbox"/> AS SHOWN
<input checked="" type="checkbox"/> NC_DRILL FILES	
PTH COPPER THICKNESS:	<input checked="" type="checkbox"/> 20-30 um
<input type="checkbox"/> OTHER _____	
BOARD FINISH:	
SILKSCREEN:	<input checked="" type="checkbox"/> TOP
<input checked="" type="checkbox"/> BOTTOM	
SILKSCREEN COLOR:	<input checked="" type="checkbox"/> WHITE
<input type="checkbox"/> OTHER _____	
SOLDER RESIST COLOR:	<input checked="" type="checkbox"/> GREEN
<input type="checkbox"/> OTHER _____	
<input checked="" type="checkbox"/> MATTE	
<input type="checkbox"/> SEMI-GLOSS	
SURFACE FINISH:	<input checked="" type="checkbox"/> IMMERSION GOLD (ENIG)
<input type="checkbox"/> ENEPIG	
<input type="checkbox"/> IMM. TIN/SILVER OR EQUIV	
<input type="checkbox"/> OTHER _____	
ARRAY/PANEL:	<input type="checkbox"/> CUT AND TRIM PER M1 BOARD OUTLINE
<input type="checkbox"/> N.C. ROUTE	
<input checked="" type="checkbox"/> V. SCORE	
CERTIFICATION: MATERIALS AND WORKMANSHIP FOR ALL PCBs TO MEET OR EXCEED THE REQUIREMENTS OF:	
<input checked="" type="checkbox"/> ANSI IPC-A-600F CLASS -> <input type="checkbox"/> 1	
<input checked="" type="checkbox"/> 2	
<input type="checkbox"/> 3	
<input checked="" type="checkbox"/> RoHS	
<input type="checkbox"/> OTHER PER ORDER	
ALL BOARDS MUST MEET OR EXCEED UL94-V0 REQUIREMENTS.	
PCB MUST BEAR THE UL94V-0 UL REGISTERED MATERIAL ID NUMBER	
ADDITIONAL REQUIREMENTS:	
MICROSECTION:	<input type="checkbox"/> YES
BARE BOARD ELEC. TEST:	<input type="checkbox"/> NONE
<input checked="" type="checkbox"/> REQUIRED	
<input type="checkbox"/> PER ORDER	
<input type="checkbox"/> XX MIL VIAS REQUIRE NON-CONDUCTIVE FILL AND PLANARIZE	
<input type="checkbox"/> XX MIL VIAS REQUIRE CONDUCTIVE FILL AND PLANARIZE	
<input type="checkbox"/> OUTER XX MIL VIAS REQUIRE 50 OHM SINGLE-ENDED IMPEDANCE	
<input type="checkbox"/> LAYER 2 & 3 (INNER LAYERS) XX MIL WIDE, XX MIL SPACE	
<input type="checkbox"/> TRACES REQUIRE 100 OHM DIFFERENTIAL IMPEDANCE	



PROJECT TITLE:  
450V-780V Three-level Buck to 390V@1.5A

DESIGNED FOR:

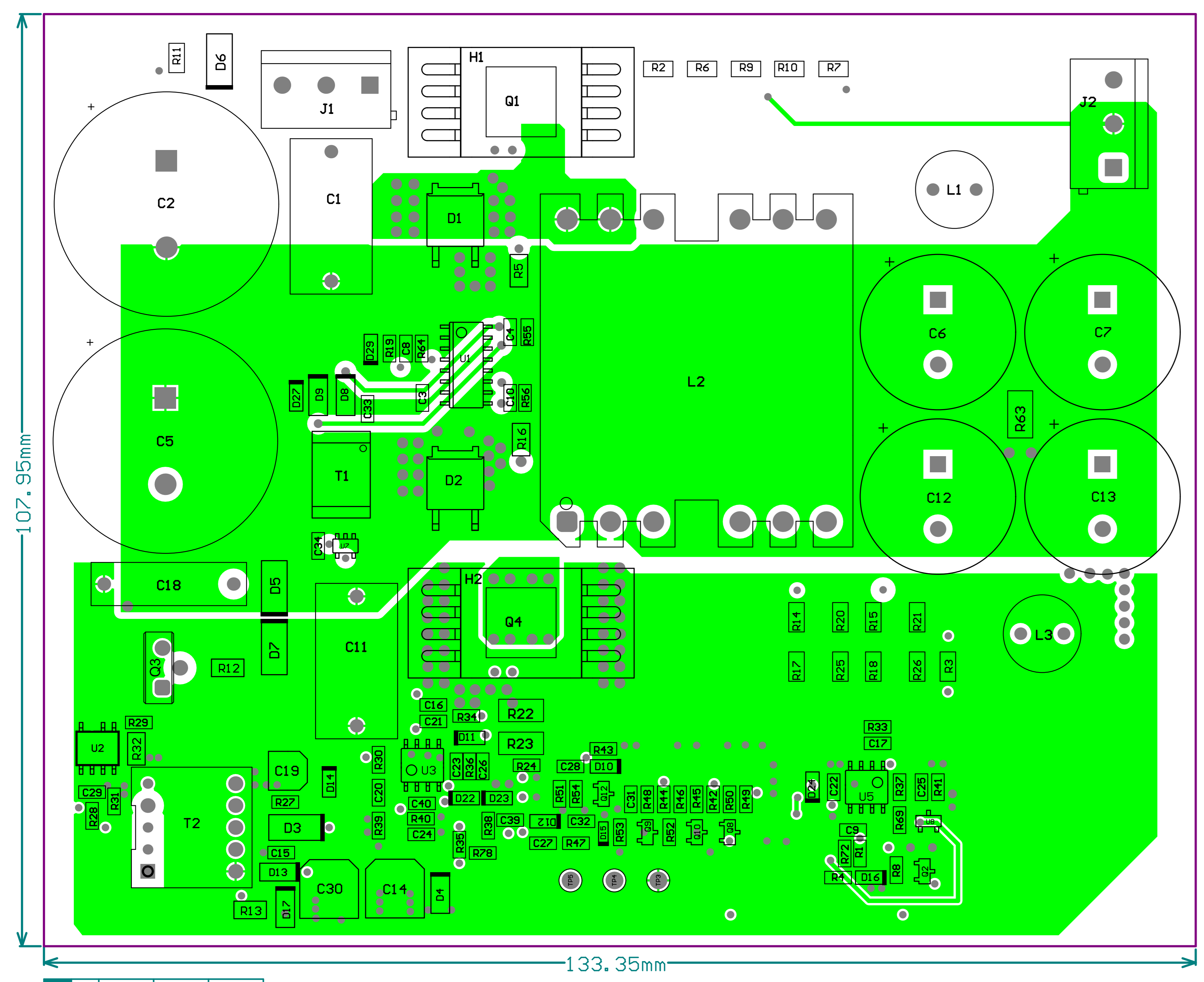
FILE NAME:  
PMP30446 Rev\_D PCB.PcbDoc

ENGINEER:  
R. Scibilia

LAYOUT BY:  
R. Scibilia

SCALE: 1.00

ALTIUM DESIGNER VERSION:  
19.1.9.167

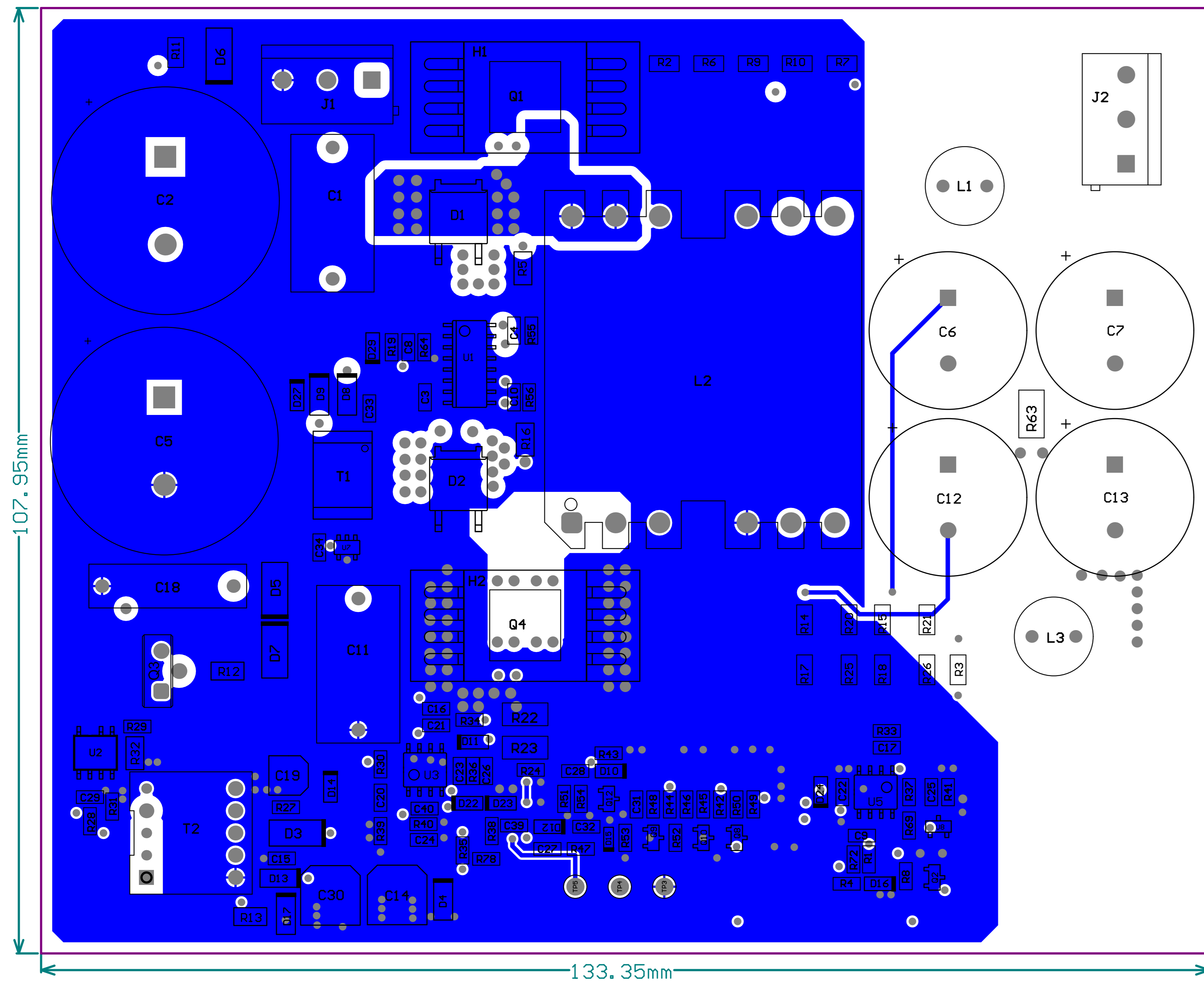


COMPONENTS MARKED 'DNP' SHOULD NOT BE POPULATED.  
ASSEMBLY VARIANT: [No Variations]

PCB VIEWED FROM TOP SIDE	BOARD #: PMP30446	REV: D	SUN REV: Not In VersionControl
MPSI Board Dimensions	TID #: .TID		
PLOT NAME = Signal Layer 2	GENERATED : 3/19/2020 5:26:41 PM	TEXAS INSTRUMENTS	

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SCALE: 1.00	ALTIUM DESIGNER VERSION: 19.1.9.167



COMPONENTS MARKED 'DNP' SHOULD NOT BE POPULATED.  
 ASSEMBLY VARIANT: [No Variations]

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MIN. VIA PAD SIZE:	24 MIL
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REGISTRATION TOLERANCES: METAL +/- 5 MIL, HOLES +/- 3 MIL HOLE SIZE TOLERANCE (UNLESS OTHERWISE SPECIFIED): +/- 3 MIL	
MATERIAL:	
<input type="checkbox"/> FR-408	<input checked="" type="checkbox"/> FR-4 High Tg <input type="checkbox"/> OTHER _____
THICKNESS:	<input checked="" type="checkbox"/> 62 MIL (1.6mm) +/-10% <input type="checkbox"/> OTHER _____
TOLERANCE:	<input checked="" type="checkbox"/> ANSI IPC-6012 TYPE 3 CLASS 2 <input type="checkbox"/> OTHER +/- _____
BOW & TWIST:	<input checked="" type="checkbox"/> ANSI IPC-6012 TYPE 3 CLASS 2 <input type="checkbox"/> OTHER +/- _____
DRILLING:	
REFERENCE:	<input checked="" type="checkbox"/> AS SHOWN <input checked="" type="checkbox"/> NC_DRILL FILES
PTH COPPER THICKNESS:	<input checked="" type="checkbox"/> 20-30 um <input type="checkbox"/> OTHER _____
BOARD FINISH:	
SILKSCREEN:	<input checked="" type="checkbox"/> TOP <input checked="" type="checkbox"/> BOTTOM
SILKSCREEN COLOR:	<input checked="" type="checkbox"/> WHITE <input type="checkbox"/> OTHER _____
SOLDER RESIST COLOR:	<input checked="" type="checkbox"/> GREEN <input type="checkbox"/> OTHER _____ <input checked="" type="checkbox"/> MATTE <input type="checkbox"/> SEMI-GLOSS
SURFACE FINISH:	<input checked="" type="checkbox"/> IMMERSION GOLD (ENIG) <input type="checkbox"/> ENEPIG <input type="checkbox"/> IMM. TIN/SILVER OR EQUIV <input type="checkbox"/> OTHER _____
ARRAY/PANEL:	<input type="checkbox"/> CUT AND TRIM PER M1 BOARD OUTLINE <input type="checkbox"/> N.C. ROUTE <input checked="" type="checkbox"/> V. SCORE
CERTIFICATION: MATERIALS AND WORKMANSHIP FOR ALL PCBs TO MEET OR EXCEED THE REQUIREMENTS OF:	
<input checked="" type="checkbox"/> ANSI IPC-A-600F CLASS -> <input type="checkbox"/> 1 <input checked="" type="checkbox"/> 2 <input type="checkbox"/> 3 <input checked="" type="checkbox"/> RoHS <input type="checkbox"/> OTHER PER ORDER	
ALL BOARDS MUST MEET OR EXCEED UL94-V0 REQUIREMENTS. PCB MUST BEAR THE UL94V-0 UL REGISTERED MATERIAL ID NUMBER	
ADDITIONAL REQUIREMENTS:	
MICROSECTION:	<input type="checkbox"/> YES
BARE BOARD ELEC. TEST:	<input type="checkbox"/> NONE <input checked="" type="checkbox"/> REQUIRED <input type="checkbox"/> PER ORDER
<input type="checkbox"/> XX MIL VIAS REQUIRE NON-CONDUCTIVE FILL AND PLANARIZE	
<input type="checkbox"/> XX MIL VIAS REQUIRE CONDUCTIVE FILL AND PLANARIZE	
<input type="checkbox"/> OUTER XX MIL VIAS REQUIRE 50 OHM SINGLE-ENDED IMPEDANCE	
<input type="checkbox"/> LAYER 2 & 3 (INNER LAYERS) XX MIL WIDE, XX MIL SPACE TRACES REQUIRE 100 OHM DIFFERENTIAL IMPEDANCE	



PROJECT TITLE:  
450V-780V Three-level Buck to 390U@1.5A

DESIGNED FOR:

FILE NAME:  
PMP30446 Rev\_D PCB.PcbDoc

PCB VIEWED FROM TOP SIDE	BOARD #: PMP30446	REV: D	SUN REV: Not In VersionControl
800mil Dimensions	TID #: .TID		
PLOT NAME = Bottom Layer	GENERATED : 3/19/2020 5:26:41 PM	TEXAS INSTRUMENTS	

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