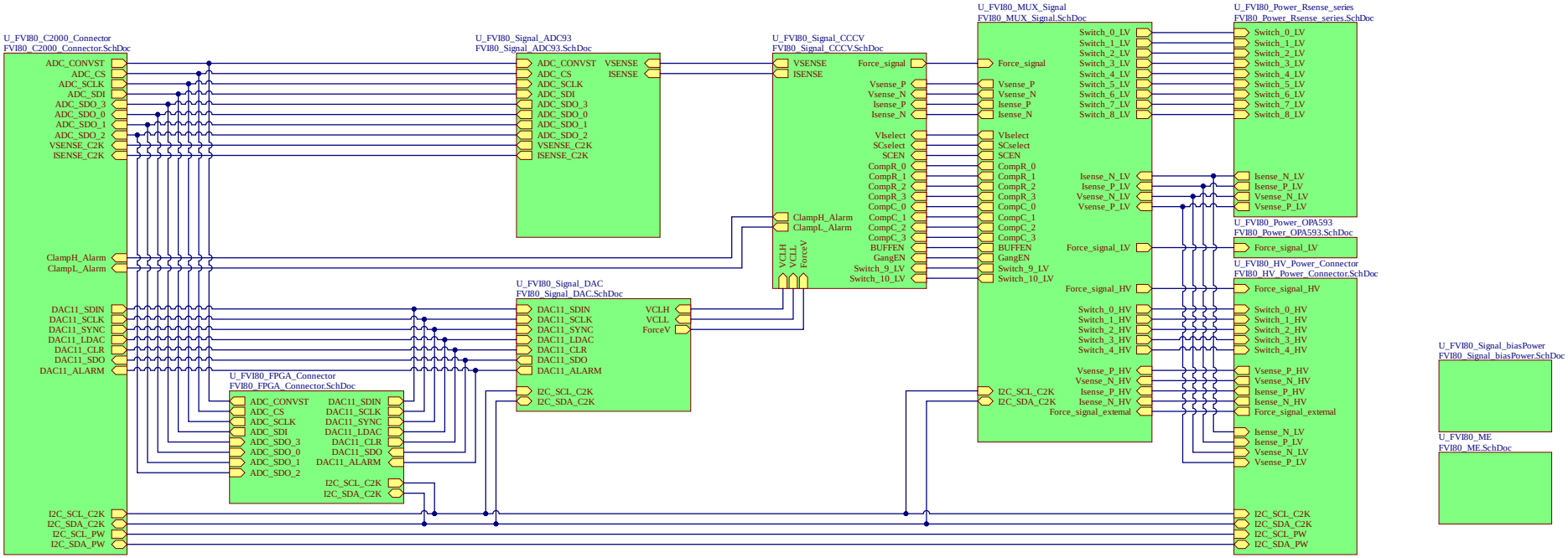

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Orderable: ChangeMe in variant	Designed for: Public Release	Mod. Date: 8/25/2025
TID #: TIDA-010962	Project Title: FVI80 for ATE	
Number: INDSYS-3257 Rev: B	Sheet Title:	
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 1 of 12
Drawn By:	File: FVI80_Top.SchDoc	Size: B
Engineer:	Contact: http://www.ti.com/support	

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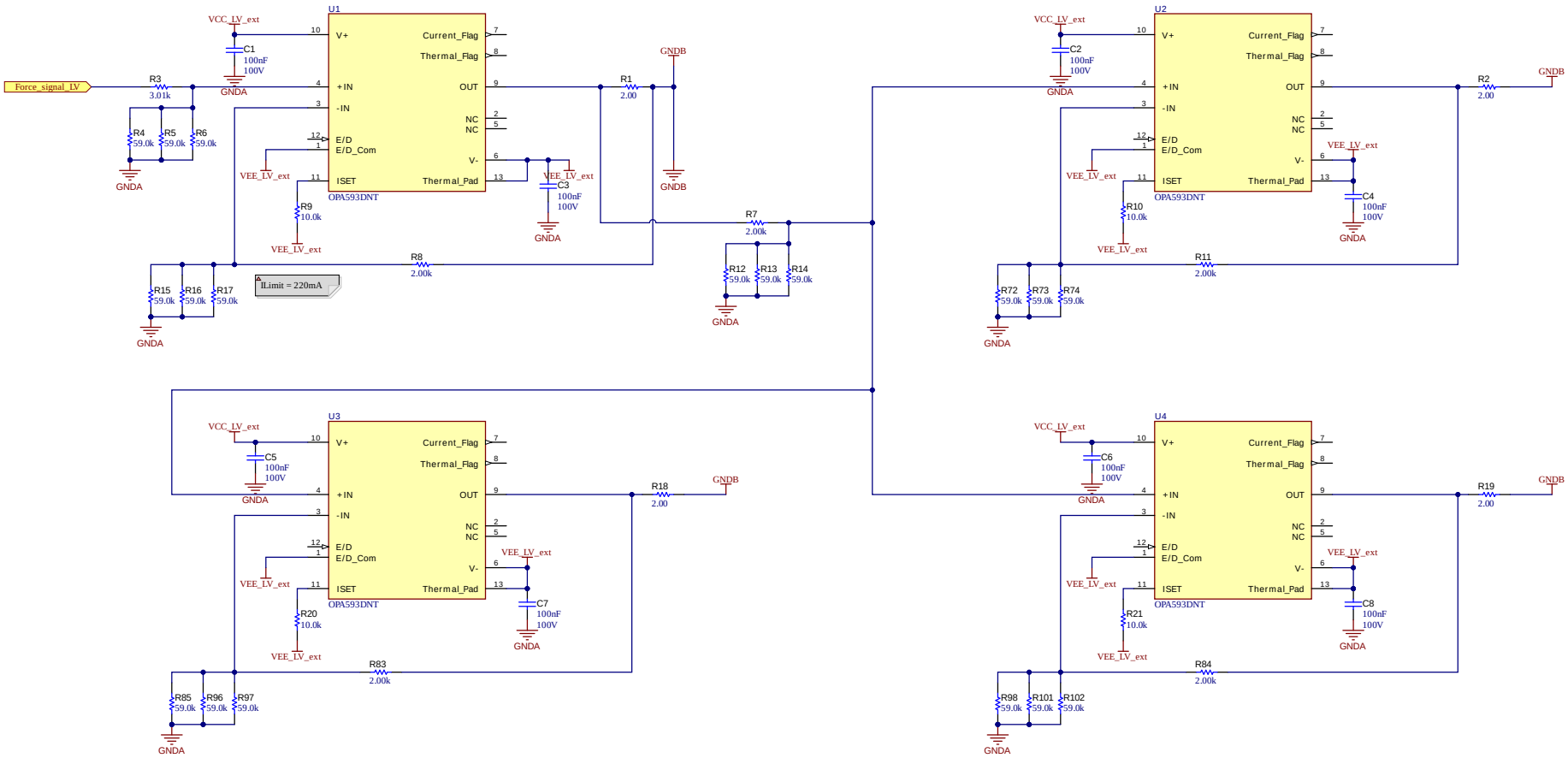

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Number: INDSYS-3257 Rev: B	Sheet Title:		
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 2 of 12	
Drawn By:	File: FVI80_Power_OPA593.SchDoc	Size: B	
Engineer:	Contact: http://www.ti.com/support		

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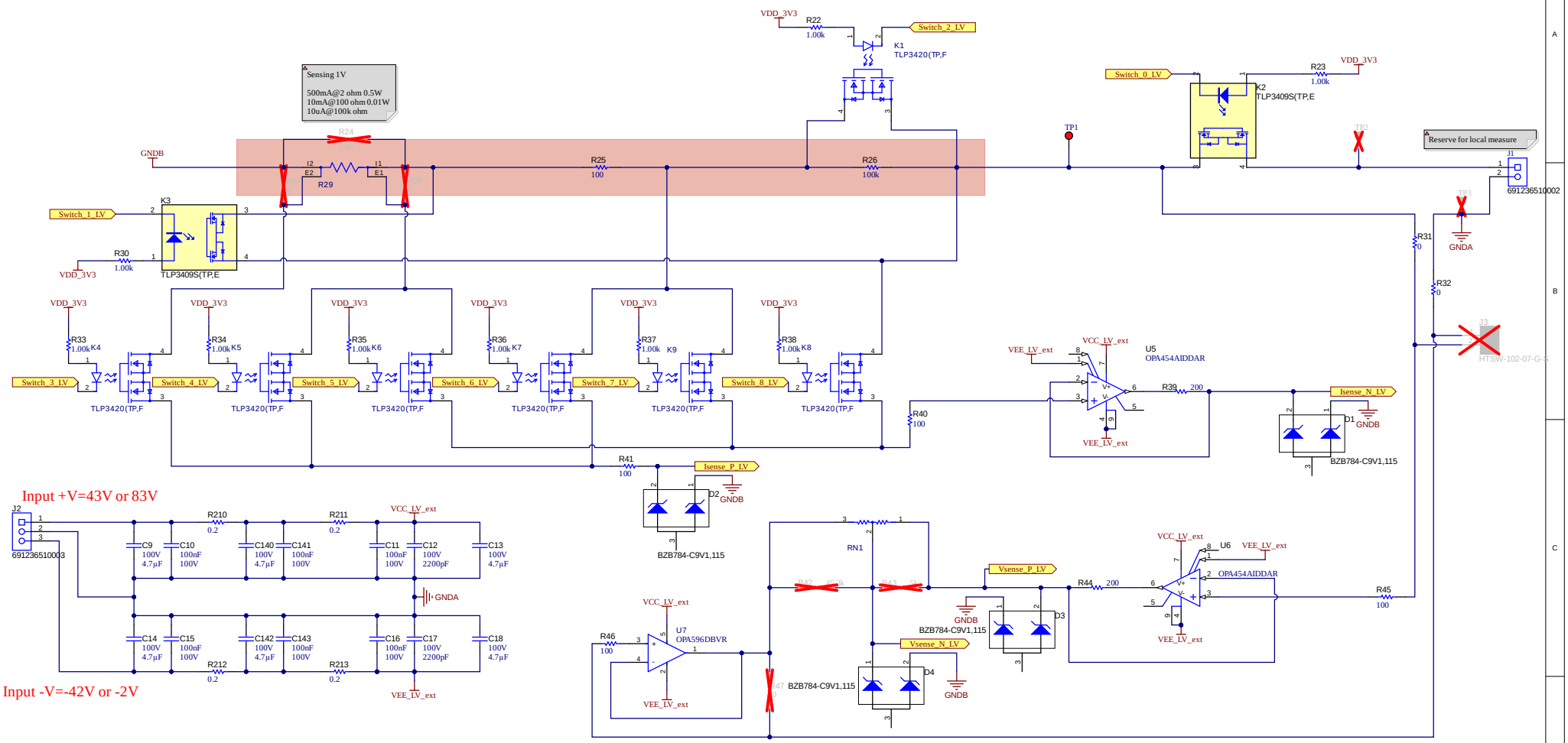

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Orderable: ChangeMe in variant	Designed for: Public Release	Mod. Date: 12/29/2025	
TID #: TIDA-010962	Project Title: FVI80 for ATE		
Number: INDSYS-3257 Rev: B	Sheet Title:		
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 3 of 12	
Drawn By:	File: FVI80_Power_Rsense_series.SchDoc	Size: B	
Engineer:	Contact: http://www.ti.com/support		

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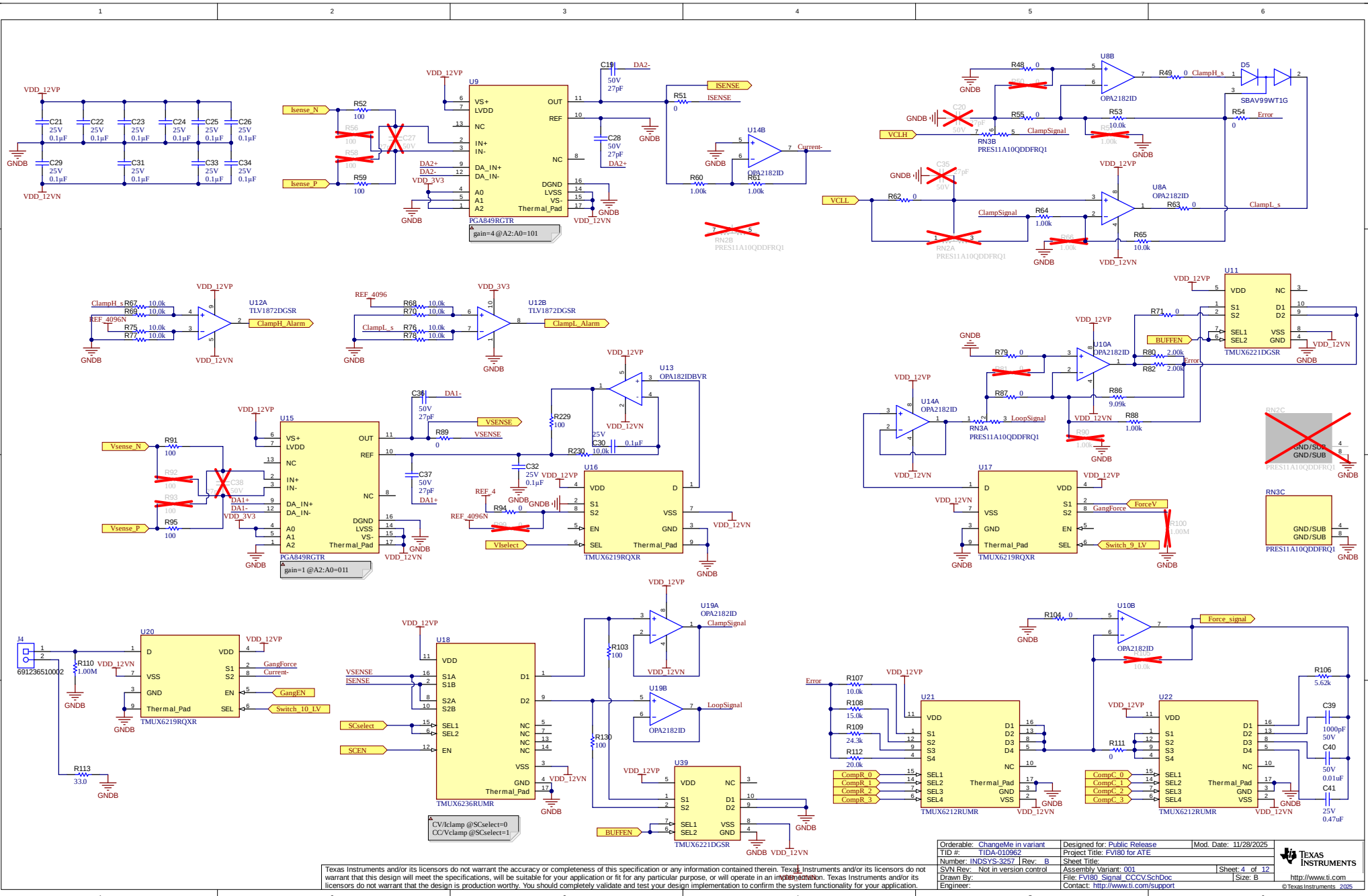

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Number: INDSYS-3257 Rev: B	Sheet Title:	
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 4 of 12
Drawn By:	File: FVI80_Signal_CCCV.SchDoc	Size: B
Engineer:	Contact: http://www.ti.com/support	

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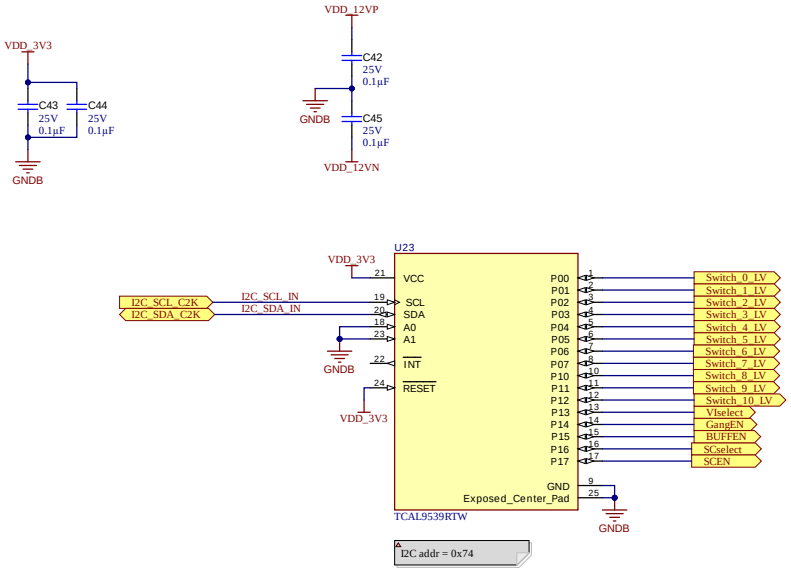

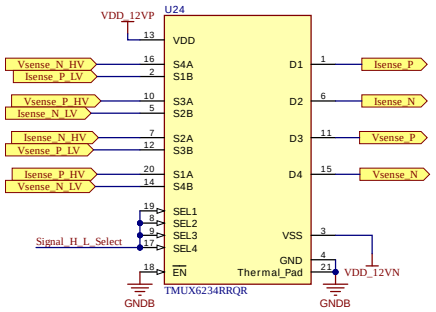

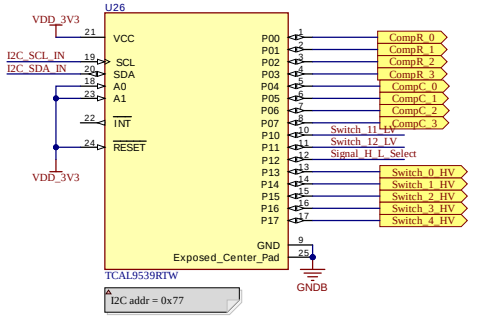

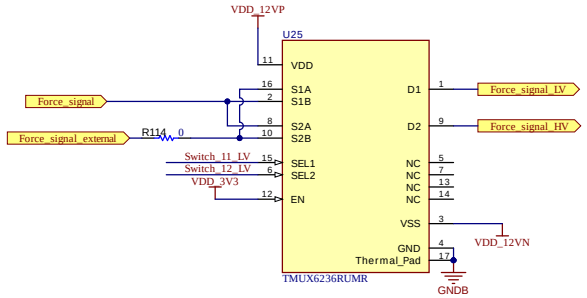

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Orderable: ChangeMe in variant	Designed for: Public Release	Mod. Date: 9/8/2025	
TID #: TIDA-010962	Project Title: FVI80 for ATE		
Number: INDSYS-3257 Rev: B	Sheet Title:		
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 5 of 12	
Drawn By:	File: FVI80_MUX_Signal.SchDoc	Size: B	
Engineer:	Contact: http://www.ti.com/support		http://www.ti.com

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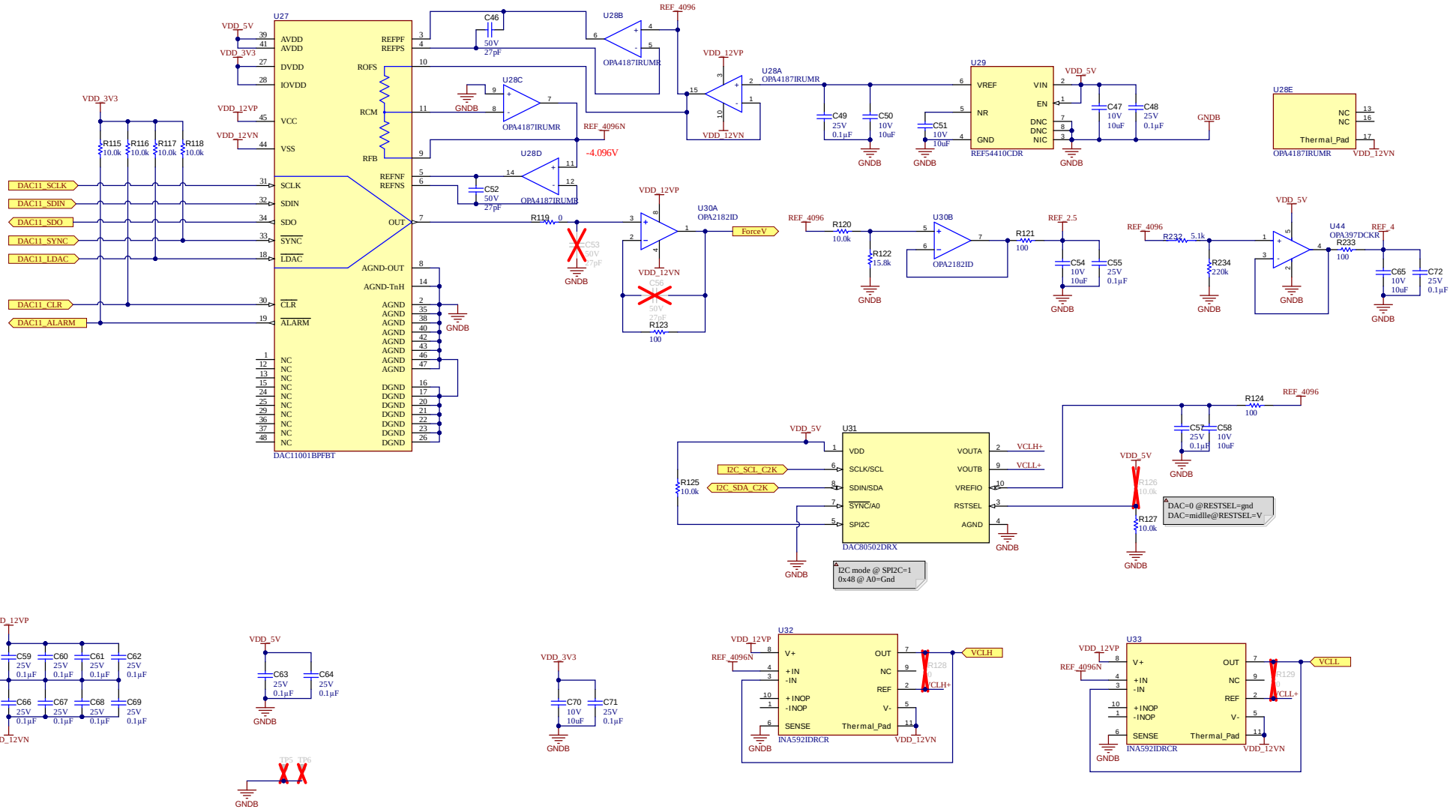

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Orderable: ChangeMe in variant	Designed for: Public Release	Mod. Date: 12/29/2025
TID #: TIDA-010962	Project Title: FVI80 for ATE	
Number: INDSYS-3257 Rev: B	Sheet Title:	
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 6 of 12
Drawn By:	File: FVI80_Signal_DAC.SchDoc	Size: B
Engineer:	Contact: http://www.ti.com/support	

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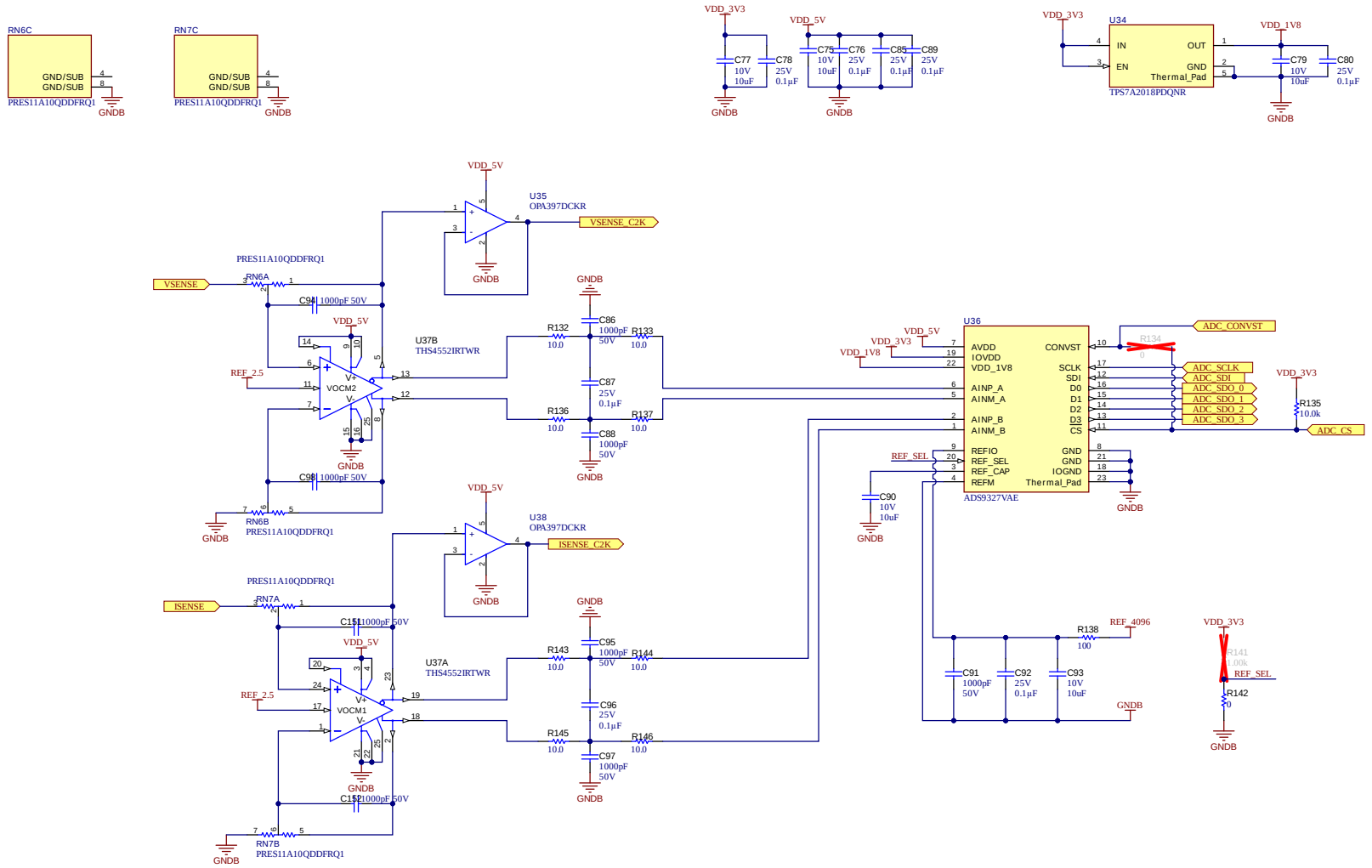

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Number: INDSYS-3257 Rev: B	Sheet Title:		
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 7 of 12	
Drawn By:	File: FVI80_Signal_ADC93.SchDoc	Size: B	
Engineer:	Contact: http://www.ti.com/support		

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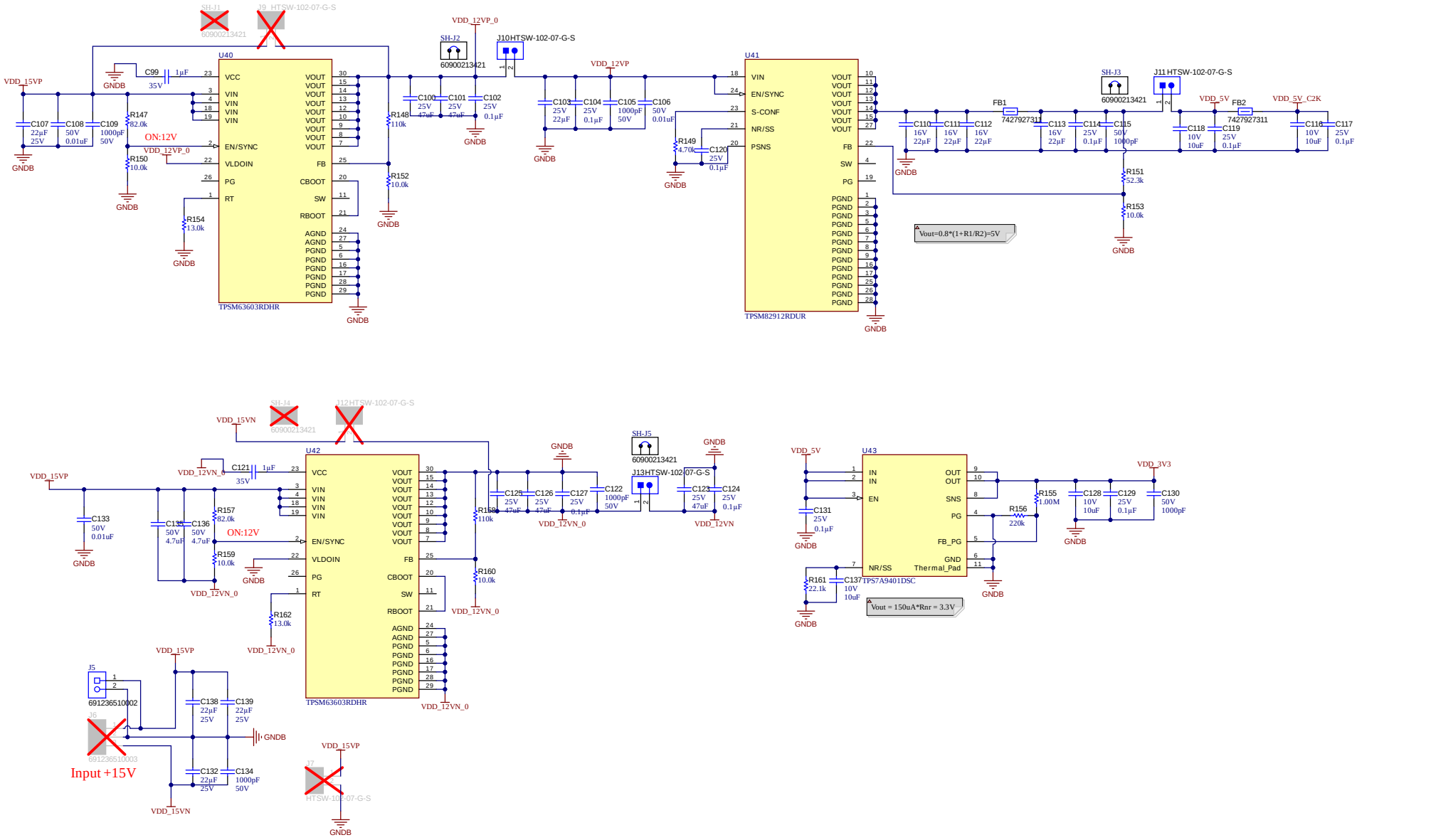

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TID #: TIDA-010962	Project Title: FVI80 for ATE	
Number: INDSYS-3257 Rev: B	Sheet Title:	
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 8 of 12
Drawn By:	File: FVI80_Signal_biasPower.SchDoc	Size: B
Engineer:	Contact: http://www.ti.com/support	

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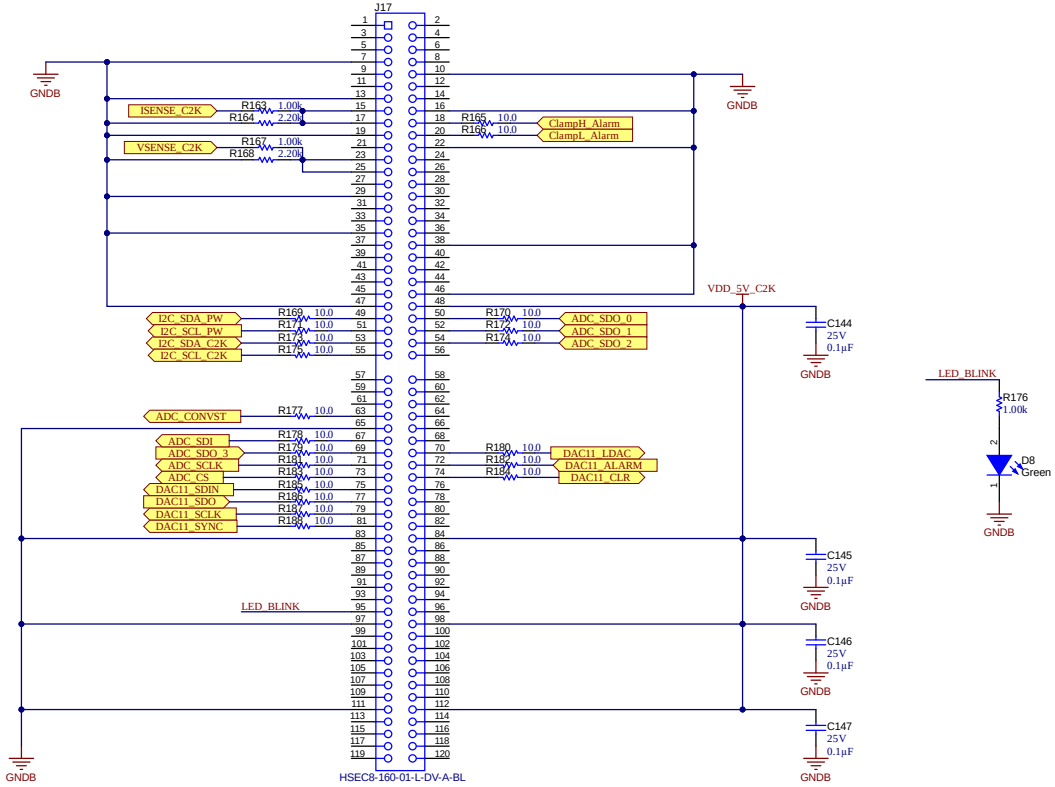
CONTROL CARD PIN MAPPING



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Orderable: ChangeMe in variant	Designed for: Public Release	Mod. Date: 8/25/2025
TID #: TIDA-010962	Project Title: FVI80 for ATE	
Number: INDSYS-3257 Rev: B	Sheet Title:	
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 9 of 12
Drawn By:	File: FVI80_C2000_Connector.SchDoc	Size: B
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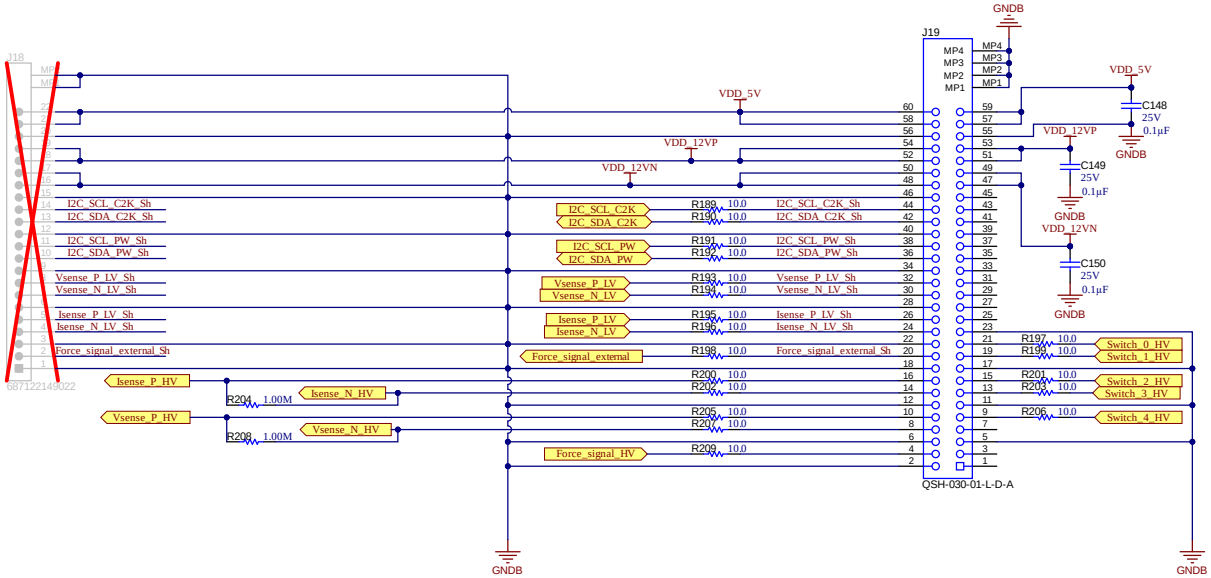
H1 NY PMS 440 0025 PH

H2 NY PMS 440 0025 PH

H3 NY PMS 440 0025 PH

Extend Digital board or Boost board connector



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Orderable: ChangeMe in variant	Designed for: Public Release	Mod. Date: 8/26/2025
TID #: TIDA-010962	Project Title: FVI80 for ATE	
Number: INDSYS-3257 Rev: B	Sheet Title:	
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 10 of 12
Drawn By:	File: FVI80_HV_Power_Connector.SchDoc	Size: B
Engineer:	Contact: http://www.ti.com/support	

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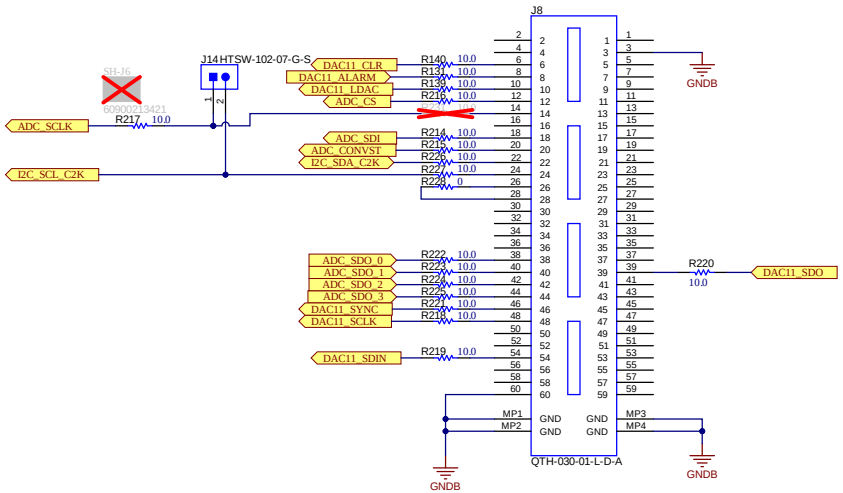

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Orderable: ChangeMe in variant	Designed for: Public Release	Mod. Date: 11/28/2025
TID #: TIDA-010962	Project Title: FVI80 for ATE	
Number: INDSYS-3257 Rev: B	Sheet Title:	
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 10 of 12
Drawn By:	File: FVI80_FPGA_Connector.SchDoc	Size: B
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FID4 FID5 FID6 FID1 FID2 FID3

PCB Number: INDSYS-3257
PCB Rev: B

PCB LOGO
Texas Instruments

CE Mark

PCB LOGO
FCC disclaimer

PCB LOGO
WEEE logo

LBL1
PCB Label
THT-14-423-10
Size: 0.65" x 0.20 "

Variant/Label Table	
Variant	Label Text
001	ChangeMe!
002	ChangeMe!

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Orderable: ChangeMe in variant	Designed for: Public Release	Mod. Date: 7/17/2025	
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Number: INDSYS-3257 Rev: B	Sheet Title:		
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 11 of 12	
Drawn By:	File: FVI80_ME.SchDoc	Size: B	
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Board Stack Report

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