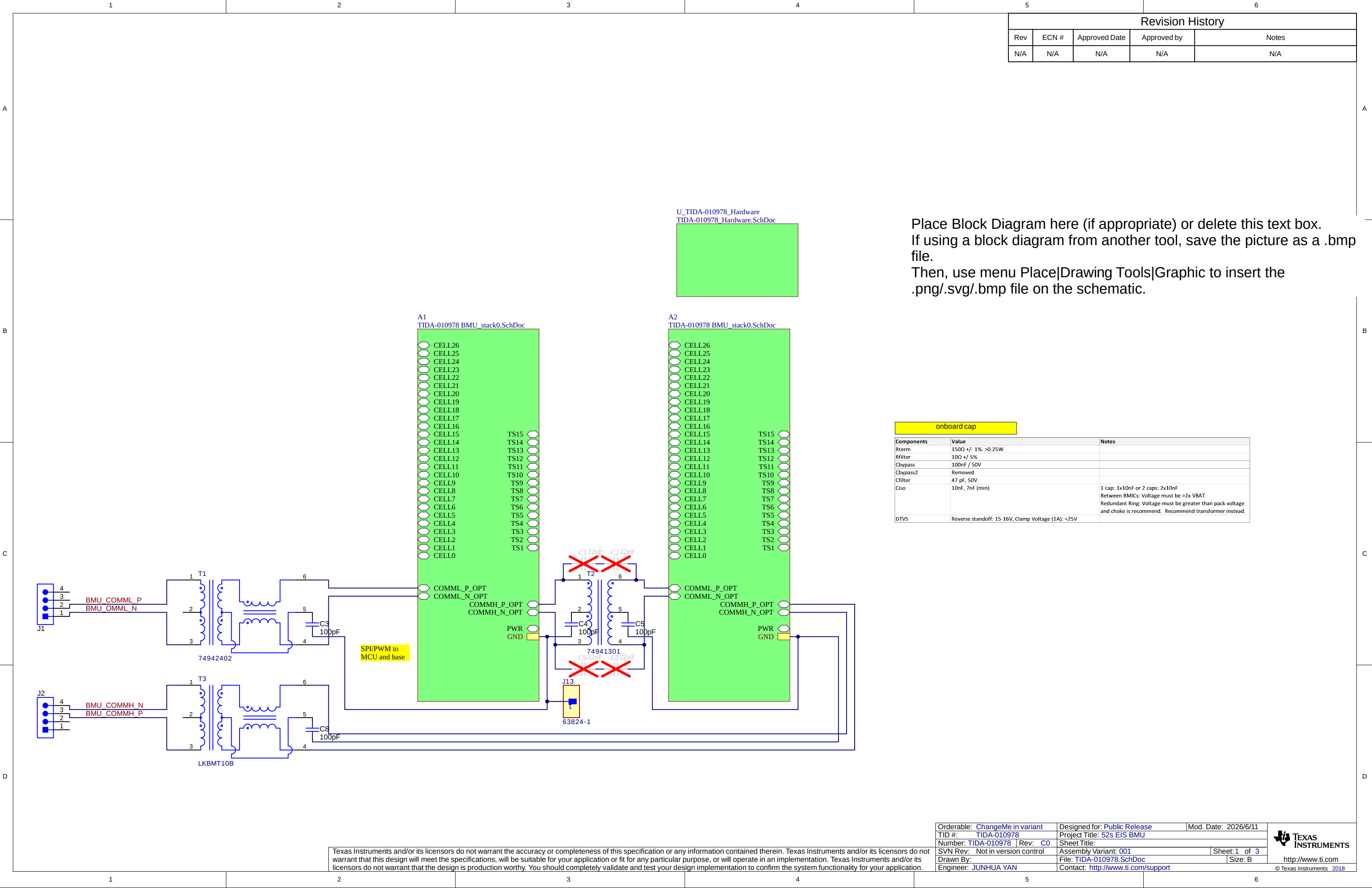
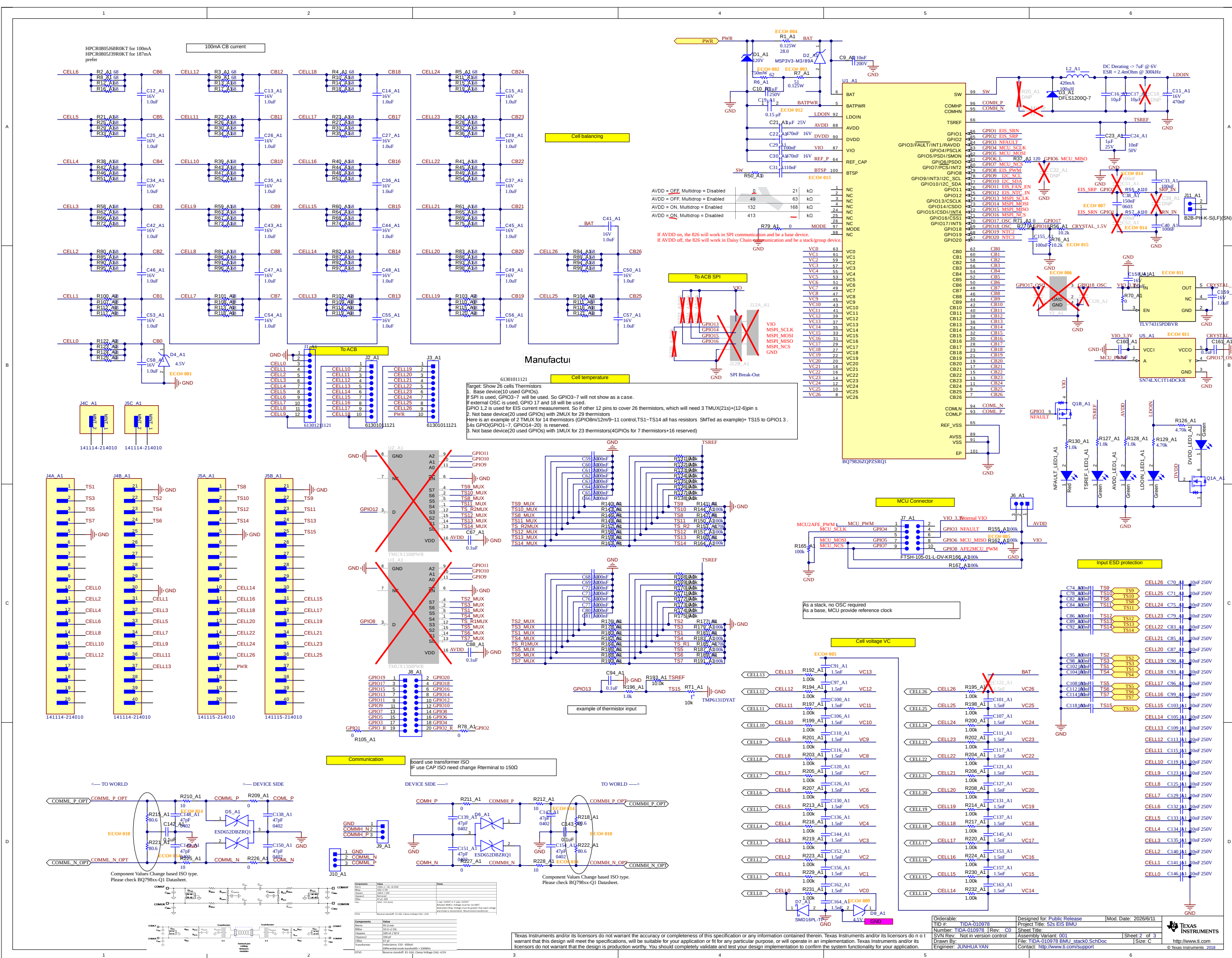
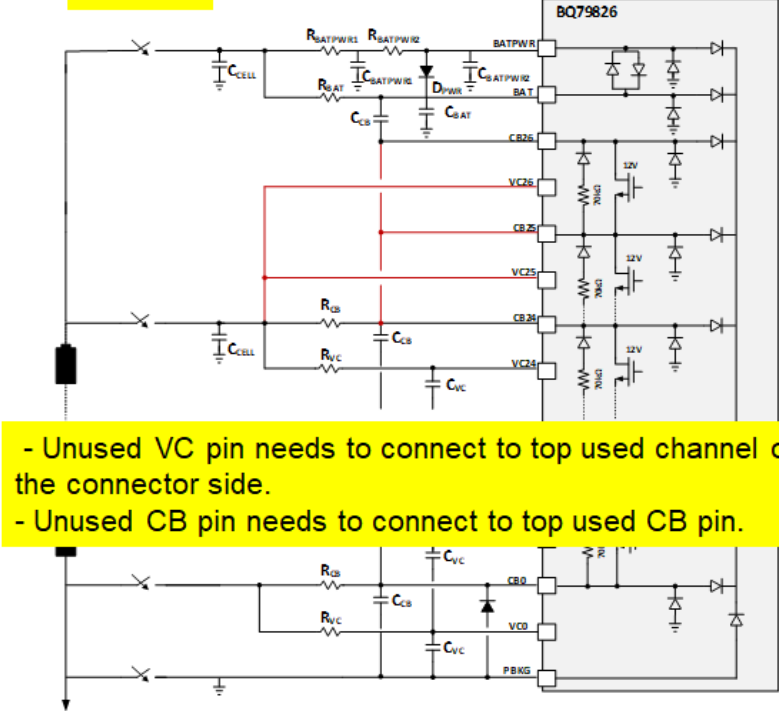


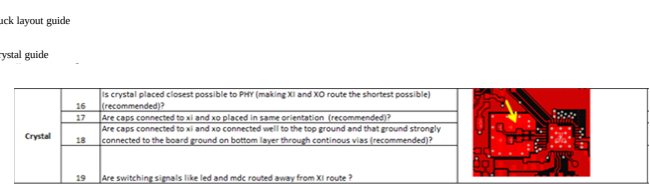
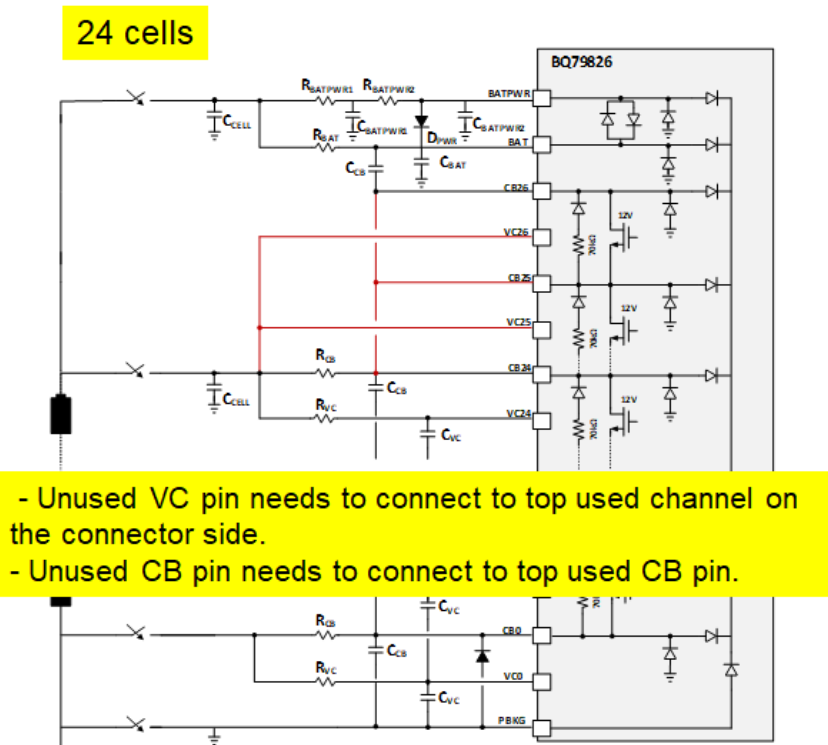




24 cells



- Unused VC pin needs to connect to top used channel on the connector side.
- Unused CB pin needs to connect to top used CB pin.



FID1

FID2

FID3

PCB Number: TIDA-010978

PCB Rev: C1.0

PCB LOGO

Texas Instruments

CE Mark

PCB LOGO

FCC disclaimer

PCB LOGO

WEEE logo

DANGER HIGH VOLTAGE

LBL1

PCB Label

THT-14-423-10

Size: 0.65" x 0.20"

ZZ1

Label Assembly Note

This Assembly Note is for PCB labels only

ZZ2

Assembly Note

These assemblies are ESD sensitive. ESD precautions shall be observed.

ZZ3

Assembly Note

These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4

Assembly Note

These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

H1

MTG_NoPads

H2

MTG_NoPads

H3

MTG_NoPads

H4

MTG_NoPads

Temp Rise	10 C			20 C			30 C			
Copper	1/2 oz	1 oz	2 oz	1/2 oz	1 oz	2 oz	1/2 oz	1 oz	2 oz	
Trace Width	Maximum Current Amps									
inch	mm	5	10	14	0.6	1.2	1.6	7	1.5	2.2
10	0.254	5	10	14	0.6	1.2	1.6	7	1.5	2.2
15	0.381	7	12	16	0.8	1.3	2.4	1.0	1.6	3.0
20	0.508	7	13	21	1.0	1.7	3.0	1.2	2.4	3.6
25	0.635	9	17	25	1.2	2.2	3.3	1.5	2.8	4.0
30	0.762	11	19	30	1.4	2.5	4.0	1.7	3.2	5.0
40	1.27	15	26	40	2.0	3.6	6.0	2.6	4.4	7.3
75	1.905	20	35	57	2.8	4.5	7.8	3.5	6.0	10.0
100	2.54	26	42	69	3.5	6.0	9.9	4.3	7.5	12.5
200	5.08	42	70	115	6.0	10.0	11.0	7.5	13.0	20.5
250	6.35	50	83	123	7.2	12.3	20.0	9.0	14.0	24.5

Orderable: ChangeMe in variant

Designed for: Public Release

Mod. Date: 2025/8/22

TID #: TIDA-010978

Project Title: 52s EIS BMU

Number: TIDA-010978 | Rev: C0

Sheet Title:

SVN Rev: Not in version control

Assembly Variant: 001

Sheet: 3 of 3

Drawn By:

File: TIDA-010978_Hardware_SchDoc

Size: B

Engineer: JUNHUA YAN

Contact: http://www.ti.com/support

TEXAS

INSTRUMENTS

http://www.ti.com

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