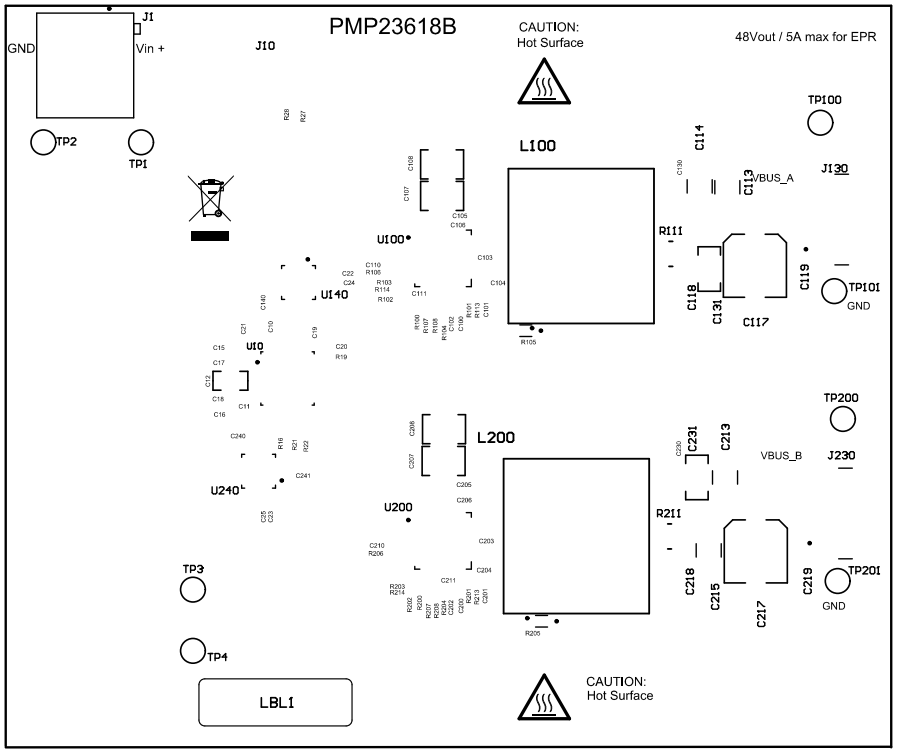
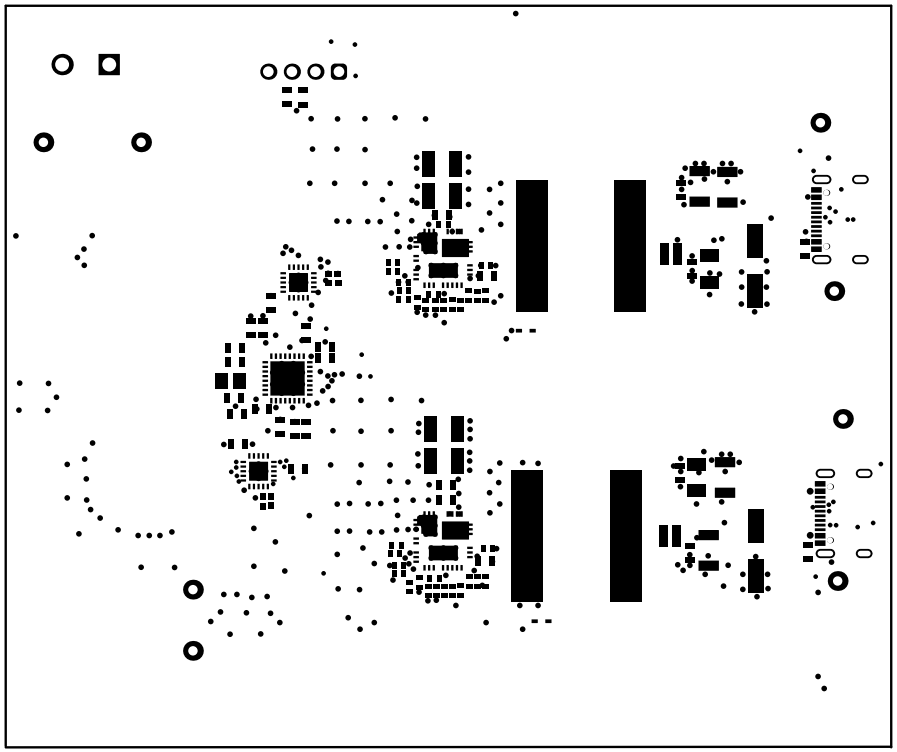
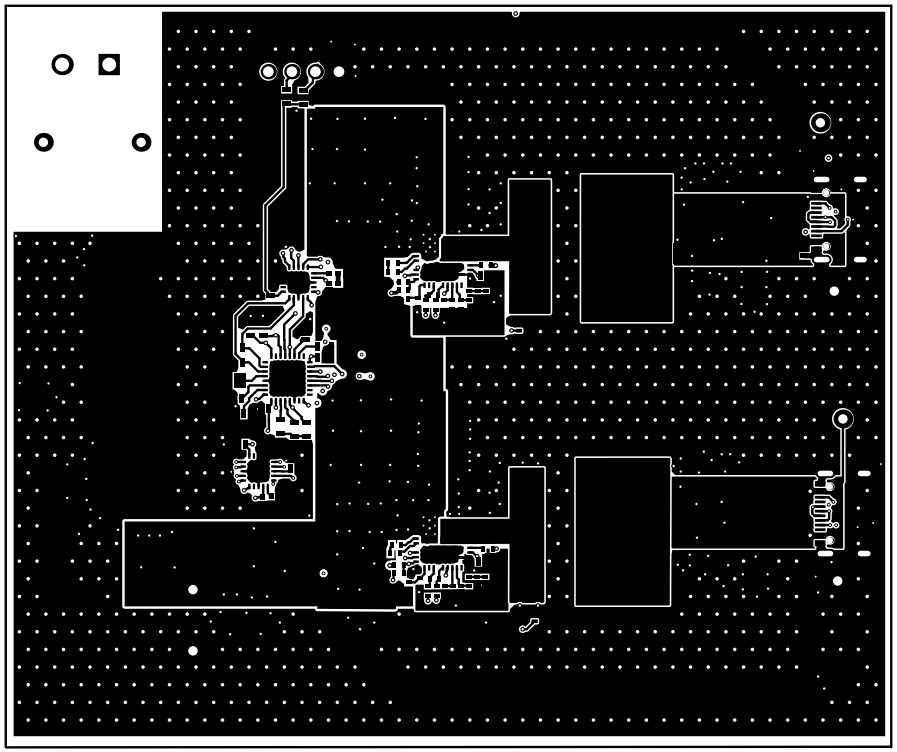


ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PMP23618	REV: B	SVN REV: Not in version control
LAYER NAME = Top Overlay	TID #: N/A		
PLOT NAME = Top Overlay	GENERATED : 3/18/2026 2:57:56 PM		TEXAS INSTRUMENTS

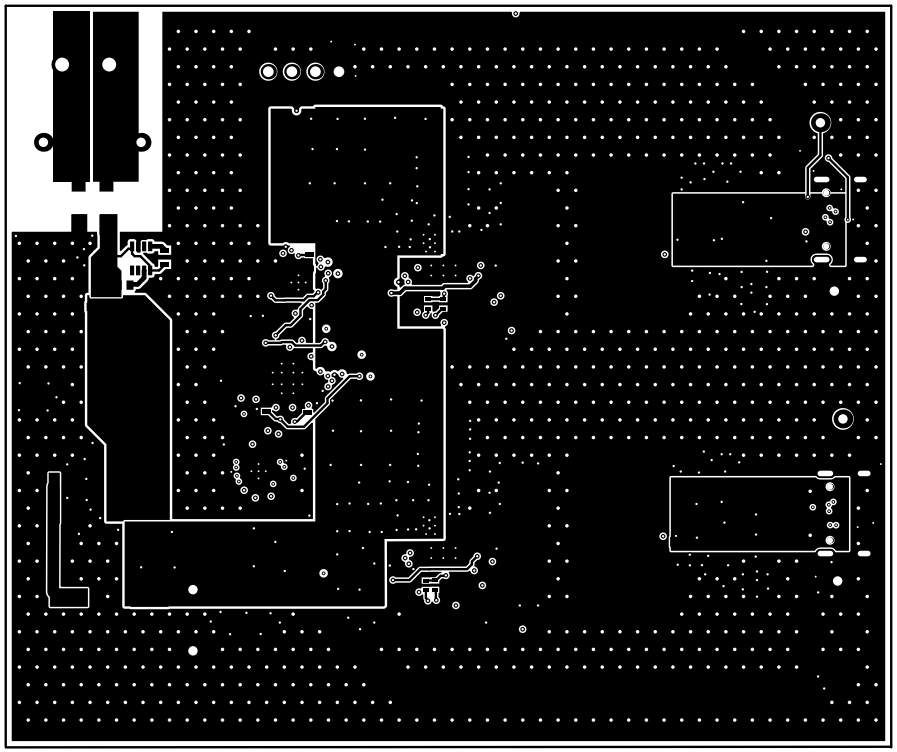


ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PMP23618	REV: B	SVN REV: Not in version control
LAYER NAME = Top Solder	TID #: N/A		
PLOT NAME = Top Solder Mask	GENERATED : 3/18/2026 2:57:57 PM		TEXAS INSTRUMENTS

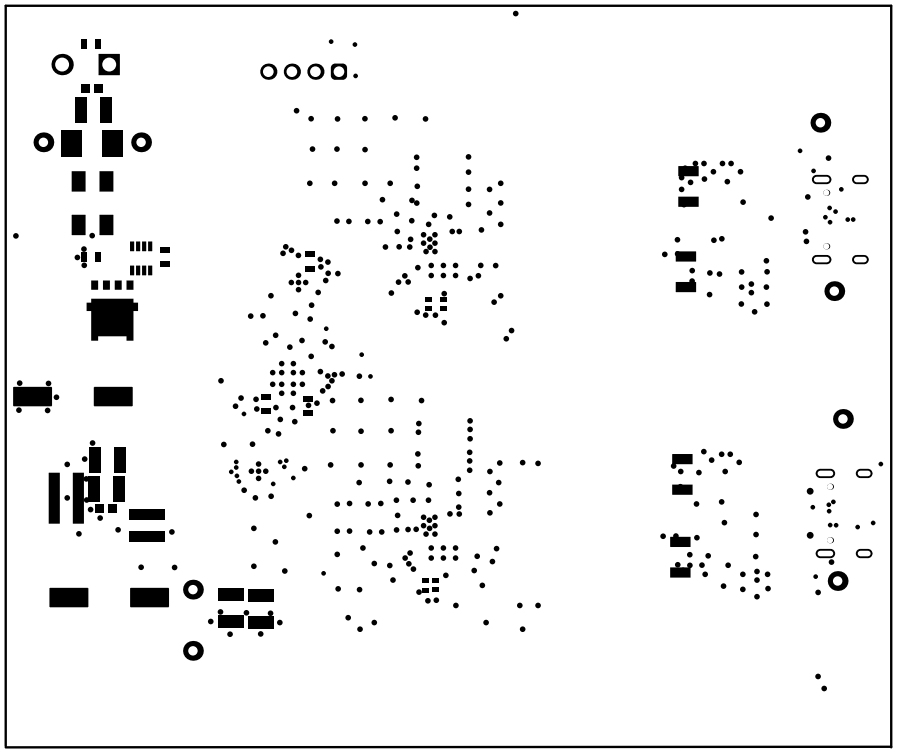


Here are the main changes in layout between Rev A(built on PMP23501) and Rev B:
1. Reduced board size from 125mmx87mm to 95mmx80mm
2. EMI filter was rotated and placed on the bottom for better power flow and shielding.
3. Connectors for the Hercules were removed
4. Additional copper was added to the Vin trace as well as Vias under the pins/pads to get the heat out more effectively
5. TCA controller was moved close to the DC/DC to provide shorter traces for high frequency traces(sync/I2C)
6. Different usb c connector was used for better layout and power flow.
7. Inductor was moved further away from the IC and pad size was made larger

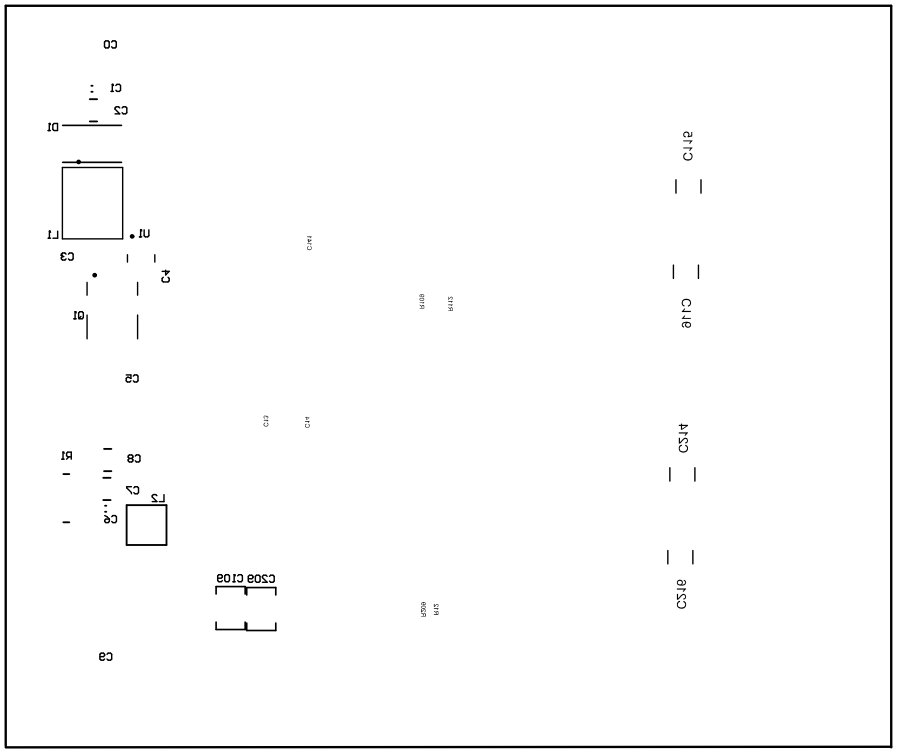
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PMP23618	REV: B	SVN REV: Not in version control
LAYER NAME = Top Layer	TID #: N/A		
PLOT NAME = Top Layer	GENERATED : 3/18/2026 2:57:57 PM		TEXAS INSTRUMENTS



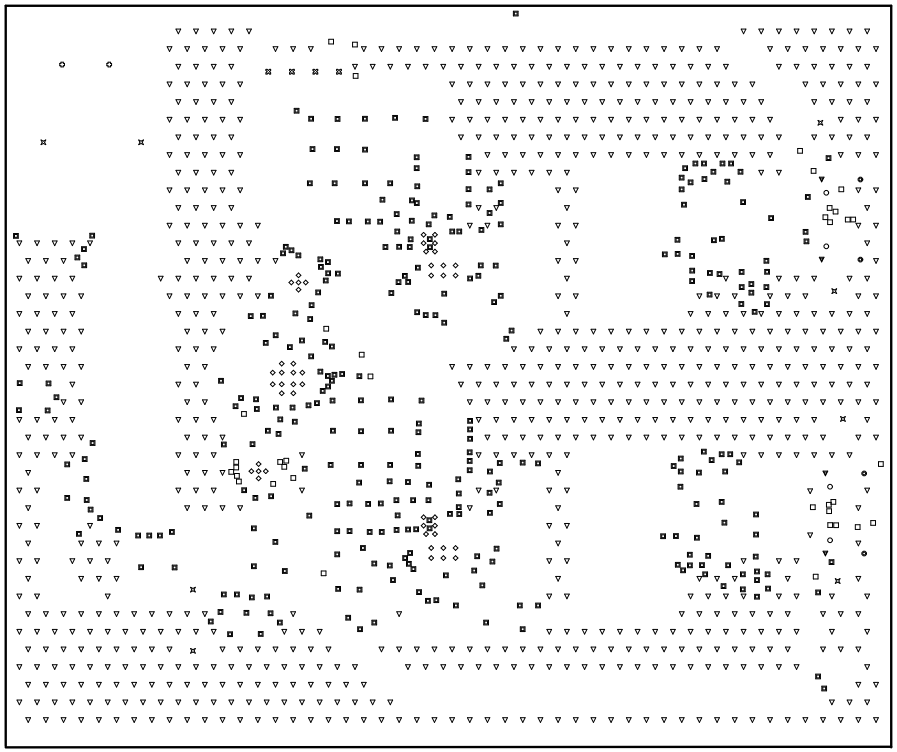
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PMP23618	REV: B	SVN REV: Not in version control
LAYER NAME = Bottom Layer	TID #: N/A		
PLOT NAME = Bottom Layer	GENERATED : 3/18/2026 2:57:57 PM		TEXAS INSTRUMENTS



ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PMP23618	REV: B	SVN REV: Not in version control
LAYER NAME = Bottom Solder	TID #: N/A		
PLOT NAME = Bottom Solder Mask	GENERATED : 3/18/2026 2:57:57 PM		TEXAS INSTRUMENTS



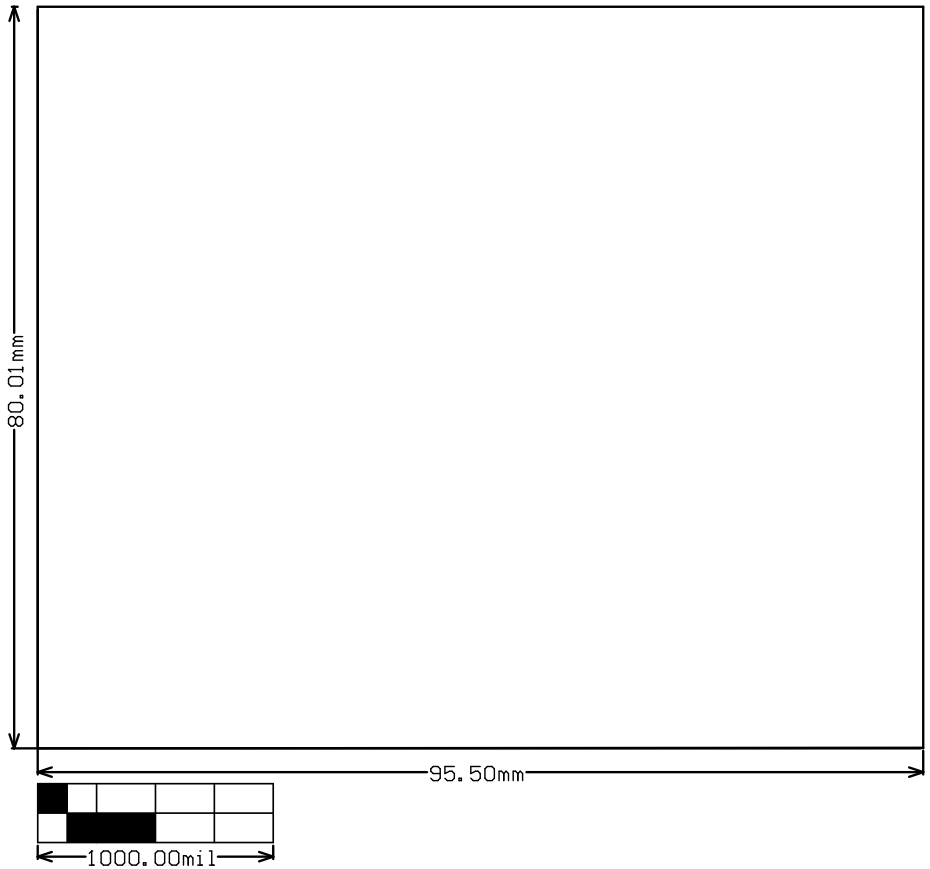
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PMP23618	REV: B	SVN REV: Not in version control
LAYER NAME = Bottom Overlay	TID #: N/A		
PLOT NAME = Bottom Overlay	GENERATED : 3/18/2026 2:57:57 PM		TEXAS INSTRUMENTS



Symbol	Quantity	Finished Hole Size	Plated	Hole Type	Drill Layer Pair	Hole Tolerance
○	4	25.59mil (0.650mm)	NPTH	Round	Top Layer - Bottom Layer	
◇	46	7.87mil (0.200mm)	PTH	Round	Top Layer - Bottom Layer	
□	37	8.00mil (0.203mm)	PTH	Round	Top Layer - Bottom Layer	
▣	321	10.00mil (0.254mm)	PTH	Round	Top Layer - Bottom Layer	
▽	862	15.00mil (0.381mm)	PTH	Round	Top Layer - Bottom Layer	
⨯	8	40.00mil (1.016mm)	PTH	Round	Top Layer - Bottom Layer	
⊠	4	45.28mil (1.150mm)	PTH	Round	Top Layer - Bottom Layer	
✪	2	59.06mil (1.500mm)	PTH	Round	Top Layer - Bottom Layer	
◉	4	55.12mil (1.400mm)	PTH	Slot	Top Layer - Bottom Layer	
▼	4	66.93mil (1.700mm)	PTH	Slot	Top Layer - Bottom Layer	
	1292 Total					

Slot definitions : Routed Path Length = Calculated from tool start centre position to tool end centre position.
Hole Length = Routed Path Length + Tool Size = Slot length as defined in the PCB layout

ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PMP23618	REV: B	SVN REV: Not in version control
LAYER NAME = Drill Drawing	TID #: N/A		
PLOT NAME = Drill Drawing	GENERATED : 3/18/2026 2:57:57 PM		TEXAS INSTRUMENTS



ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PMP23618	REV: B	SVN REV: Not in version control
LAYER NAME = M2 Board Dimensions	TID #: N/A		
PLOT NAME = Board Dimensions	GENERATED : 3/18/2026 2:57:58 PM		TEXAS INSTRUMENTS

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Last updated 10/2025