

## SN74LVC1G14 Single Schmitt-Trigger Inverter

### 1 Features

- Latch-up performance exceeds 100mA per JESD 78, Class II
- ESD protection exceeds JESD 22
  - 2000V human-body model (A114-A)
  - 200V machine model (A115-A)
  - 1000V charged-device model (C101)
- Available in the Texas Instruments NanoFree™ package
- Supports 5V  $V_{CC}$  operation
- Inputs accept voltages to 5.5V
- Maximum  $t_{pd}$  of 4.6ns at 3.3V
- Low power consumption, 10 $\mu$ A maximum  $I_{CC}$
- $\pm 24$ mA output drive at 3.3V
- $I_{off}$  supports partial-power-down mode operation

### 2 Applications

- AV receiver
- Audio dock: portable
- Blu-ray player and home theater
- Embedded PC
- MP3 player/recorder (portable audio)
- Personal Digital Assistant (PDA)
- Power: telecom/server AC/DC supply: single controller: analog and digital
- Solid State Drive (SSD): client and enterprise
- TV: LCD/Digital and High-Definition (HDTV)
- Tablet: enterprise
- Video analytics: server
- Wireless headset, keyboard, and mouse

### 3 Description

This single Schmitt-trigger inverter is designed for 1.65V to 5.5V  $V_{CC}$  operation.

The SN74LVC1G14 device contains one inverter and performs the Boolean function  $Y = \bar{A}$ . The device functions as an independent inverter with Schmitt-trigger inputs, so the device has different input threshold levels for positive-going ( $V_{T+}$ ) and negative-going ( $V_{T-}$ ) signals to provide hysteresis ( $\Delta V_T$ ) which makes the device tolerant to slow or noisy input signals.

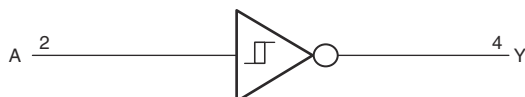
NanoFree™ package technology is a major breakthrough in IC packaging concepts, using the die as the package.

This device is fully specified for partial-power-down applications using  $I_{off}$ . The  $I_{off}$  circuitry disables the outputs when the device is powered down. This inhibits current backflow into the device which prevents damage to the device.

#### Package Information

| PART NUMBER | PACKAGE <sup>(1)</sup> | PACKAGE SIZE <sup>(2)</sup> | BODY SIZE (NOM) <sup>(3)</sup> |
|-------------|------------------------|-----------------------------|--------------------------------|
| SN74LVC1G14 | DBV (SOT-23, 5)        | 2.90mm × 2.80mm             | 2.90mm × 1.60mm                |
|             | DCK (SC70, 5)          | 2.00mm × 2.10mm             | 2.00mm × 1.25mm                |
|             | DRL (SOT-5X3, 5)       | 1.60mm × 1.60mm             | 1.60mm × 1.20mm                |
|             | DRY (USON, 6)          | 1.45mm × 1.00mm             | 1.45mm × 1.00mm                |
|             | DSF (X2SON, 6)         | 1.00mm × 1.00mm             | 1.00mm × 1.00mm                |
|             | YZP (DSBGA, 5)         | 1.75mm × 1.75mm             | 1.39mm × 0.89mm                |
|             | YZV (DSBGA, 4)         | 1.25mm × 1.25mm             | 0.89mm × 0.89mm                |
|             | DPW (X2SON, 5)         | 0.80mm × 0.80mm             | 0.80mm × 0.80mm                |

- (1) For all available packages, see the orderable addendum at the end of the data sheet.
- (2) The package size (length × width) is a nominal value and includes pins, where applicable.
- (3) The body size (length × width) is a nominal value and does not include pins.



**Logic Diagram (Positive Logic)**  
(DBV, DCK, DRL, DRY, DPW, and YZP Package)



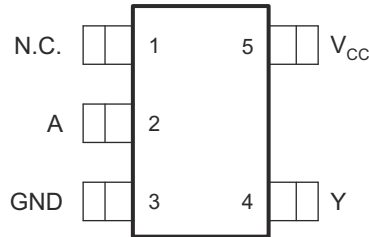
**Logic Diagram (Positive Logic)**  
(YZV Package)



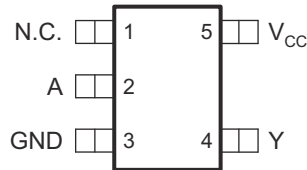
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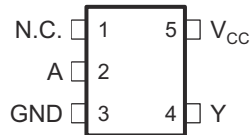
## 4 Pin Configuration and Functions



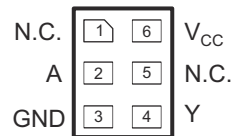
**Figure 4-1. DBV Package 5-Pin SOT-23 Top View**



**Figure 4-2. DCK Package 5-Pin SC70 Top View**



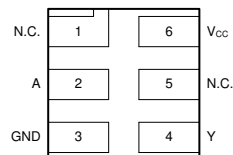
**Figure 4-3. DRL Package 5-Pin SOT-5X3 Top View**



**Figure 4-4. DRY Package 6-Pin SON Top View**

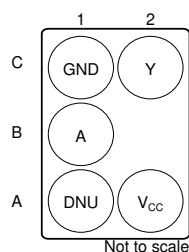


**Figure 4-5. DPW Package 5-Pin X2SON Top View**

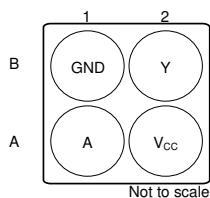


See mechanical drawings for dimensions.  
N.C. – No internal connection

**Figure 4-6. DSF Package 6-Pin SON Top View**



DNU – Do not use

**Figure 4-7. YZP Package 5-Pin DSBGA Bottom View****Figure 4-8. YZV Package 4-Pin DSBGA Bottom View****Table 4-1. Pin Functions**

| NAME            | PIN                |          |     |     | I/O | DESCRIPTION                           |
|-----------------|--------------------|----------|-----|-----|-----|---------------------------------------|
|                 | DBV, DCK, DRL, DPW | DRY, DSF | YZP | YZV |     |                                       |
| A               | 2                  | 2        | B1  | A1  | I   | Signal Input                          |
| GND             | 3                  | 3        | C1  | B1  | —   | Ground                                |
| N.C.            | 1                  | 1, 5     | —   | —   | —   | No internal connection <sup>(1)</sup> |
| DNU             | —                  | —        | A1  | —   | —   | Do not use <sup>(2)</sup>             |
| V <sub>CC</sub> | 5                  | 6        | A2  | A2  | —   | Positive Supply                       |
| Y               | 4                  | 4        | C2  | B2  | O   | Signal Output                         |

- (1) Pins labeled N.C. can be connected to any signal or voltage source, including ground. They should always be soldered to the board.
- (2) Pins labeled DNU should not be connected to any signal or voltage source, including ground. They should always be soldered to the board.

## 5 Specifications

### 5.1 Absolute Maximum Ratings

over operating free-air temperature range (unless otherwise noted)<sup>(1)</sup>

|                  |                                                                                             |                    | MIN  | MAX                   | UNIT |
|------------------|---------------------------------------------------------------------------------------------|--------------------|------|-----------------------|------|
| V <sub>CC</sub>  | Supply voltage                                                                              |                    | −0.5 | 6.5                   | V    |
| V <sub>I</sub>   | Input voltage <sup>(2)</sup>                                                                |                    | −0.5 | 6.5                   | V    |
| V <sub>O</sub>   | Voltage range applied to any output in the high-impedance or power-off state <sup>(2)</sup> |                    | −0.5 | 6.5                   | V    |
| V <sub>O</sub>   | Voltage range applied to any output in the high or low state <sup>(2)</sup> <sup>(3)</sup>  |                    | −0.5 | V <sub>CC</sub> + 0.5 | V    |
| I <sub>IK</sub>  | Input clamp current                                                                         | V <sub>I</sub> < 0 | −50  |                       | mA   |
| I <sub>OK</sub>  | Output clamp current                                                                        | V <sub>O</sub> < 0 | −50  |                       | mA   |
| I <sub>O</sub>   | Continuous output current                                                                   |                    | ±50  |                       | mA   |
|                  | Continuous current through V <sub>CC</sub> or GND                                           |                    | ±100 |                       | mA   |
| T <sub>J</sub>   | Maximum junction temperature                                                                |                    | 150  |                       | °C   |
| T <sub>stg</sub> | Storage temperature                                                                         |                    | −65  | 150                   | °C   |

- (1) Stresses beyond those listed under *Absolute Maximum Ratings* may cause permanent damage to the device. These are stress ratings only, which do not imply functional operation of the device at these or any other conditions beyond those indicated under *Recommended Operating Conditions*. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.
- (2) The input and output negative-voltage ratings may be exceeded if the input and output current ratings are observed.
- (3) The value of V<sub>CC</sub> is provided in the recommended operating conditions table.

### 5.2 ESD Ratings

|                    |                         | VALUE                                                                          | UNIT |
|--------------------|-------------------------|--------------------------------------------------------------------------------|------|
| V <sub>(ESD)</sub> | Electrostatic discharge | Human-body model (HBM), per ANSI/ESDA/JEDEC JS-001 <sup>(1)</sup>              | 2000 |
|                    |                         | Charged-device model (CDM), per JEDEC specification JESD22-C101 <sup>(2)</sup> | 1000 |
|                    |                         | Machine Model (A115-A)                                                         | 200  |

- (1) JEDEC document JEP155 states that 500-V HBM allows safe manufacturing with a standard ESD control process.
- (2) JEDEC document JEP157 states that 250-V CDM allows safe manufacturing with a standard ESD control process.

### 5.3 Recommended Operating Conditions

over operating free-air temperature range (unless otherwise noted)<sup>(1)</sup>

|                 |                                | MIN                        | MAX             | UNIT |    |
|-----------------|--------------------------------|----------------------------|-----------------|------|----|
| V <sub>CC</sub> | Supply voltage                 | Operating                  | 1.65            | 5.5  | V  |
|                 |                                | Data retention only        | 1.5             |      |    |
| V <sub>I</sub>  | Input voltage                  | 0                          | 5.5             | V    |    |
| V <sub>O</sub>  | Output voltage                 | 0                          | V <sub>CC</sub> | V    |    |
| I <sub>OH</sub> | High-level output current      | V <sub>CC</sub> = 1.65 V   | –4              | mA   |    |
|                 |                                | V <sub>CC</sub> = 2.3 V    | –8              |      |    |
|                 |                                | V <sub>CC</sub> = 3 V      | –16             |      |    |
|                 |                                |                            | –24             |      |    |
|                 |                                | V <sub>CC</sub> = 4.5 V    | –32             |      |    |
| I <sub>OL</sub> | Low-level output current       | V <sub>CC</sub> = 1.65 V   | 4               | mA   |    |
|                 |                                | V <sub>CC</sub> = 2.3 V    | 8               |      |    |
|                 |                                | V <sub>CC</sub> = 3 V      | 16              |      |    |
|                 |                                |                            | 24              |      |    |
|                 |                                | V <sub>CC</sub> = 4.5 V    | 32              |      |    |
| T <sub>A</sub>  | Operating free-air temperature | YZP, YZV, and DPW packages | –40             | 85   | °C |
|                 |                                | All other packages         | –40             | 125  |    |

- (1) All unused inputs of the device must be held at V<sub>CC</sub> or GND to assure proper device operation. See [Implications of Slow or Floating CMOS Inputs](#).

### 5.4 Thermal Information

| THERMAL METRIC <sup>(1)</sup> |                                              | SN74LVC1G14     |               |                  |              |                |                |                | UNIT |
|-------------------------------|----------------------------------------------|-----------------|---------------|------------------|--------------|----------------|----------------|----------------|------|
|                               |                                              | DBV<br>(SOT-23) | DCK<br>(SC70) | DRL<br>(SOT-5X3) | DRY<br>(SON) | DPW<br>(X2SON) | YZV<br>(DSBGA) | YZP<br>(DSBGA) |      |
|                               |                                              | 5 PINS          | 5 PINS        | 5 PINS           | 5 PINS       | 5 PINS         | 4 PINS         | 5 PINS         |      |
| R <sub>θJA</sub>              | Junction-to-ambient thermal resistance       | 357.1           | 371.0         | 296.2            | 369.6        | 522.9          | 168.2          | 146.2          | °C/W |
| R <sub>θJC(top)</sub>         | Junction-to-case (top) thermal resistance    | 263.7           | 297.5         | 137.3            | 257.6        | 250.5          | 2.1            | 1.4            | °C/W |
| R <sub>θJB</sub>              | Junction-to-board thermal resistance         | 264.4           | 258.6         | 145.3            | 230.8        | 384.0          | 55.9           | 39.8           | °C/W |
| Ψ <sub>JT</sub>               | Junction-to-top characterization parameter   | 195.6           | 195.6         | 14.7             | 77.2         | 46.5           | 1.1            | 0.7            | °C/W |
| Ψ <sub>JB</sub>               | Junction-to-board characterization parameter | 262.2           | 256.2         | 145.9            | 231.0        | 382.8          | 56.3           | 39.3           | °C/W |
| R <sub>θJC(bot)</sub>         | Junction-to-case (bottom) thermal resistance | N/A             | N/A           | N/A              | N/A          | 174.1          | N/A            | N/A            | °C/W |

- (1) For more information about traditional and new thermal metrics, see the [Semiconductor and IC Package Thermal Metrics](#) application note.

## 5.5 Electrical Characteristics

over recommended operating free-air temperature range (unless otherwise noted)

| PARAMETER                                                              |         | TEST CONDITIONS                                                              | V <sub>CC</sub> | –40°C to 85°C         |                    |      | –40°C to 125°C <sup>(2)</sup> |     |      | UNIT |
|------------------------------------------------------------------------|---------|------------------------------------------------------------------------------|-----------------|-----------------------|--------------------|------|-------------------------------|-----|------|------|
|                                                                        |         |                                                                              |                 | MIN                   | TYP <sup>(1)</sup> | MAX  | MIN                           | TYP | MAX  |      |
| V <sub>T+</sub><br>Positive-going<br>input<br>threshold<br>voltage     |         |                                                                              | 1.65 V          | 0.79                  |                    | 1.16 | .79                           |     | 1.16 | V    |
|                                                                        |         |                                                                              | 2.3 V           | 1.11                  |                    | 1.56 | 1.11                          |     | 1.56 |      |
|                                                                        |         |                                                                              | 3 V             | 1.5                   |                    | 1.87 | 1.5                           |     | 1.87 |      |
|                                                                        |         |                                                                              | 4.5 V           | 2.16                  |                    | 2.74 | 2.16                          |     | 2.74 |      |
|                                                                        |         |                                                                              | 5.5 V           | 2.61                  |                    | 3.33 | 2.61                          |     | 3.33 |      |
| V <sub>T–</sub><br>Negative-<br>going<br>input<br>threshold<br>voltage |         | DBV, DCK, DRL, DRY, DSF, YZV and YZP packages                                | 1.65 V          | 0.39                  |                    | 0.62 | .39                           |     | .64  | V    |
|                                                                        |         |                                                                              | 2.3 V           | 0.58                  |                    | 0.87 | .58                           |     | .89  |      |
|                                                                        |         |                                                                              | 3 V             | 0.84                  |                    | 1.14 | .84                           |     | 1.16 |      |
|                                                                        |         |                                                                              | 4.5 V           | 1.41                  |                    | 1.79 | 1.41                          |     | 1.79 |      |
|                                                                        |         |                                                                              | 5.5 V           | 1.87                  |                    | 2.29 | 1.87                          |     | 2.29 |      |
| V <sub>T–</sub><br>Negative-<br>going<br>input<br>threshold<br>voltage |         | DPW package                                                                  | 1.65 V          | 0.44                  |                    | 0.67 |                               |     |      | V    |
|                                                                        |         |                                                                              | 2.3 V           | 0.63                  |                    | 0.92 |                               |     |      |      |
|                                                                        |         |                                                                              | 3 V             | 0.89                  |                    | 1.19 |                               |     |      |      |
|                                                                        |         |                                                                              | 4.5 V           | 1.46                  |                    | 1.84 |                               |     |      |      |
|                                                                        |         |                                                                              | 5.5 V           | 1.92                  |                    | 2.34 |                               |     |      |      |
| ΔV <sub>T</sub><br>Hysteresis<br>(V <sub>T+</sub> – V <sub>T–</sub> )  |         |                                                                              | 1.65 V          | 0.37                  |                    | 0.62 | 0.37                          |     | 0.62 | V    |
|                                                                        |         |                                                                              | 2.3 V           | 0.48                  |                    | 0.77 | 0.48                          |     | 0.77 |      |
|                                                                        |         |                                                                              | 3 V             | 0.56                  |                    | 0.87 | 0.56                          |     | 0.87 |      |
|                                                                        |         |                                                                              | 4.5 V           | 0.71                  |                    | 1.04 | 0.71                          |     | 1.04 |      |
|                                                                        |         |                                                                              | 5.5 V           | 0.71                  |                    | 1.11 | 0.71                          |     | 1.11 |      |
| V <sub>OH</sub>                                                        |         | I <sub>OL</sub> = –100 μA                                                    | 1.65 V to 4.5 V | V <sub>CC</sub> – 0.1 |                    |      | V <sub>CC</sub> – 0.1         |     |      | V    |
|                                                                        |         |                                                                              | 1.65 V          | 1.2                   |                    |      | 1.2                           |     |      |      |
|                                                                        |         |                                                                              | 2.3 V           | 1.9                   |                    |      | 1.9                           |     |      |      |
|                                                                        |         |                                                                              | 3 V             | 2.4                   |                    |      | 2.4                           |     |      |      |
|                                                                        |         |                                                                              |                 | 2.3                   |                    |      | 2.3                           |     |      |      |
|                                                                        |         |                                                                              | 4.5 V           | 3.8                   |                    |      | 3.8                           |     |      |      |
| V <sub>OL</sub>                                                        |         | I <sub>OL</sub> = 100 μA                                                     | 1.65 V to 4.5 V | 0.1                   |                    |      | 0.1                           |     |      | V    |
|                                                                        |         |                                                                              | 1.65 V          | 0.45                  |                    |      | 0.45                          |     |      |      |
|                                                                        |         |                                                                              | 2.3 V           | 0.3                   |                    |      | 0.3                           |     |      |      |
|                                                                        |         |                                                                              | 3 V             | 0.4                   |                    |      | 0.4                           |     |      |      |
|                                                                        |         |                                                                              |                 | 0.55                  |                    |      | 0.55                          |     |      |      |
|                                                                        |         |                                                                              | 4.5 V           | 0.55                  |                    |      | 0.7                           |     |      |      |
| I <sub>I</sub>                                                         | A input | V <sub>I</sub> = 5.5 V or GND                                                | 0 to 5.5 V      | ±5                    |                    |      | ±5                            |     |      | μA   |
| I <sub>off</sub>                                                       |         | V <sub>I</sub> or V <sub>O</sub> = 5.5 V                                     | 0               | ±10                   |                    |      | ±10                           |     |      | μA   |
| I <sub>CC</sub>                                                        |         | V <sub>I</sub> = 5.5 V or GND, I <sub>O</sub> = 0                            | 1.65 V to 5.5 V | 10                    |                    |      | 10                            |     |      | μA   |
| ΔI <sub>CC</sub>                                                       |         | One input at V <sub>CC</sub> – 0.6 V, Other inputs at V <sub>CC</sub> or GND | 3 V to 5.5 V    | 500                   |                    |      | 500                           |     |      | μA   |
| C <sub>i</sub>                                                         |         | V <sub>I</sub> = V <sub>CC</sub> or GND                                      | 3.3 V           | 4.5                   |                    |      | 4.5                           |     |      | pF   |

(1) All typical values are at V<sub>CC</sub> = 3.3 V, T<sub>A</sub> = 25°C.

(2) These specifications do not apply to DPW, YZV and YZP packages. DPW, YZV and YZP have a recommended operating free-air temperature range of –40°C to 85°C.

## 5.6 Switching Characteristics: –40°C to 85°C

over recommended operating free-air temperature range, (–40°C to 85°C unless otherwise noted)

| PARAMETER | FROM<br>(INPUT) | TO<br>(OUTPUT) | $V_{CC}$                         | $C_L = 15\text{ pF}$ |     | $C_L = 30\text{ pF}$<br>or 50 pF |     | UNIT |
|-----------|-----------------|----------------|----------------------------------|----------------------|-----|----------------------------------|-----|------|
|           |                 |                |                                  | MIN                  | MAX | MIN                              | MAX |      |
| $t_{pd}$  | A               | Y              | $1.8\text{ V} \pm 0.15\text{ V}$ | 2.8                  | 9.9 | 3.8                              | 11  | ns   |
|           |                 |                | $2.5\text{ V} \pm 0.2\text{ V}$  | 1.6                  | 5.5 | 2                                | 6.5 |      |
|           |                 |                | $3.3\text{ V} \pm 0.3\text{ V}$  | 1.5                  | 4.6 | 1.8                              | 5.5 |      |
|           |                 |                | $5\text{ V} \pm 0.5\text{ V}$    | 0.9                  | 4.4 | 1.2                              | 5   |      |

## 5.7 Switching Characteristics: –40°C to 125°C

over operating free-air temperature range, (–40°C to 125°C unless otherwise noted)

| PARAMETER | FROM<br>(INPUT) | TO<br>(OUTPUT) | $V_{CC}$                         | $C_L = 30\text{ pF}$<br>or 50 pF |     | UNIT |
|-----------|-----------------|----------------|----------------------------------|----------------------------------|-----|------|
|           |                 |                |                                  | MIN                              | MAX |      |
| $t_{pd}$  | A               | Y              | $1.8\text{ V} \pm 0.15\text{ V}$ | 3.8                              | 13  | ns   |
|           |                 |                | $2.5\text{ V} \pm 0.2\text{ V}$  | 2                                | 8   |      |
|           |                 |                | $3.3\text{ V} \pm 0.3\text{ V}$  | 1.8                              | 6.5 |      |
|           |                 |                | $5\text{ V} \pm 0.5\text{ V}$    | 1.2                              | 6   |      |

## 5.8 Operating Characteristics

$T_A = 25^\circ\text{C}$

| PARAMETER                              | TEST CONDITIONS     | $V_{CC}$ | TYP | UNIT |
|----------------------------------------|---------------------|----------|-----|------|
| $C_{pd}$ Power dissipation capacitance | $f = 10\text{ MHz}$ | 1.8 V    | 20  | pF   |
|                                        |                     | 2.5 V    | 21  |      |
|                                        |                     | 3.3 V    | 22  |      |
|                                        |                     | 5 V      | 25  |      |

## 5.9 Typical Characteristics

$T_A = 25^\circ\text{C}$

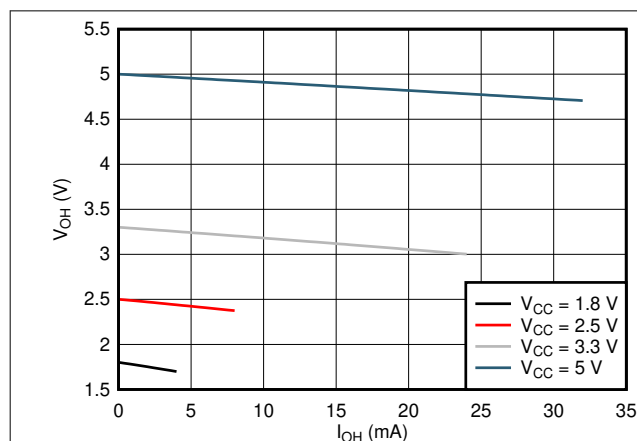


Figure 5-1. Typical  $V_{OH}$  vs  $I_{OH}$  - 25°C

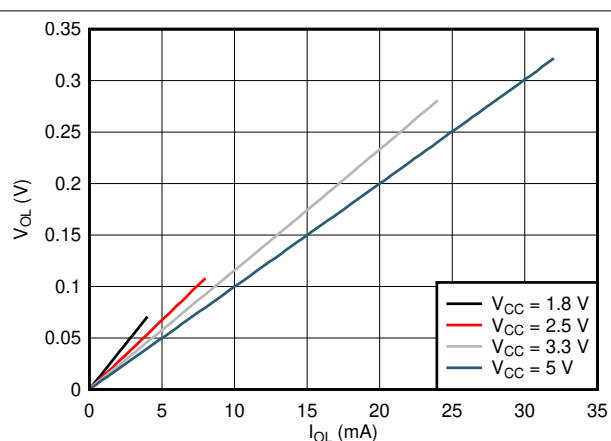
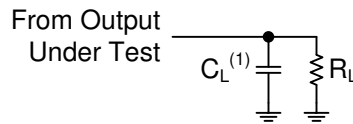


Figure 5-2. Typical  $V_{OL}$  vs  $I_{OL}$  - 25°C



## Parameter Measurement Information

- Input pulse is supplied by generator having the following characteristics:  $PRR \leq 10\text{MHz}$ ,  $Z_O = 50\Omega$ .
- The outputs are measured one at a time, with one transition per measurement.

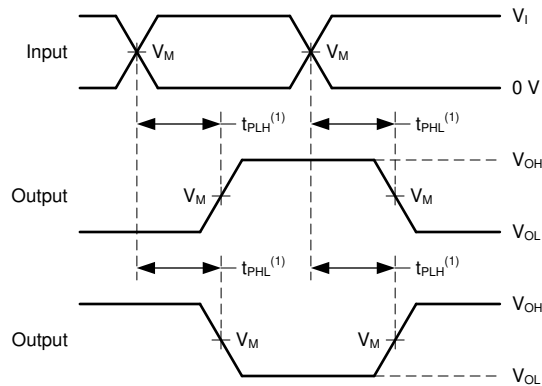


A.  $C_L$  includes probe and jig capacitance.

**Figure 6-1. Load Circuit**

**Table 6-1. Parameter Measurement Conditions**

| $V_{CC}$                         | INPUTS   |                      | $V_M$      | $V_{LOAD}$        | $C_L$ | $R_L$        | $V_D$  |
|----------------------------------|----------|----------------------|------------|-------------------|-------|--------------|--------|
|                                  | $V_I$    | $t_r/t_f$            |            |                   |       |              |        |
| $1.8\text{ V} \pm 0.15\text{ V}$ | $V_{CC}$ | $\leq 2\text{ ns}$   | $V_{CC}/2$ | $2 \times V_{CC}$ | 15 pF | 1 M $\Omega$ | 0.15 V |
|                                  |          |                      |            |                   | 30 pF | 1 k $\Omega$ |        |
| $2.5\text{ V} \pm 0.2\text{ V}$  | $V_{CC}$ | $\leq 2\text{ ns}$   | $V_{CC}/2$ | $2 \times V_{CC}$ | 15 pF | 1 M $\Omega$ | 0.15 V |
|                                  |          |                      |            |                   | 30 pF | 500 $\Omega$ |        |
| $3.3\text{ V} \pm 0.3\text{ V}$  | 3 V      | $\leq 2.5\text{ ns}$ | 1.5 V      | 6 V               | 15 pF | 1 M $\Omega$ | 0.3 V  |
|                                  |          |                      |            |                   | 50 pF | 500 $\Omega$ |        |
| $5\text{ V} \pm 0.5\text{ V}$    | $V_{CC}$ | $\leq 2.5\text{ ns}$ | $V_{CC}/2$ | $2 \times V_{CC}$ | 15 pF | 1 M $\Omega$ | 0.3 V  |
|                                  |          |                      |            |                   | 50 pF | 500 $\Omega$ |        |



A. The maximum value of  $t_{pd}$  is the worst case of  $t_{PLH}$  or  $t_{PHL}$

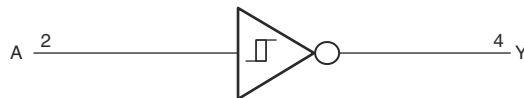
**Figure 6-2. Voltage Waveforms, Propagation Delay Times, Inverting and Non-Inverting Outputs**

## 6 Detailed Description

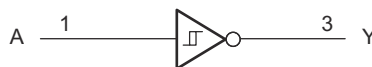
### 6.1 Overview

The SN74LVC1G14 single Schmitt-trigger inverter is designed for 1.65 V to 5.5 V operation and performs the Boolean function  $Y = \overline{A}$ . This device is fully specified for partial-power-down applications using  $I_{off}$ . The  $I_{off}$  circuitry disables the outputs when the device is powered down. This inhibits current backflow into the device which prevents damage to the device.

### 6.2 Functional Block Diagrams



**Figure 6-1. Logic Diagram (Positive Logic)**  
(DBV, DCK, DRL, DRY, DPW, and YZP Package)



**Figure 6-2. Logic Diagram (Positive Logic)**  
(YZV Package)

### 6.3 Feature Description

#### 6.3.1 Balanced High-Drive CMOS Push-Pull Outputs

A balanced output allows the device to sink and source similar currents. The high drive capability of this device creates fast edges into light loads so routing and load conditions should be considered to prevent ringing. Additionally, the outputs of this device are capable of driving larger currents than the device can sustain without being damaged. It is important for the power output of the device to be limited to avoid thermal runaway and damage due to over-current. The electrical and thermal limits defined in the [Absolute Maximum Ratings](#)

[Absolute Maximum Ratings](#) must be followed at all times.

#### 6.3.2 CMOS Schmitt-Trigger Inputs

Standard CMOS inputs are high impedance and are typically modeled as a resistor in parallel with the input capacitance given in the [Electrical Characteristics](#). The worst case resistance is calculated with the maximum input voltage, given in the [Absolute Maximum Ratings](#), and the maximum input leakage current, given in the [Electrical Characteristics](#), using ohm's law ( $R = V \div I$ ).

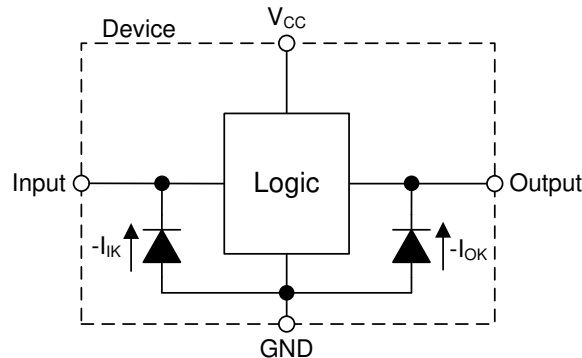
The Schmitt-trigger input architecture provides hysteresis as defined in the [Electrical Characteristics](#), which makes this device extremely tolerant to slow or noisy inputs. While the inputs can be driven much slower than standard CMOS inputs, it is still recommended to properly terminate unused inputs. Driving the inputs slowly will also increase dynamic current consumption of the device.

### 6.3.3 Clamp Diodes

The inputs and outputs to this device have negative clamping diodes.

#### CAUTION

Voltages beyond the values specified in the [Absolute Maximum Ratings](#) table can cause damage to the device. The input negative-voltage and output negative-voltage ratings may be exceeded if the input and output clamp-current ratings are observed.



**Figure 6-3. Electrical Placement of Clamping Diodes for Each Input and Output**

### 6.3.4 Partial Power Down ( $I_{off}$ )

The inputs and outputs for this device enter a high impedance state when the supply voltage is 0 V. The maximum leakage into or out of any input or output pin on the device is specified by  $I_{off}$  in the [Electrical Characteristics](#).

### 6.3.5 Over-Voltage Tolerant Inputs

Input signals to this device can be driven above the supply voltage so long as they remain below the maximum input voltage value specified in the [Absolute Maximum Ratings](#).

## 6.4 Device Functional Modes

[Table 6-1](#) lists the functional modes of the SN74LVC1G14 device.

**Table 6-1. Function Table**

| INPUT<br>A | OUTPUT<br>Y |
|------------|-------------|
| H          | L           |
| L          | H           |

## 7 Application and Implementation

### Note

Information in the following applications sections is not part of the TI component specification, and TI does not warrant its accuracy or completeness. TI's customers are responsible for determining suitability of components for their purposes, as well as validating and testing their design implementation to confirm system functionality.

### 7.1 Application Information

Mechanical input elements, such as push buttons or rotary knobs, offer simple ways to interact with electronic systems. Typically, these elements have recoil or bouncing, where the mechanical element makes and breaks contact multiple times during human interaction. This bouncing can cause one or more repeated signals to be passed, triggering multiple actions when only a single input was intended. One potential solution to mitigating these multiple inputs is by utilizing a Schmitt-trigger to create a debounce circuit. Figure 7-1 shows an example of this solution.

### 7.2 Typical Application

The input due to the push button switches multiple times, causing the output of a non Schmitt-trigger device to trigger multiple times, while the Schmitt-trigger input device with RC delay limits the output pulse to a single pulse desired by the user. The separated positive and negative input voltage threshold values, see Figure 7-2, prevent multiple triggers from occurring.

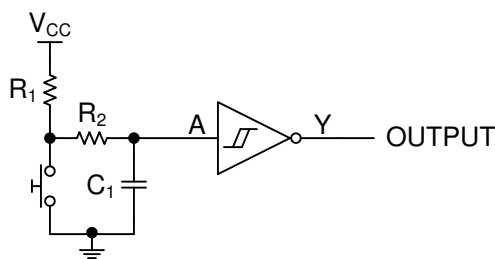


Figure 7-1. Push Button Debounce Circuit Schematic

#### 7.2.1 Design Requirements

This device uses CMOS technology and has balanced output drive. Take care to avoid bus contention because it can drive currents that would exceed maximum limits. The high drive also creates fast edges into light loads so routing and load conditions should be considered to prevent ringing.

#### 7.2.2 Detailed Design Procedure

- Recommended Input Conditions:
  - For specified high and low levels, see ( $V_{T+}$  and  $V_{T-}$ ) in the [Recommended Operating Conditions](#) table.
  - Inputs are overvoltage tolerant allowing them to go as high as ( $V_I$  max) in the [Recommended Operating Conditions](#) table at any valid  $V_{CC}$ .
- Recommended Output Conditions:
  - Load currents should not exceed ( $I_O$  max) per output and should not exceed (Continuous current through  $V_{CC}$  or GND) total current for the part. These limits are located in the [Absolute Maximum Ratings](#) table.

### 7.2.3 Application Curve

Figure 7-2 is created from the values given in the [Electrical Characteristics](#). Linear interpolation shows the values between each given point.

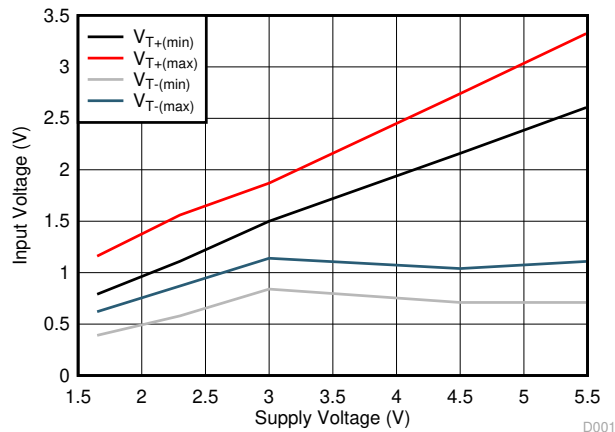


Figure 7-2. Interpolated Threshold Voltages vs. V<sub>CC</sub>

## 7.3 Power Supply Recommendations

The power supply can be any voltage between the minimum and maximum supply voltage rating located in the [Recommended Operating Conditions](#) table.

The V<sub>CC</sub> pin must have a good bypass capacitor to prevent power disturbance. A 0.1-μF capacitor is recommended, and it is ok to parallel multiple bypass caps to reject different frequencies of noise. 0.1-μF and 1-μF capacitors are commonly used in parallel. The bypass capacitor must be installed as close to the power pin as possible for best results.

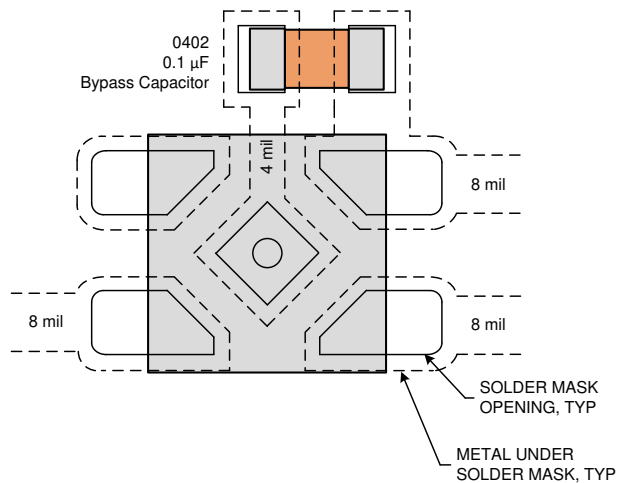
## 7.4 Layout

### 7.4.1 Layout Guidelines

Even low data rate digital signals can contain high-frequency signal components due to fast edge rates. When a printed-circuit board (PCB) trace turns a corner at a 90° angle, a reflection can occur. A reflection occurs primarily because of the change of width of the trace. At the apex of the turn, the trace width increases to 1.414 times the width. This increase upsets the transmission-line characteristics, especially the distributed capacitance and self-inductance of the trace which results in the reflection. Not all PCB traces can be straight and therefore some traces must turn corners.

An example layout is given in [Figure 7-3](#) for the DPW (X2SON-5) package. This example layout includes a 0402 (metric) capacitor and uses the measurements found in the example board layout appended to this end of this datasheet. A via of diameter 0.1 mm (3.973 mil) is placed directly in the center of the device. This via can be used to trace out the center pin connection through another board layer, or it can be left out of the layout

## 7.4.2 Layout Example



**Figure 7-3. Example Layout With DPW (X2SON-5) Package**

## 8 Device and Documentation Support

### 8.1 Documentation Support

#### 8.1.1 Related Documentation

For related documentation see the following:

- Texas Instruments, [Implications of Slow or Floating CMOS Inputs application note](#)

### 8.2 Receiving Notification of Documentation Updates

To receive notification of documentation updates, navigate to the device product folder on [ti.com](#). Click on *Notifications* to register and receive a weekly digest of any product information that has changed. For change details, review the revision history included in any revised document.

### 8.3 Support Resources

[TI E2E™ support forums](#) are an engineer's go-to source for fast, verified answers and design help — straight from the experts. Search existing answers or ask your own question to get the quick design help you need.

Linked content is provided "AS IS" by the respective contributors. They do not constitute TI specifications and do not necessarily reflect TI's views; see TI's [Terms of Use](#).

### 8.4 Trademarks

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All trademarks are the property of their respective owners.

### 8.5 Electrostatic Discharge Caution



This integrated circuit can be damaged by ESD. Texas Instruments recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage.

ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.

### 8.6 Glossary

[TI Glossary](#) This glossary lists and explains terms, acronyms, and definitions.

## 9 Revision History

NOTE: Page numbers for previous revisions may differ from page numbers in the current version.

| Changes from Revision Z (June 2025) to Revision AA (October 2025)                                         | Page |
|-----------------------------------------------------------------------------------------------------------|------|
| • Changed Junction-to-ambient thermal resistance value for DCK package from: 276.1°C/W to: 371.0°C/W .... | 6    |
| • Changed Junction-to-case (top) thermal resistance value for DCK package from: 178.9°C/W to: 297.5°C/W.. | 6    |
| • Changed Junction-to-board thermal resistance value for DCK package from: 70.9°C/W to: 258.6°C/W.....    | 6    |
| • Changed Junction-to-top characterization value for DCK package from: 47.0°C/W to: 195.6°C/W.....        | 6    |
| • Changed Junction-to-board characterization value for DCK package from: 69.3°C/W to: 256.2°C/W.....      | 6    |

| Changes from Revision Y (November 2018) to Revision Z (June 2025)                                         | Page |
|-----------------------------------------------------------------------------------------------------------|------|
| • Updated the numbering format for tables, figures, and cross-references throughout the document.....     | 1    |
| • Changed <i>Device Information</i> table to <i>Package Information</i> .....                             | 1    |
| • Changed Junction-to-ambient thermal resistance value for DBV package from: 247.2°C/W to: 357.1°C/W .... | 6    |
| • Changed Junction-to-case (top) thermal resistance value for DBV package from: 154.5°C/W to: 263.7°C/W.. | 6    |
| • Changed Junction-to-board thermal resistance value for DBV package from: 86.8°C/W to: 264.4°C/W.....    | 6    |
| • Changed Junction-to-top characterization value for DBV package from: 58.0°C/W to: 195.6°C/W.....        | 6    |
| • Changed Junction-to-board characterization value for DBV package from: 86.4°C/W to: 262.2°C/W.....      | 6    |

**Changes from Revision X (August 2017) to Revision Y (November 2018)****Page**

|                                                                                                                                            |   |
|--------------------------------------------------------------------------------------------------------------------------------------------|---|
| • Changed New package pinout added to Pin Functions table. Multiple Pin Functions tables condensed to one.....                             | 3 |
| • Changed $T_j$ and $T_{stg}$ lines switched for consistency with other devices.....                                                       | 5 |
| • Added differentiated ROC temperatures for DPW, YZP and YZV packages .....                                                                | 6 |
| • Changed format of Switching Characteristics tables to include columns for different $C_L$ conditions .....                               | 8 |
| • Added temperature range to Conditions statement for Switching Characteristics tables .....                                               | 8 |
| • Replaced PMI section with updated load circuit and relevant waveform figures. Collapsed parameter measurement values into one table..... | 9 |

**10 Mechanical, Packaging, and Orderable Information**

The following pages include mechanical packaging and orderable information. This information is the most current data available for the designated devices. This data is subject to change without notice and revision of this document. For browser-based versions of this data sheet, refer to the left-hand navigation.



## PACKAGING INFORMATION

| Orderable part number             | Status<br>(1) | Material type<br>(2) | Package   Pins   | Package qty   Carrier | RoHS<br>(3) | Lead finish/<br>Ball material<br>(4) | MSL rating/<br>Peak reflow<br>(5) | Op temp (°C) | Part marking<br>(6)                            |
|-----------------------------------|---------------|----------------------|------------------|-----------------------|-------------|--------------------------------------|-----------------------------------|--------------|------------------------------------------------|
| <a href="#">SN74LVC1G14DBVR</a>   | Active        | Production           | SOT-23 (DBV)   5 | 3000   LARGE T&R      | Yes         | NIPDAU   SN                          | Level-1-260C-UNLIM                | -40 to 125   | (C145, C14F, C14J, C14K, C14R)<br>(C14H, C14S) |
| SN74LVC1G14DBVR.A                 | Active        | Production           | SOT-23 (DBV)   5 | 3000   LARGE T&R      | Yes         | SN                                   | Level-1-260C-UNLIM                | -40 to 125   | (C145, C14F, C14J, C14K, C14R)<br>(C14H, C14S) |
| SN74LVC1G14DBVR.B                 | Active        | Production           | SOT-23 (DBV)   5 | 3000   LARGE T&R      | Yes         | SN                                   | Level-1-260C-UNLIM                | -40 to 125   | (C145, C14F, C14J, C14K, C14R)<br>(C14H, C14S) |
| SN74LVC1G14DBVRE4                 | Active        | Production           | SOT-23 (DBV)   5 | 3000   LARGE T&R      | Yes         | NIPDAU                               | Level-1-260C-UNLIM                | -40 to 125   | C14F                                           |
| <a href="#">SN74LVC1G14DBVRG4</a> | Active        | Production           | SOT-23 (DBV)   5 | 3000   LARGE T&R      | Yes         | NIPDAU                               | Level-1-260C-UNLIM                | -40 to 125   | C14F                                           |
| SN74LVC1G14DBVRG4.A               | Active        | Production           | SOT-23 (DBV)   5 | 3000   LARGE T&R      | Yes         | NIPDAU                               | Level-1-260C-UNLIM                | -40 to 125   | C14F                                           |
| SN74LVC1G14DBVRG4.B               | Active        | Production           | SOT-23 (DBV)   5 | 3000   LARGE T&R      | Yes         | NIPDAU                               | Level-1-260C-UNLIM                | -40 to 125   | C14F                                           |
| <a href="#">SN74LVC1G14DBVT</a>   | Active        | Production           | SOT-23 (DBV)   5 | 250   SMALL T&R       | Yes         | NIPDAU   SN                          | Level-1-260C-UNLIM                | -40 to 125   | (C145, C14F, C14J, C14K, C14R)<br>(C14H, C14S) |
| SN74LVC1G14DBVT.B                 | Active        | Production           | SOT-23 (DBV)   5 | 250   SMALL T&R       | Yes         | NIPDAU                               | Level-1-260C-UNLIM                | -40 to 125   | (C145, C14F, C14J, C14K, C14R)<br>(C14H, C14S) |
| SN74LVC1G14DBVTE4                 | Active        | Production           | SOT-23 (DBV)   5 | 250   SMALL T&R       | Yes         | NIPDAU                               | Level-1-260C-UNLIM                | -40 to 125   | C14F                                           |
| <a href="#">SN74LVC1G14DBVTG4</a> | Active        | Production           | SOT-23 (DBV)   5 | 250   SMALL T&R       | Yes         | NIPDAU                               | Level-1-260C-UNLIM                | -40 to 125   | C14F                                           |
| SN74LVC1G14DBVTG4.B               | Active        | Production           | SOT-23 (DBV)   5 | 250   SMALL T&R       | Yes         | NIPDAU                               | Level-1-260C-UNLIM                | -40 to 125   | C14F                                           |
| <a href="#">SN74LVC1G14DCKR</a>   | Active        | Production           | SC70 (DCK)   5   | 3000   LARGE T&R      | Yes         | NIPDAU   SN                          | Level-1-260C-UNLIM                | -40 to 125   | (CF5, CFF, CFJ, CFK, CFR, CFT)<br>(CFH, CFS)   |
| SN74LVC1G14DCKR.A                 | Active        | Production           | SC70 (DCK)   5   | 3000   LARGE T&R      | Yes         | SN                                   | Level-1-260C-UNLIM                | -40 to 125   | (CF5, CFF, CFJ, CFK, CFR, CFT)<br>(CFH, CFS)   |
| SN74LVC1G14DCKR.B                 | Active        | Production           | SC70 (DCK)   5   | 3000   LARGE T&R      | Yes         | SN                                   | Level-1-260C-UNLIM                | -40 to 125   | (CF5, CFF, CFJ, CFK, CFR, CFT)<br>(CFH, CFS)   |
| SN74LVC1G14DCKRE4                 | Active        | Production           | SC70 (DCK)   5   | 3000   LARGE T&R      | Yes         | NIPDAU                               | Level-1-260C-UNLIM                | -40 to 125   | CF5<br>CFS                                     |

| Orderable part number             | Status<br>(1) | Material type<br>(2) | Package   Pins    | Package qty   Carrier | RoHS<br>(3) | Lead finish/<br>Ball material<br>(4) | MSL rating/<br>Peak reflow<br>(5) | Op temp (°C) | Part marking<br>(6)                              |
|-----------------------------------|---------------|----------------------|-------------------|-----------------------|-------------|--------------------------------------|-----------------------------------|--------------|--------------------------------------------------|
| SN74LVC1G14DCKRG4.A               | Active        | Production           | SC70 (DCK)   5    | 3000   LARGE T&R      | Yes         | NIPDAU                               | Level-1-260C-UNLIM                | -40 to 125   | CF5<br>CFS                                       |
| SN74LVC1G14DCKRG4.B               | Active        | Production           | SC70 (DCK)   5    | 3000   LARGE T&R      | Yes         | NIPDAU                               | Level-1-260C-UNLIM                | -40 to 125   | CF5<br>CFS                                       |
| <a href="#">SN74LVC1G14DCKT</a>   | Active        | Production           | SC70 (DCK)   5    | 250   SMALL T&R       | Yes         | NIPDAU   SN                          | Level-1-260C-UNLIM                | -40 to 125   | (CF5, CFF, CFJ, CF<br>K, CFR, CFT)<br>(CFH, CFS) |
| SN74LVC1G14DCKT.B                 | Active        | Production           | SC70 (DCK)   5    | 250   SMALL T&R       | Yes         | NIPDAU                               | Level-1-260C-UNLIM                | -40 to 125   | (CF5, CFF, CFJ, CF<br>K, CFR, CFT)<br>(CFH, CFS) |
| SN74LVC1G14DCKTE4                 | Active        | Production           | SC70 (DCK)   5    | 250   SMALL T&R       | Yes         | NIPDAU                               | Level-1-260C-UNLIM                | -40 to 125   | CF5<br>CFS                                       |
| <a href="#">SN74LVC1G14DCKTG4</a> | Active        | Production           | SC70 (DCK)   5    | 250   SMALL T&R       | Yes         | NIPDAU                               | Level-1-260C-UNLIM                | -40 to 125   | CF5<br>CFS                                       |
| SN74LVC1G14DCKTG4.B               | Active        | Production           | SC70 (DCK)   5    | 250   SMALL T&R       | Yes         | NIPDAU                               | Level-1-260C-UNLIM                | -40 to 125   | CF5<br>CFS                                       |
| <a href="#">SN74LVC1G14DPWR</a>   | Active        | Production           | X2SON (DPW)   5   | 3000   LARGE T&R      | Yes         | NIPDAU                               | Level-1-260C-UNLIM                | -40 to 85    | 9H                                               |
| SN74LVC1G14DPWR.B                 | Active        | Production           | X2SON (DPW)   5   | 3000   LARGE T&R      | Yes         | NIPDAU                               | Level-1-260C-UNLIM                | -40 to 85    | 9H                                               |
| <a href="#">SN74LVC1G14DRLR</a>   | Active        | Production           | SOT-5X3 (DRL)   5 | 4000   LARGE T&R      | Yes         | NIPDAUAG                             | Level-1-260C-UNLIM                | -40 to 125   | (CF7, CFR)                                       |
| SN74LVC1G14DRLR.B                 | Active        | Production           | SOT-5X3 (DRL)   5 | 4000   LARGE T&R      | Yes         | NIPDAUAG                             | Level-1-260C-UNLIM                | -40 to 125   | (CF7, CFR)                                       |
| SN74LVC1G14DRLRG4                 | Active        | Production           | SOT-5X3 (DRL)   5 | 4000   LARGE T&R      | Yes         | NIPDAUAG                             | Level-1-260C-UNLIM                | -40 to 125   | (CF7, CFR)                                       |
| <a href="#">SN74LVC1G14DRYR</a>   | Active        | Production           | SON (DRY)   6     | 5000   LARGE T&R      | Yes         | NIPDAU                               | Level-1-260C-UNLIM                | -40 to 125   | CF                                               |
| SN74LVC1G14DRYR.B                 | Active        | Production           | SON (DRY)   6     | 5000   LARGE T&R      | Yes         | NIPDAU                               | Level-1-260C-UNLIM                | -40 to 125   | CF                                               |
| SN74LVC1G14DRYRG4.B               | Active        | Production           | SON (DRY)   6     | 5000   LARGE T&R      | Yes         | NIPDAU                               | Level-1-260C-UNLIM                | -40 to 125   | CF                                               |
| <a href="#">SN74LVC1G14DSFR</a>   | Active        | Production           | SON (DSF)   6     | 5000   LARGE T&R      | Yes         | NIPDAU   NIPDAUAG                    | Level-1-260C-UNLIM                | -40 to 125   | CF                                               |
| SN74LVC1G14DSFR.B                 | Active        | Production           | SON (DSF)   6     | 5000   LARGE T&R      | Yes         | NIPDAUAG                             | Level-1-260C-UNLIM                | -40 to 125   | CF                                               |
| SN74LVC1G14DSFRG4                 | Active        | Production           | SON (DSF)   6     | 5000   LARGE T&R      | Yes         | NIPDAU                               | Level-1-260C-UNLIM                | -40 to 125   | CF                                               |
| SN74LVC1G14DSFRG4.B               | Active        | Production           | SON (DSF)   6     | 5000   LARGE T&R      | Yes         | NIPDAU                               | Level-1-260C-UNLIM                | -40 to 125   | CF                                               |
| <a href="#">SN74LVC1G14YZPR</a>   | Active        | Production           | DSBGA (YZP)   5   | 3000   LARGE T&R      | Yes         | SNAGCU                               | Level-1-260C-UNLIM                | -40 to 85    | (CF7, CFN)                                       |
| SN74LVC1G14YZPR.B                 | Active        | Production           | DSBGA (YZP)   5   | 3000   LARGE T&R      | Yes         | SNAGCU                               | Level-1-260C-UNLIM                | -40 to 85    | (CF7, CFN)                                       |
| <a href="#">SN74LVC1G14YZVR</a>   | Active        | Production           | DSBGA (YZV)   4   | 3000   LARGE T&R      | Yes         | SNAGCU                               | Level-1-260C-UNLIM                | -40 to 85    | CF<br>(7, N)                                     |
| SN74LVC1G14YZVR.B                 | Active        | Production           | DSBGA (YZV)   4   | 3000   LARGE T&R      | Yes         | SNAGCU                               | Level-1-260C-UNLIM                | -40 to 85    | CF<br>(7, N)                                     |

<sup>(1)</sup> **Status:** For more details on status, see our [product life cycle](#).

<sup>(2)</sup> **Material type:** When designated, preproduction parts are prototypes/experimental devices, and are not yet approved or released for full production. Testing and final process, including without limitation quality assurance, reliability performance testing, and/or process qualification, may not yet be complete, and this item is subject to further changes or possible discontinuation. If available for ordering, purchases will be subject to an additional waiver at checkout, and are intended for early internal evaluation purposes only. These items are sold without warranties of any kind.

<sup>(3)</sup> **RoHS values:** Yes, No, RoHS Exempt. See the [TI RoHS Statement](#) for additional information and value definition.

<sup>(4)</sup> **Lead finish/Ball material:** Parts may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead finish/Ball material values may wrap to two lines if the finish value exceeds the maximum column width.

<sup>(5)</sup> **MSL rating/Peak reflow:** The moisture sensitivity level ratings and peak solder (reflow) temperatures. In the event that a part has multiple moisture sensitivity ratings, only the lowest level per JEDEC standards is shown. Refer to the shipping label for the actual reflow temperature that will be used to mount the part to the printed circuit board.

<sup>(6)</sup> **Part marking:** There may be an additional marking, which relates to the logo, the lot trace code information, or the environmental category of the part.

Multiple part markings will be inside parentheses. Only one part marking contained in parentheses and separated by a "~" will appear on a part. If a line is indented then it is a continuation of the previous line and the two combined represent the entire part marking for that device.

**Important Information and Disclaimer:** The information provided on this page represents TI's knowledge and belief as of the date that it is provided. TI bases its knowledge and belief on information provided by third parties, and makes no representation or warranty as to the accuracy of such information. Efforts are underway to better integrate information from third parties. TI has taken and continues to take reasonable steps to provide representative and accurate information but may not have conducted destructive testing or chemical analysis on incoming materials and chemicals. TI and TI suppliers consider certain information to be proprietary, and thus CAS numbers and other limited information may not be available for release.

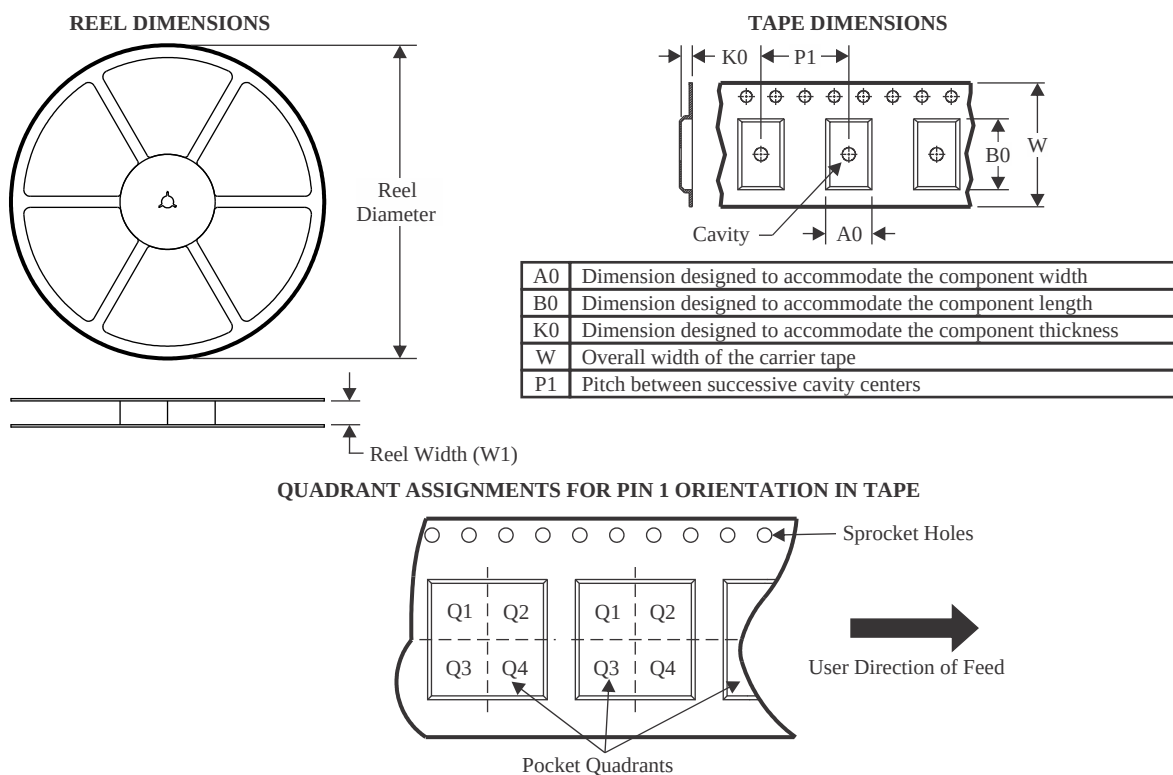
In no event shall TI's liability arising out of such information exceed the total purchase price of the TI part(s) at issue in this document sold by TI to Customer on an annual basis.

#### OTHER QUALIFIED VERSIONS OF SN74LVC1G14 :

- Automotive : [SN74LVC1G14-Q1](#)
- Enhanced Product : [SN74LVC1G14-EP](#)

NOTE: Qualified Version Definitions:

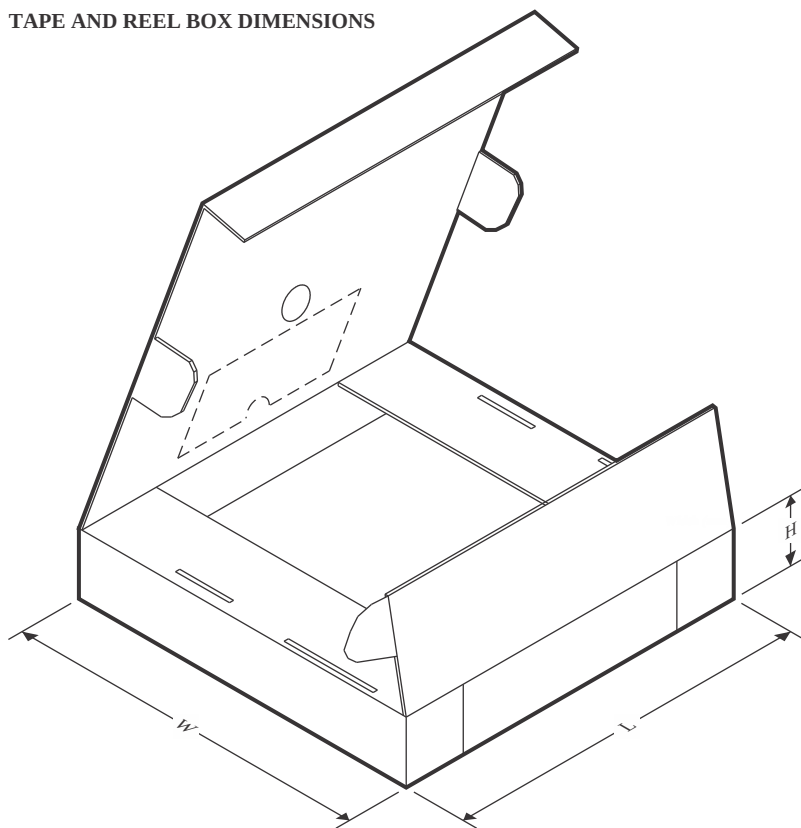
- Automotive - Q100 devices qualified for high-reliability automotive applications targeting zero defects
- Enhanced Product - Supports Defense, Aerospace and Medical Applications

**TAPE AND REEL INFORMATION**


\*All dimensions are nominal

| Device            | Package Type | Package Drawing | Pins | SPQ  | Reel Diameter (mm) | Reel Width W1 (mm) | A0 (mm) | B0 (mm) | K0 (mm) | P1 (mm) | W (mm) | Pin1 Quadrant |
|-------------------|--------------|-----------------|------|------|--------------------|--------------------|---------|---------|---------|---------|--------|---------------|
| SN74LVC1G14DBVR   | SOT-23       | DBV             | 5    | 3000 | 178.0              | 8.4                | 3.2     | 3.2     | 1.4     | 4.0     | 8.0    | Q3            |
| SN74LVC1G14DBVRG4 | SOT-23       | DBV             | 5    | 3000 | 178.0              | 9.0                | 3.23    | 3.17    | 1.37    | 4.0     | 8.0    | Q3            |
| SN74LVC1G14DBVT   | SOT-23       | DBV             | 5    | 250  | 178.0              | 9.0                | 3.3     | 3.2     | 1.4     | 4.0     | 8.0    | Q3            |
| SN74LVC1G14DBVTG4 | SOT-23       | DBV             | 5    | 250  | 178.0              | 9.0                | 3.23    | 3.17    | 1.37    | 4.0     | 8.0    | Q3            |
| SN74LVC1G14DCKR   | SC70         | DCK             | 5    | 3000 | 178.0              | 8.4                | 2.25    | 2.45    | 1.2     | 4.0     | 8.0    | Q3            |
| SN74LVC1G14DCKT   | SC70         | DCK             | 5    | 250  | 178.0              | 9.0                | 2.4     | 2.5     | 1.2     | 4.0     | 8.0    | Q3            |
| SN74LVC1G14DCKTG4 | SC70         | DCK             | 5    | 250  | 178.0              | 9.2                | 2.4     | 2.4     | 1.22    | 4.0     | 8.0    | Q3            |
| SN74LVC1G14DPWR   | X2SON        | DPW             | 5    | 3000 | 178.0              | 8.4                | 0.91    | 0.91    | 0.5     | 2.0     | 8.0    | Q3            |
| SN74LVC1G14DRLR   | SOT-5X3      | DRL             | 5    | 4000 | 180.0              | 8.4                | 1.98    | 1.78    | 0.69    | 4.0     | 8.0    | Q3            |
| SN74LVC1G14DRYR   | SON          | DRY             | 6    | 5000 | 180.0              | 9.5                | 1.2     | 1.65    | 0.7     | 4.0     | 8.0    | Q1            |
| SN74LVC1G14DSFR   | SON          | DSF             | 6    | 5000 | 180.0              | 9.5                | 1.16    | 1.16    | 0.5     | 4.0     | 8.0    | Q2            |
| SN74LVC1G14DSFRG4 | SON          | DSF             | 6    | 5000 | 180.0              | 9.5                | 1.16    | 1.16    | 0.5     | 4.0     | 8.0    | Q2            |
| SN74LVC1G14YZPR   | DSBGA        | YZP             | 5    | 3000 | 178.0              | 9.2                | 1.02    | 1.52    | 0.63    | 4.0     | 8.0    | Q1            |
| SN74LVC1G14YZVR   | DSBGA        | YZV             | 4    | 3000 | 178.0              | 9.2                | 1.0     | 1.0     | 0.63    | 4.0     | 8.0    | Q1            |

## TAPE AND REEL BOX DIMENSIONS

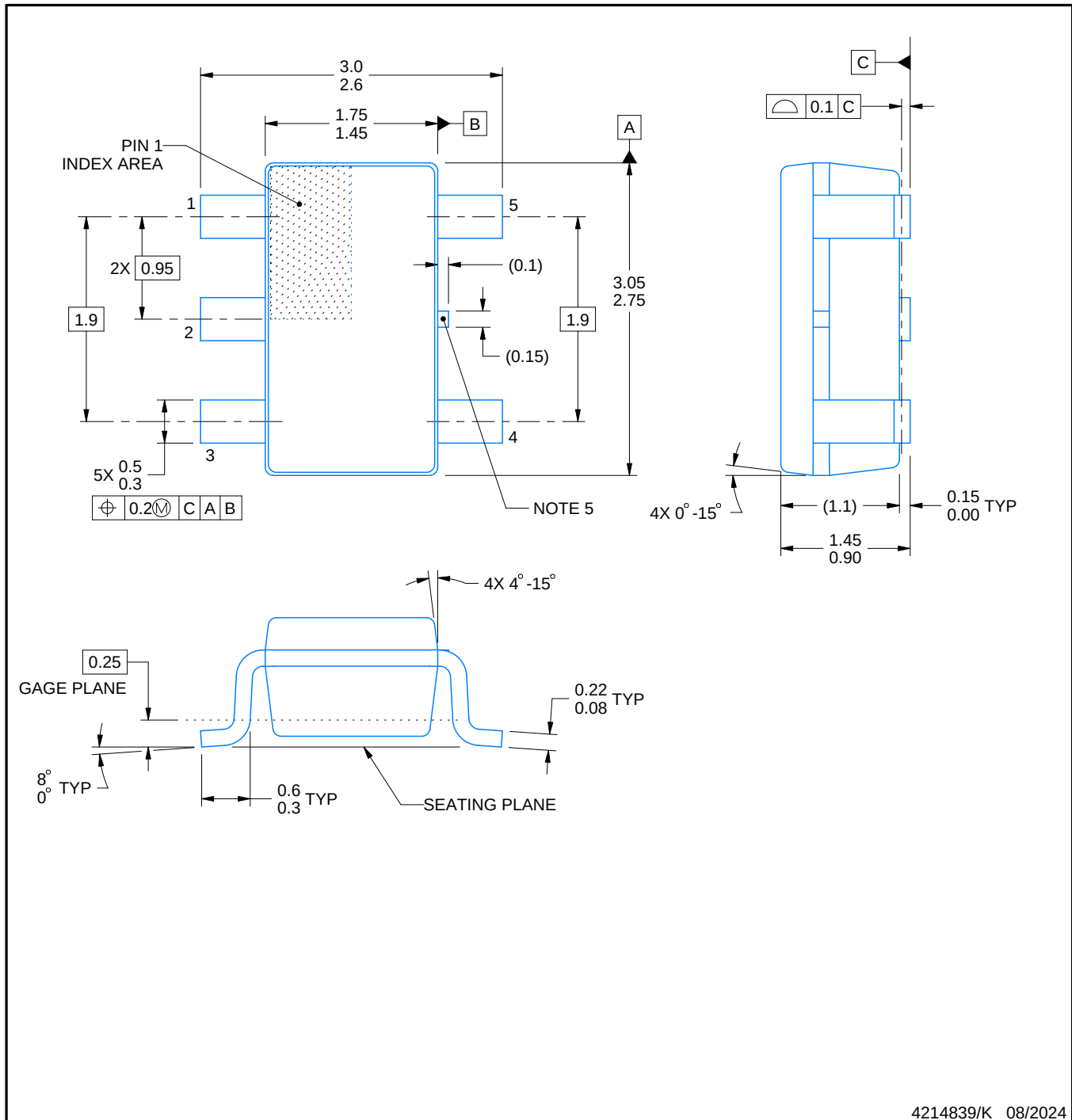


\*All dimensions are nominal

| Device            | Package Type | Package Drawing | Pins | SPQ  | Length (mm) | Width (mm) | Height (mm) |
|-------------------|--------------|-----------------|------|------|-------------|------------|-------------|
| SN74LVC1G14DBVR   | SOT-23       | DBV             | 5    | 3000 | 208.0       | 191.0      | 35.0        |
| SN74LVC1G14DBVRG4 | SOT-23       | DBV             | 5    | 3000 | 180.0       | 180.0      | 18.0        |
| SN74LVC1G14DBVT   | SOT-23       | DBV             | 5    | 250  | 180.0       | 180.0      | 18.0        |
| SN74LVC1G14DBVTG4 | SOT-23       | DBV             | 5    | 250  | 180.0       | 180.0      | 18.0        |
| SN74LVC1G14DCKR   | SC70         | DCK             | 5    | 3000 | 208.0       | 191.0      | 35.0        |
| SN74LVC1G14DCKT   | SC70         | DCK             | 5    | 250  | 180.0       | 180.0      | 18.0        |
| SN74LVC1G14DCKTG4 | SC70         | DCK             | 5    | 250  | 180.0       | 180.0      | 18.0        |
| SN74LVC1G14DPWR   | X2SON        | DPW             | 5    | 3000 | 205.0       | 200.0      | 33.0        |
| SN74LVC1G14DRLR   | SOT-5X3      | DRL             | 5    | 4000 | 202.0       | 201.0      | 28.0        |
| SN74LVC1G14DRYR   | SON          | DRY             | 6    | 5000 | 189.0       | 185.0      | 36.0        |
| SN74LVC1G14DSFR   | SON          | DSF             | 6    | 5000 | 184.0       | 184.0      | 19.0        |
| SN74LVC1G14DSFRG4 | SON          | DSF             | 6    | 5000 | 184.0       | 184.0      | 19.0        |
| SN74LVC1G14YZPR   | DSBGA        | YZP             | 5    | 3000 | 220.0       | 220.0      | 35.0        |
| SN74LVC1G14YZVR   | DSBGA        | YZV             | 4    | 3000 | 220.0       | 220.0      | 35.0        |

**DBV0005A****PACKAGE OUTLINE****SOT-23 - 1.45 mm max height**

SMALL OUTLINE TRANSISTOR

**NOTES:**

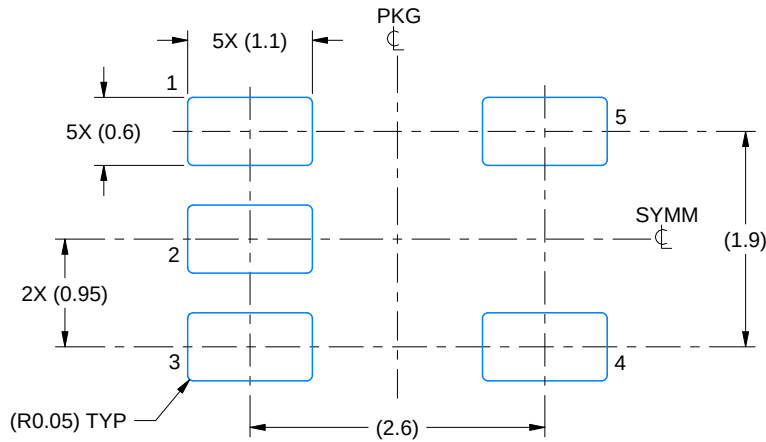
1. All linear dimensions are in millimeters. Any dimensions in parenthesis are for reference only. Dimensioning and tolerancing per ASME Y14.5M.
2. This drawing is subject to change without notice.
3. Reference JEDEC MO-178.
4. Body dimensions do not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, or gate burrs shall not exceed 0.25 mm per side.
5. Support pin may differ or may not be present.

# EXAMPLE BOARD LAYOUT

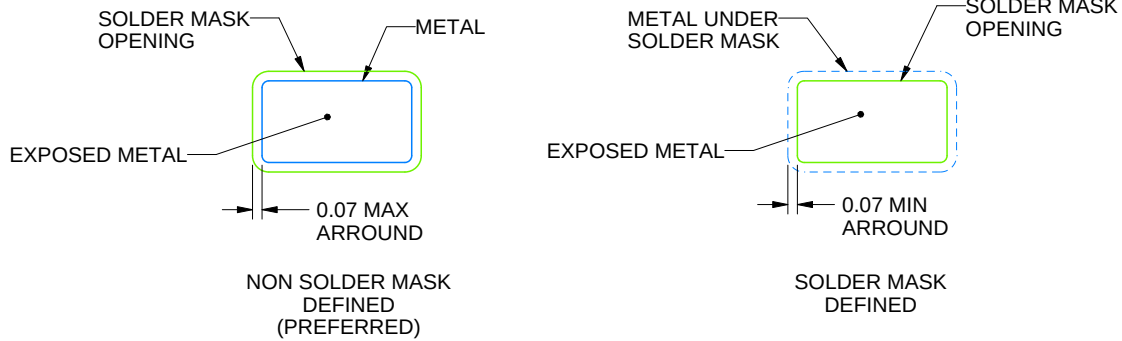
DBV0005A

SOT-23 - 1.45 mm max height

SMALL OUTLINE TRANSISTOR



LAND PATTERN EXAMPLE  
EXPOSED METAL SHOWN  
SCALE:15X



SOLDER MASK DETAILS

4214839/K 08/2024

NOTES: (continued)

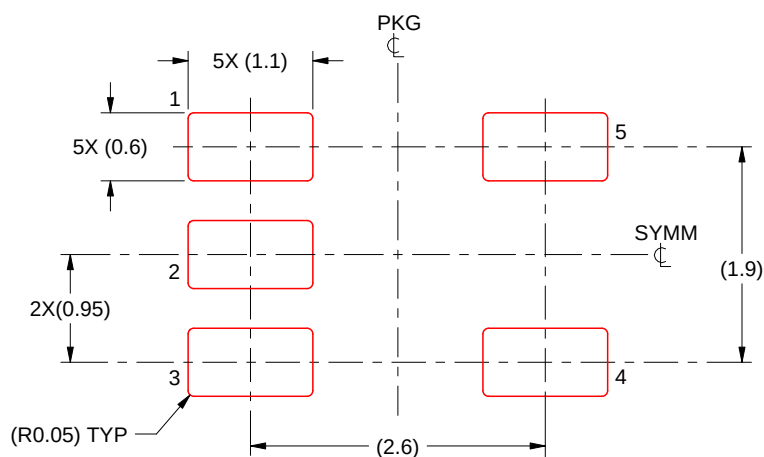
6. Publication IPC-7351 may have alternate designs.
7. Solder mask tolerances between and around signal pads can vary based on board fabrication site.

## EXAMPLE STENCIL DESIGN

DBV0005A

SOT-23 - 1.45 mm max height

SMALL OUTLINE TRANSISTOR



SOLDER PASTE EXAMPLE  
BASED ON 0.125 mm THICK STENCIL  
SCALE:15X

4214839/K 08/2024

NOTES: (continued)

8. Laser cutting apertures with trapezoidal walls and rounded corners may offer better paste release. IPC-7525 may have alternate design recommendations.
9. Board assembly site may have different recommendations for stencil design.



## GENERIC PACKAGE VIEW

**DRY 6**

**USON - 0.6 mm max height**

PLASTIC SMALL OUTLINE - NO LEAD



Images above are just a representation of the package family, actual package may vary.  
Refer to the product data sheet for package details.

4207181/G

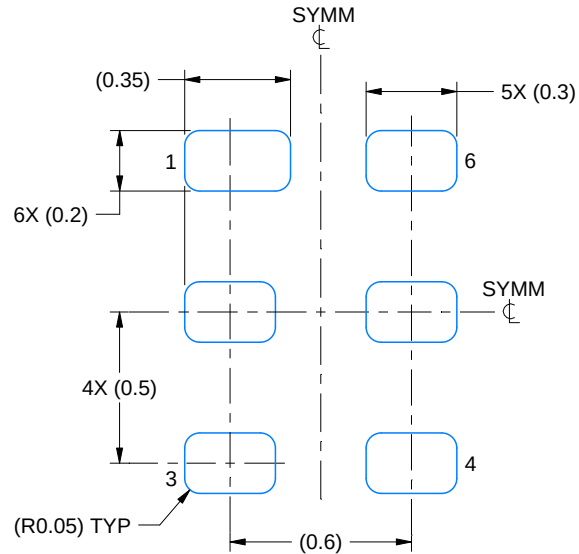


# EXAMPLE BOARD LAYOUT

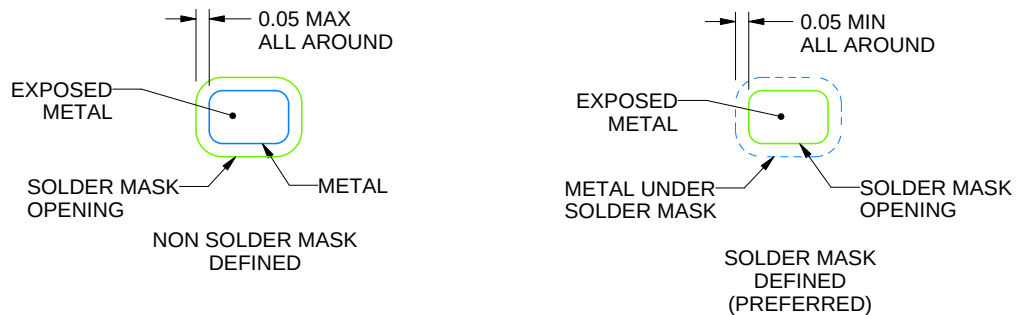
DRY0006A

USON - 0.6 mm max height

PLASTIC SMALL OUTLINE - NO LEAD



**LAND PATTERN EXAMPLE**  
1:1 RATIO WITH PKG SOLDER PADS  
EXPOSED METAL SHOWN  
SCALE:40X



**SOLDER MASK DETAILS**

4222894/A 01/2018

NOTES: (continued)

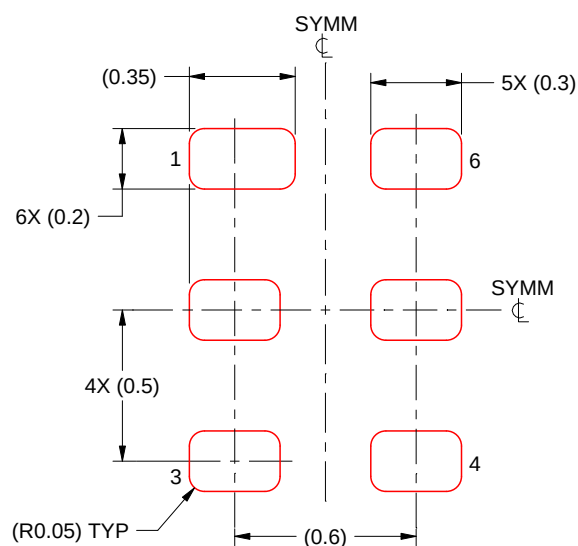
3. For more information, see QFN/SON PCB application report in literature No. SLUA271 ([www.ti.com/lit/sluea271](http://www.ti.com/lit/sluea271)).

## EXAMPLE STENCIL DESIGN

DRY0006A

USON - 0.6 mm max height

PLASTIC SMALL OUTLINE - NO LEAD



SOLDER PASTE EXAMPLE  
BASED ON 0.075 - 0.1 mm THICK STENCIL  
SCALE:40X

4222894/A 01/2018

NOTES: (continued)

4. Laser cutting apertures with trapezoidal walls and rounded corners may offer better paste release. IPC-7525 may have alternate design recommendations.



## PACKAGE OUTLINE

### USON - 0.55 mm max height

PLASTIC SMALL OUTLINE - NO LEAD



NOTES:

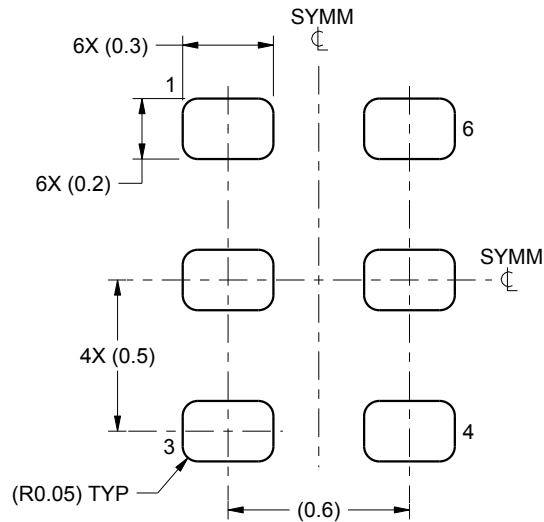
1. All linear dimensions are in millimeters. Any dimensions in parenthesis are for reference only. Dimensioning and tolerancing per ASME Y14.5M.
2. This drawing is subject to change without notice.

# EXAMPLE BOARD LAYOUT

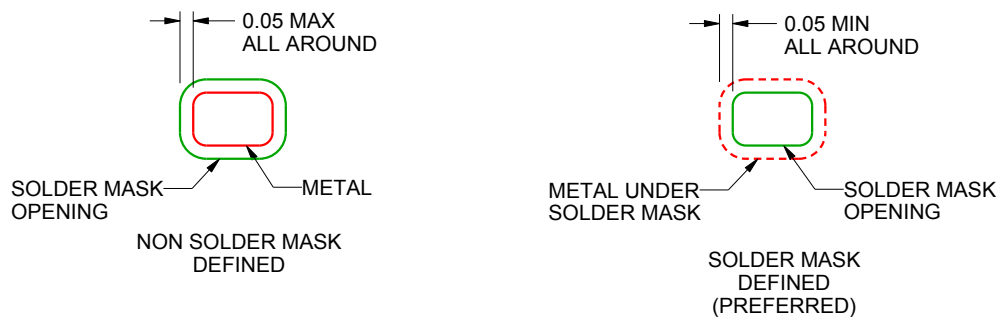
DRY0006B

USON - 0.55 mm max height

PLASTIC SMALL OUTLINE - NO LEAD



LAND PATTERN EXAMPLE  
1:1 RATIO WITH PKG SOLDER PADS  
SCALE:40X



SOLDER MASK DETAILS

4222207/B 02/2016

NOTES: (continued)

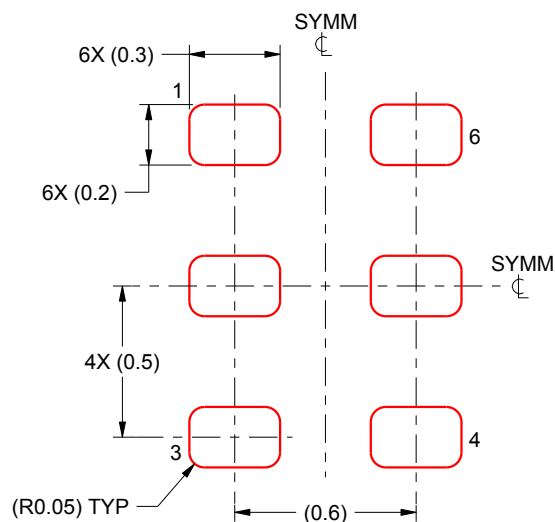
3. For more information, see QFN/SON PCB application report in literature No. SLUA271 ([www.ti.com/lit/sluea271](http://www.ti.com/lit/sluea271)).

## EXAMPLE STENCIL DESIGN

DRY0006B

USON - 0.55 mm max height

PLASTIC SMALL OUTLINE - NO LEAD



SOLDER PASTE EXAMPLE  
BASED ON 0.075 - 0.1 mm THICK STENCIL  
SCALE:40X

4222207/B 02/2016

NOTES: (continued)

4. Laser cutting apertures with trapezoidal walls and rounded corners may offer better paste release. IPC-7525 may have alternate design recommendations.

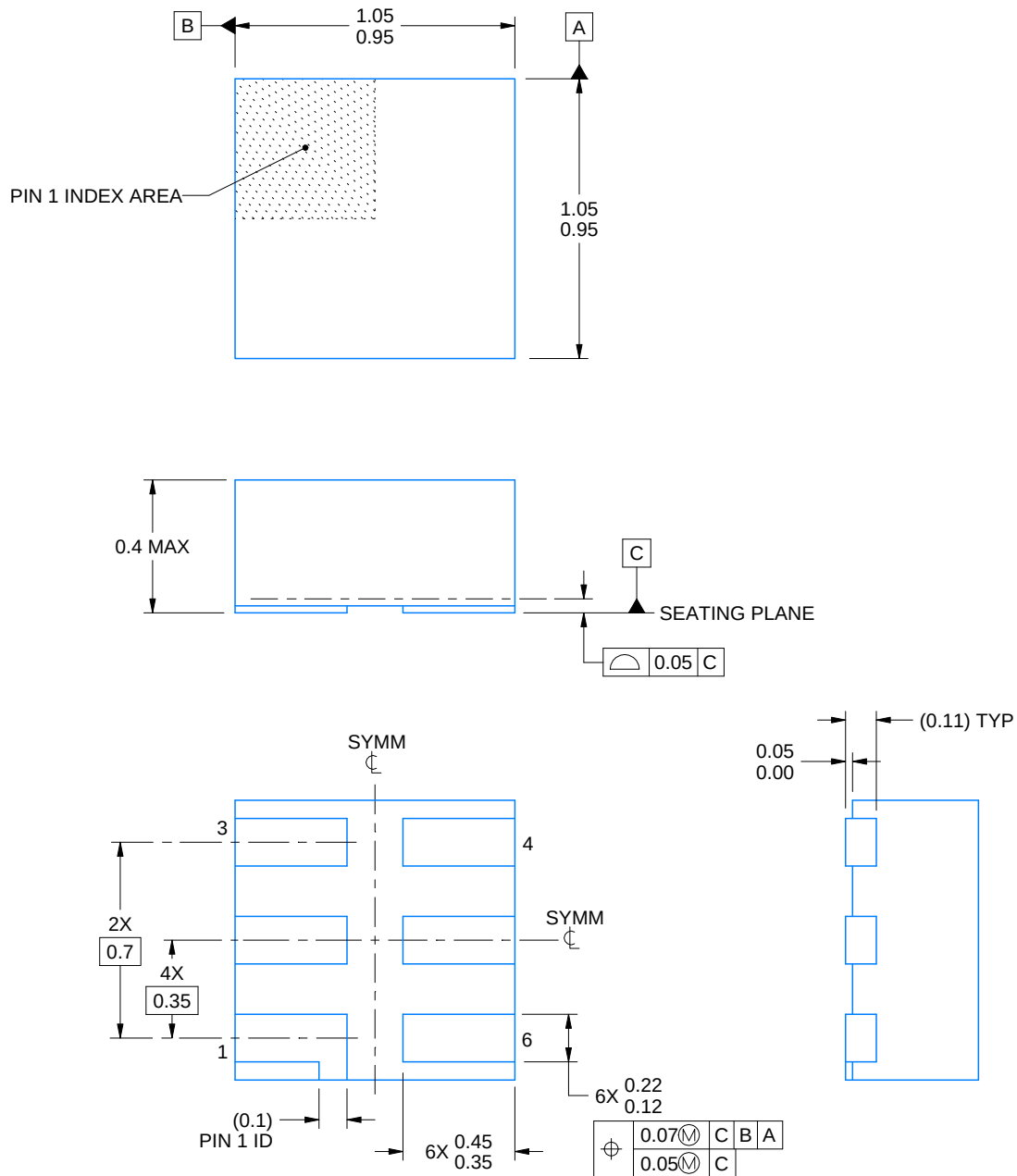


DSF0006A

## PACKAGE OUTLINE

X2SON - 0.4 mm max height

PLASTIC SMALL OUTLINE - NO LEAD



4220597/B 06/2022

### NOTES:

1. All linear dimensions are in millimeters. Any dimensions in parenthesis are for reference only. Dimensioning and tolerancing per ASME Y14.5M.
2. This drawing is subject to change without notice.
3. Reference JEDEC registration MO-287, variation X2AAF.

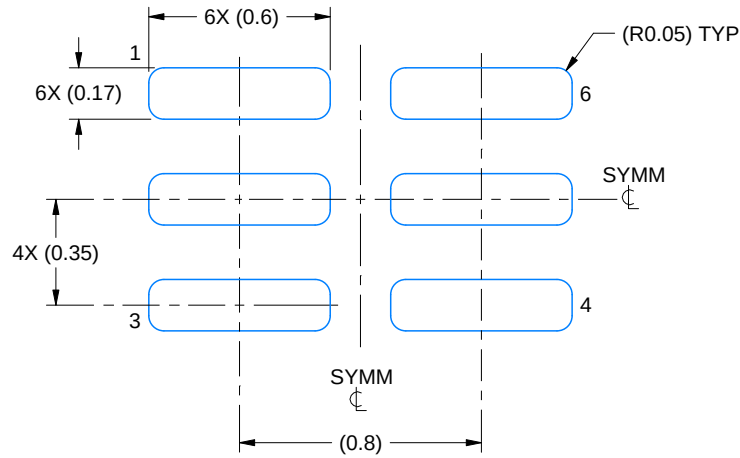


# EXAMPLE BOARD LAYOUT

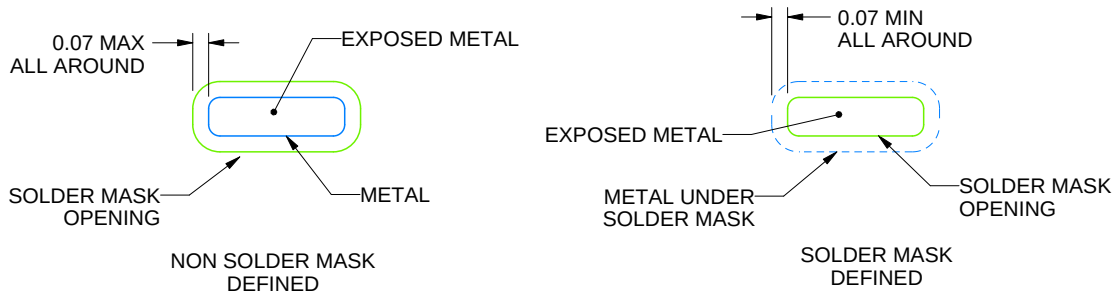
DSF0006A

X2SON - 0.4 mm max height

PLASTIC SMALL OUTLINE - NO LEAD



LAND PATTERN EXAMPLE  
EXPOSED METAL SHOWN  
SCALE:40X



SOLDER MASK DETAILS

4220597/B 06/2022

NOTES: (continued)

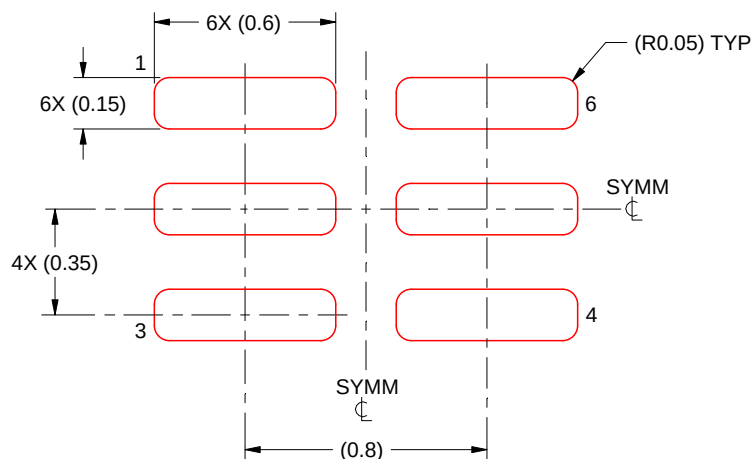
4. For more information, see Texas Instruments literature number SLUA271 ([www.ti.com/lit/sluea271](http://www.ti.com/lit/sluea271)).

## EXAMPLE STENCIL DESIGN

DSF0006A

X2SON - 0.4 mm max height

PLASTIC SMALL OUTLINE - NO LEAD



SOLDER PASTE EXAMPLE  
BASED ON 0.09 mm THICK STENCIL

PRINTED SOLDER COVERAGE BY AREA UNDER PACKAGE  
SCALE:40X

4220597/B 06/2022

4. Laser cutting apertures with trapezoidal walls and rounded corners may offer better paste release. IPC-7525 may have alternate design recommendations.



**SOT - 0.6 mm max height**

Technical drawing of a 5-pin D-subminiature connector, showing front, side, and end views with dimensions and tolerances.

**Front View:**

- Overall width: 1.5
- Overall height: 1.7
- Pin 1 ID Area: Indicated by a dashed line and label.
- Pin 1: Located at the top left.
- Pin 2: Located below Pin 1.
- Pin 3: Located below Pin 2.
- Pin 4: Located at the bottom right.
- Pin 5: Located at the top right.
- Dimensions: 1.3, 1.1, 5X 0.3, 0.1.
- Feature A: Located at the top right corner.
- Feature B: Located at the bottom left corner.
- Feature C: Located at the bottom right corner.

**Side View:**

- Overall height: 0.6 MAX
- Overall width: 5X 0.18, 0.08
- Feature A: Located at the top right corner.
- Feature B: Located at the bottom left corner.
- Feature C: Located at the bottom right corner.
- SEATING PLANE: Indicated by a dashed line and label.
- Feature D: Located at the bottom right corner.

**End View:**

- Overall width: 1.7
- Overall height: 1.5
- Feature A: Located at the top right corner.
- Feature B: Located at the bottom left corner.
- Feature C: Located at the bottom right corner.
- Feature D: Located at the bottom right corner.

**Dimensions and Tolerances:**

- 1.5
- 1.7
- 1.3
- 1.1
- 5X 0.3
- 0.1
- 0.6 MAX
- 5X 0.18
- 0.08
- 0.05
- 0.00 TYP
- 2X 0° -10°
- 2X 4° -15°
- 0.1
- 0.05
- 0.27
- 0.15
- 0.4
- 0.2

**Feature Callouts:**

- PIN 1 ID AREA
- SEATING PLANE
- SYMM
- SYMM

**Feature A:** 0.1

**Feature B:** 0.05

**Feature C:** 0.27

**Feature D:** 0.15

**Feature E:** 0.4

**Feature F:** 0.2

**Feature G:** 0.1

**Feature H:** 0.05

**Feature I:** 0.27

**Feature J:** 0.15

**Feature K:** 0.4

**Feature L:** 0.2

**Feature M:** 0.1

**Feature N:** 0.05

**Feature O:** 0.27

**Feature P:** 0.15

**Feature Q:** 0.4

**Feature R:** 0.2

**Feature S:** 0.1

**Feature T:** 0.05

**Feature U:** 0.27

**Feature V:** 0.15

**Feature W:** 0.4

**Feature X:** 0.2

**Feature Y:** 0.1

**Feature Z:** 0.05

**Feature AA:** 0.27

**Feature AB:** 0.15

**Feature AC:** 0.4

**Feature AD:** 0.2

**Feature AE:** 0.1

**Feature AF:** 0.05

**Feature AG:** 0.27

**Feature AH:** 0.15

**Feature AI:** 0.4

**Feature AJ:** 0.2

**Feature AK:** 0.1

**Feature AL:** 0.05

**Feature AM:** 0.27

**Feature AN:** 0.15

**Feature AO:** 0.4

**Feature AP:** 0.2

**Feature AQ:** 0.1

**Feature AR:** 0.05

**Feature AS:** 0.27

**Feature AT:** 0.15

**Feature AU:** 0.4

**Feature AV:** 0.2

**Feature AW:** 0.1

**Feature AX:** 0.05

**Feature AY:** 0.27

**Feature AZ:** 0.15

**Feature BA:** 0.4

**Feature BB:** 0.2

**Feature BC:** 0.1

**Feature BD:** 0.05

**Feature BE:** 0.27

**Feature BF:** 0.15

**Feature BG:** 0.4

**Feature BH:** 0.2

**Feature BI:** 0.1

**Feature BJ:** 0.05

**Feature BK:** 0.27

**Feature BL:** 0.15

**Feature BM:** 0.4

**Feature BN:** 0.2

**Feature BO:** 0.1

**Feature BP:** 0.05

**Feature BQ:** 0.27

**Feature BR:** 0.15

**Feature BS:** 0.4

**Feature BT:** 0.2

**Feature BU:** 0.1

**Feature BV:** 0.05

**Feature BW:** 0.27

**Feature BX:** 0.15

**Feature BY:** 0.4

**Feature BZ:** 0.2

**Feature CA:** 0.1

**Feature CB:** 0.05

**Feature CC:** 0.27

**Feature CD:** 0.15

**Feature CE:** 0.4

**Feature CF:** 0.2

**Feature CG:** 0.1

**Feature CH:** 0.05

**Feature CI:** 0.27

**Feature CJ:** 0.15

**Feature CK:** 0.4

**Feature CL:** 0.2

**Feature CM:** 0.1

**Feature CN:** 0.05

**Feature CO:** 0.27

**Feature CP:** 0.15

**Feature CQ:** 0.4

**Feature CR:** 0.2

**Feature CS:** 0.1

**Feature CT:** 0.05

**Feature CU:** 0.27

**Feature CV:** 0.15

**Feature CW:** 0.4

**Feature CX:** 0.2

**Feature CY:** 0.1

**Feature CZ:** 0.05

**Feature DA:** 0.27

**Feature DB:** 0.15

**Feature DC:** 0.4

**Feature DD:** 0.2

**Feature DE:** 0.1

**Feature DF:** 0.05

**Feature DG:** 0.27

**Feature DH:** 0.15

**Feature DI:** 0.4

**Feature DJ:** 0.2

**Feature DK:** 0.1

**Feature DL:** 0.05

**Feature DM:** 0.27

**Feature DN:** 0.15

**Feature DO:** 0.4

**Feature DP:** 0.2

**Feature DQ:** 0.1

**Feature DR:** 0.05

**Feature DS:** 0.27

**Feature DT:** 0.15

**Feature DU:** 0.4

**Feature DV:** 0.2

**Feature DW:** 0.1

**Feature DX:** 0.05

**Feature DY:** 0.27

**Feature DZ:** 0.15

**Feature EA:** 0.4

**Feature EB:** 0.2

**Feature EC:** 0.1

**Feature ED:** 0.05

**Feature EE:** 0.27

**Feature EF:** 0.15

**Feature EG:** 0.4

**Feature EH:** 0.2

**Feature EI:** 0.1

**Feature EJ:** 0.05

**Feature EK:** 0.27

**Feature EL:** 0.15

**Feature EM:** 0.4

**Feature EN:** 0.2

**Feature EO:** 0.1

**Feature EP:** 0.05

**Feature EQ:** 0.27

**Feature ER:** 0.15

**Feature ES:** 0.4

**Feature ET:** 0.2

**Feature EU:** 0.1

**Feature EV:** 0.05

**Feature EW:** 0.27

**Feature EX:** 0.15

**Feature EY:** 0.4

**Feature EZ:** 0.2

**Feature FA:** 0.1

**Feature FB:** 0.05

**Feature FC:** 0.27

**Feature FD:** 0.15

**Feature FE:** 0.4

**Feature FF:** 0.2

**Feature FG:** 0.1

**Feature FH:** 0.05

**Feature FI:** 0.27

**Feature FJ:** 0.15

**Feature FK:** 0.4

**Feature FL:** 0.2

**Feature FM:** 0.1

**Feature FN:** 0.05

**Feature FO:** 0.27

**Feature FP:** 0.15

**Feature FQ:** 0.4

**Feature FR:** 0.2

**Feature FS:** 0.1

**Feature FT:** 0.05

**Feature FU:** 0.27

**Feature FV:** 0.15

**Feature FW:** 0.4

**Feature FX:** 0.2

**Feature FY:** 0.1

**Feature FZ:** 0.05

**Feature GA:** 0.27

**Feature GB:** 0.15

**Feature GC:** 0.4

**Feature GD:** 0.2

**Feature GE:** 0.1

**Feature GF:** 0.05

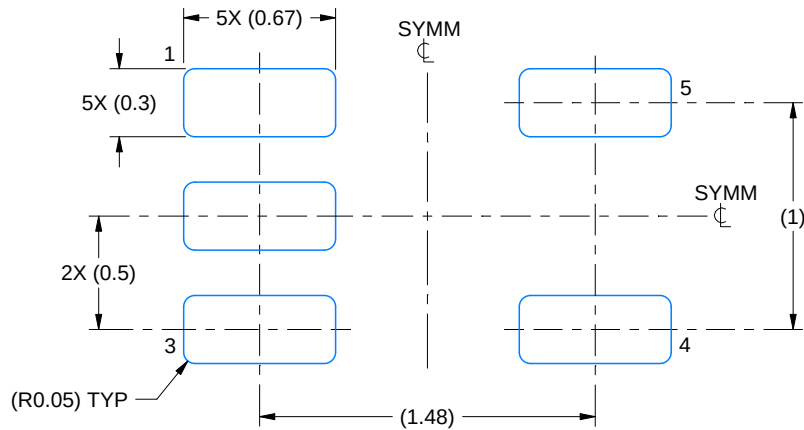
1. All linear dimensions are in millimeters. Any dimensions in parenthesis are for reference only. Dimensioning and tolerancing per ASME Y14.5M.
2. This drawing is subject to change without notice.
3. This dimension does not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, or gate burrs shall not exceed 0.15 mm per side.
4. Reference JEDEC registration MO-293 Variation UAAD-1

# EXAMPLE BOARD LAYOUT

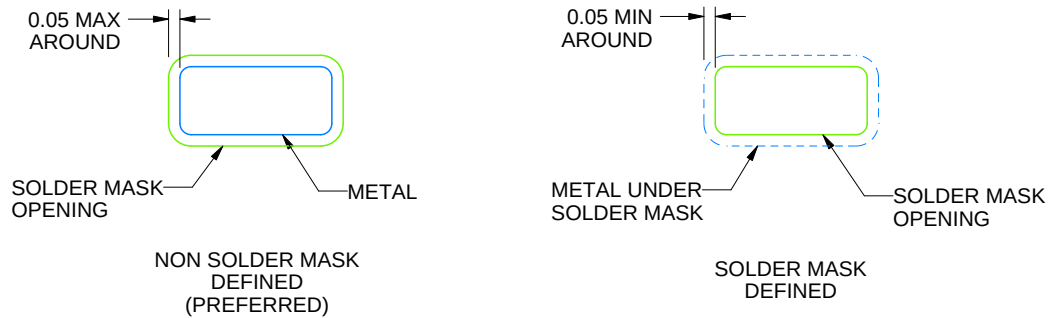
DRL0005A

SOT - 0.6 mm max height

PLASTIC SMALL OUTLINE



LAND PATTERN EXAMPLE  
SCALE:30X



SOLDERMASK DETAILS

4220753/E 11/2024

NOTES: (continued)

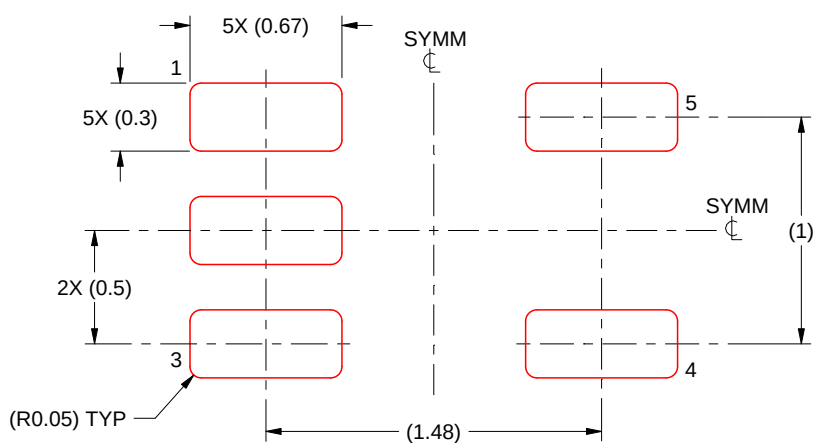
5. Publication IPC-7351 may have alternate designs.
6. Solder mask tolerances between and around signal pads can vary based on board fabrication site.

## EXAMPLE STENCIL DESIGN

DRL0005A

SOT - 0.6 mm max height

PLASTIC SMALL OUTLINE



SOLDER PASTE EXAMPLE  
BASED ON 0.1 mm THICK STENCIL  
SCALE:30X

4220753/E 11/2024

NOTES: (continued)

7. Laser cutting apertures with trapezoidal walls and rounded corners may offer better paste release. IPC-7525 may have alternate design recommendations.
8. Board assembly site may have different recommendations for stencil design.

## GENERIC PACKAGE VIEW

**DPW 5**

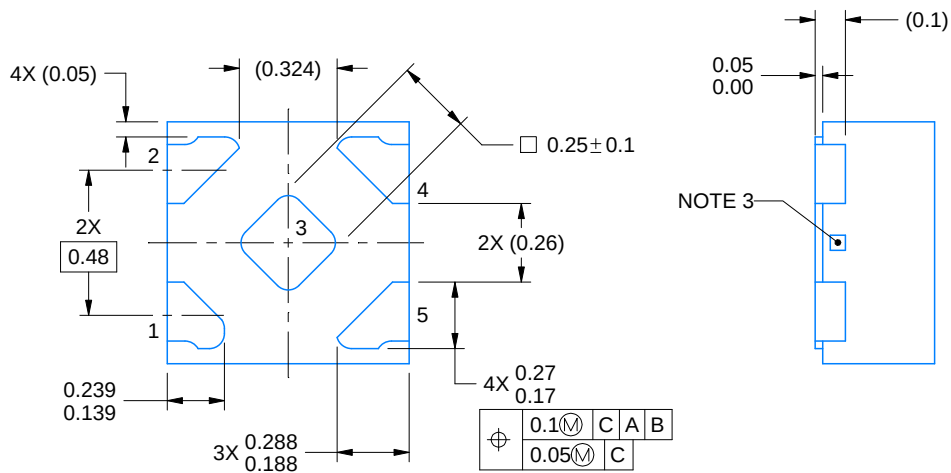
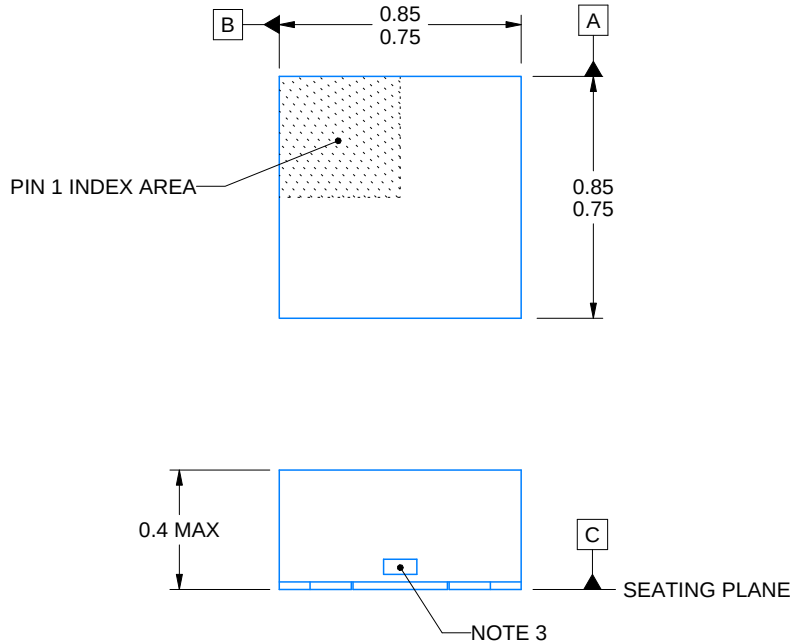
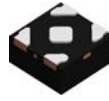
**X2SON - 0.4 mm max height**

PLASTIC SMALL OUTLINE - NO LEAD



Images above are just a representation of the package family, actual package may vary.  
Refer to the product data sheet for package details.

4211218-3/D



4223102/D 03/2022

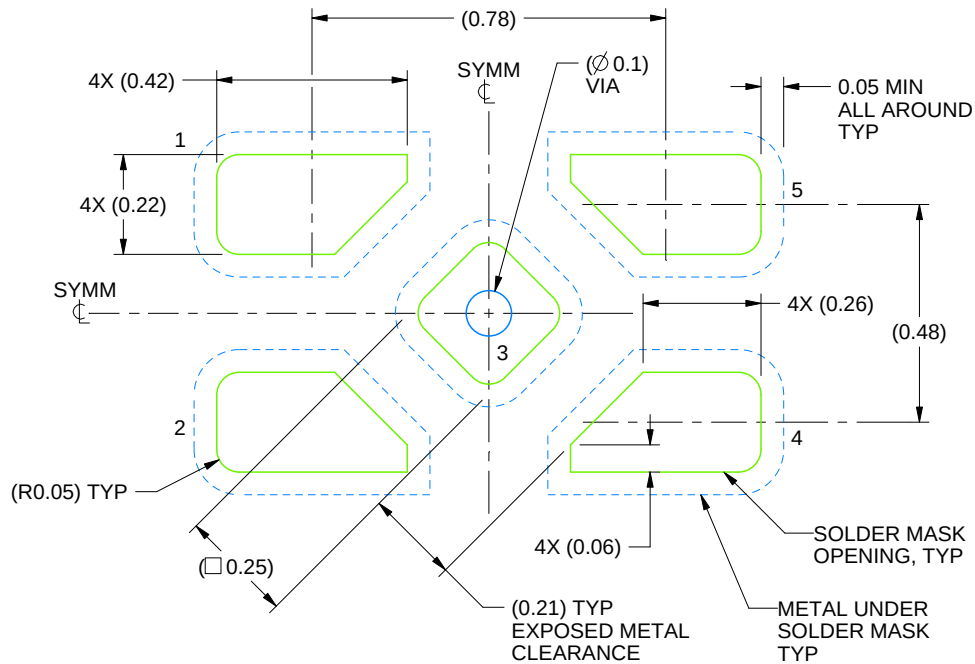
## NOTES:

1. All linear dimensions are in millimeters. Any dimensions in parenthesis are for reference only. Dimensioning and tolerancing per ASME Y14.5M.
2. This drawing is subject to change without notice.
3. The size and shape of this feature may vary.

**DPW0005A**

**X2SON - 0.4 mm max height**

PLASTIC SMALL OUTLINE - NO LEAD



LAND PATTERN EXAMPLE  
SOLDER MASK DEFINED  
SCALE:60X

4223102/D 03/2022

NOTES: (continued)

4. This package is designed to be soldered to a thermal pad on the board. For more information, refer to QFN/SON PCB application note in literature No. SLUA271 ([www.ti.com/lit/slua271](http://www.ti.com/lit/slua271)).

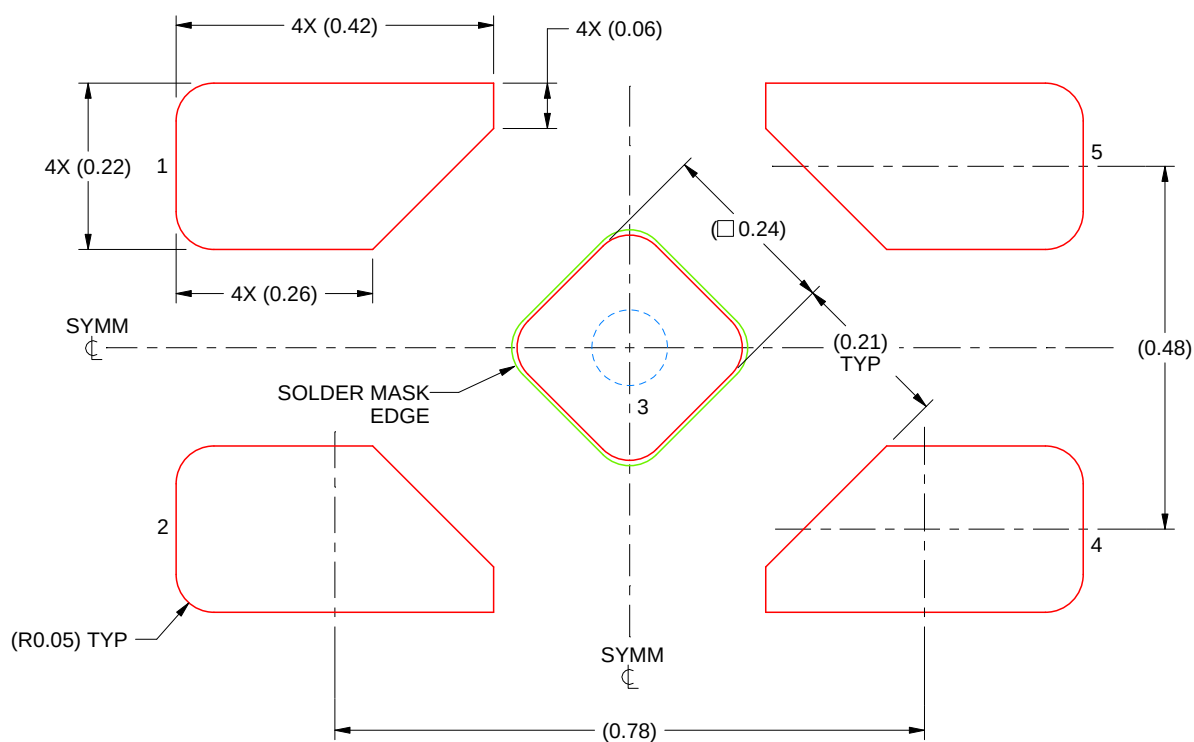


# EXAMPLE STENCIL DESIGN

DPW0005A

X2SON - 0.4 mm max height

PLASTIC SMALL OUTLINE - NO LEAD



SOLDER PASTE EXAMPLE  
BASED ON 0.1 mm THICK STENCIL

EXPOSED PAD 3  
92% PRINTED SOLDER COVERAGE BY AREA UNDER PACKAGE  
SCALE:100X

4223102/D 03/2022

NOTES: (continued)

5. Laser cutting apertures with trapezoidal walls and rounded corners may offer better paste release. IPC-7525 may have alternate design recommendations.

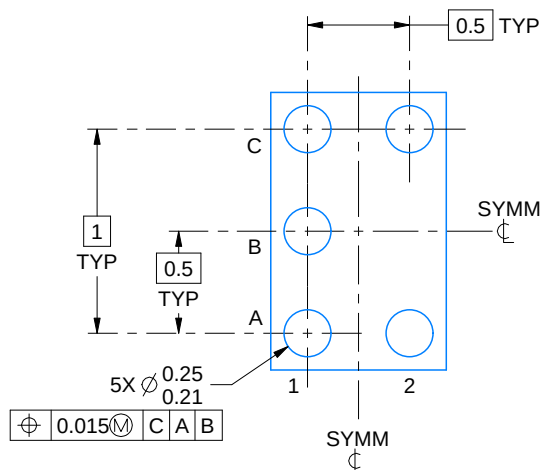
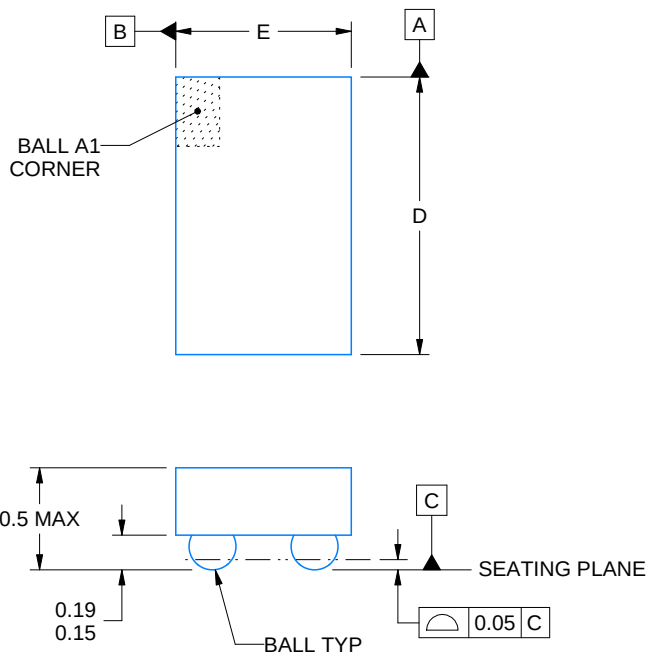
YZP0005



# PACKAGE OUTLINE

DSBGA - 0.5 mm max height

DIE SIZE BALL GRID ARRAY



D: Max = 1.418 mm, Min = 1.358 mm  
E: Max = 0.918 mm, Min = 0.858 mm

4219492/A 05/2017

## NOTES:

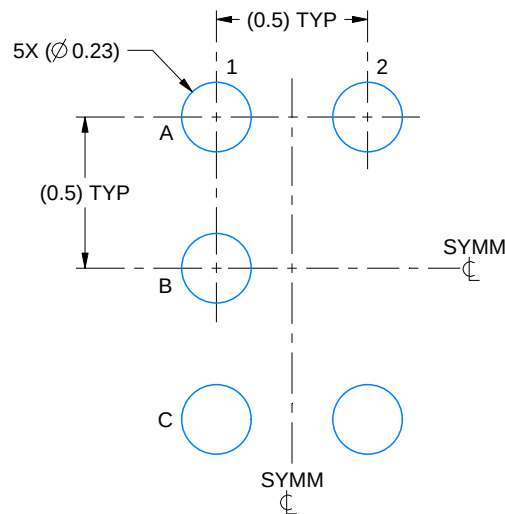
1. All linear dimensions are in millimeters. Any dimensions in parenthesis are for reference only. Dimensioning and tolerancing per ASME Y14.5M.
2. This drawing is subject to change without notice.

# EXAMPLE BOARD LAYOUT

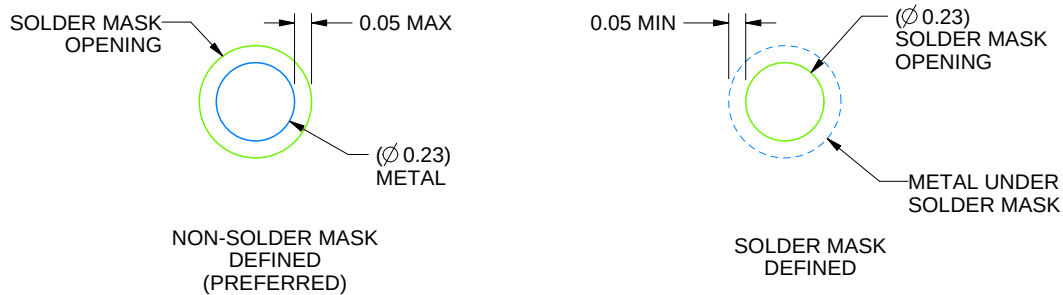
YZP0005

DSBGA - 0.5 mm max height

DIE SIZE BALL GRID ARRAY



LAND PATTERN EXAMPLE  
SCALE:40X



SOLDER MASK DETAILS  
NOT TO SCALE

4219492/A 05/2017

NOTES: (continued)

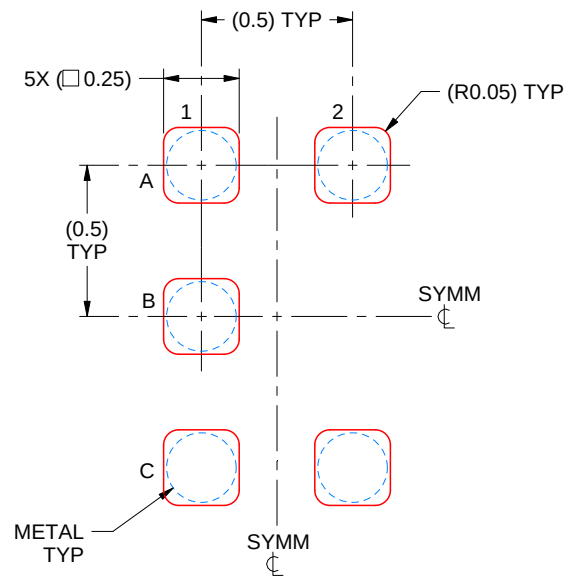
- Final dimensions may vary due to manufacturing tolerance considerations and also routing constraints. For more information, see Texas Instruments literature number SNVA009 ([www.ti.com/lit/snva009](http://www.ti.com/lit/snva009)).

## EXAMPLE STENCIL DESIGN

YZP0005

DSBGA - 0.5 mm max height

DIE SIZE BALL GRID ARRAY



SOLDER PASTE EXAMPLE  
BASED ON 0.1 mm THICK STENCIL  
SCALE:40X

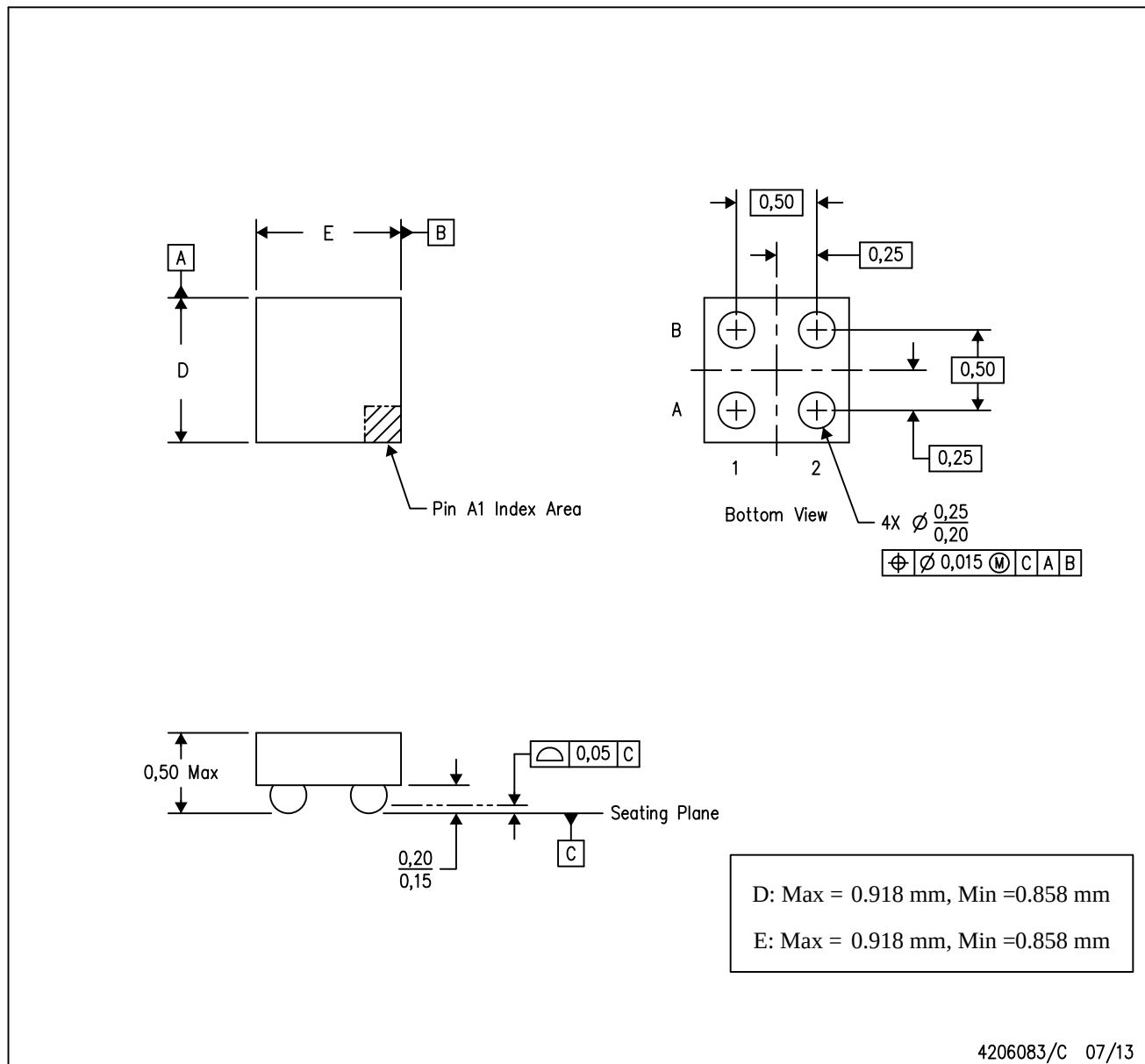
4219492/A 05/2017

NOTES: (continued)

4. Laser cutting apertures with trapezoidal walls and rounded corners may offer better paste release.

YZV (S-XBGA-N4)

DIE-SIZE BALL GRID ARRAY



- NOTES:
- A. All linear dimensions are in millimeters. Dimensioning and tolerancing per ASME Y14.5M-1994.
  - B. This drawing is subject to change without notice.
  - C. NanoFree™ package configuration.

NanoFree is a trademark of Texas Instruments.

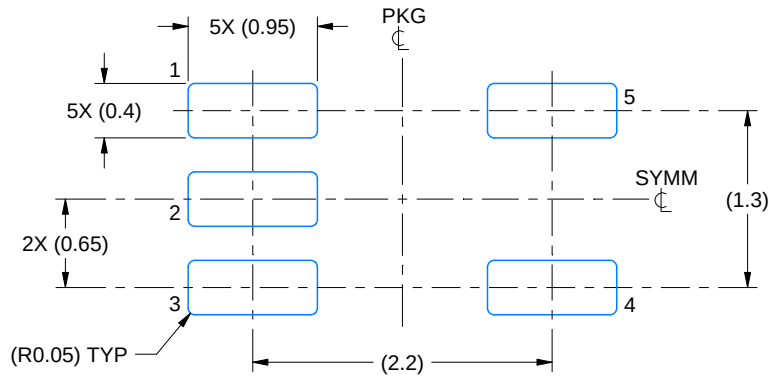


# EXAMPLE BOARD LAYOUT

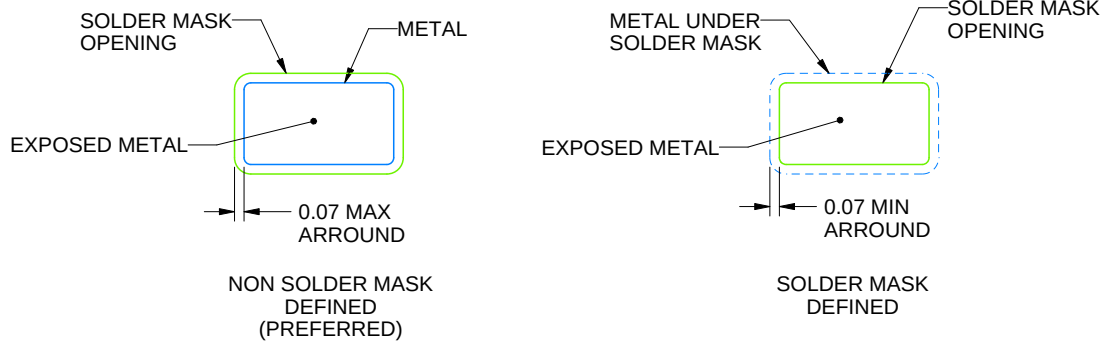
DCK0005A

SOT - 1.1 max height

SMALL OUTLINE TRANSISTOR



LAND PATTERN EXAMPLE  
EXPOSED METAL SHOWN  
SCALE:18X



SOLDER MASK DETAILS

4214834/G 11/2024

NOTES: (continued)

7. Publication IPC-7351 may have alternate designs.
8. Solder mask tolerances between and around signal pads can vary based on board fabrication site.





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