LM137JAN 3-Terminal Adjustable Negative Regulators

Check for Samples: LM137JAN

FEATURES

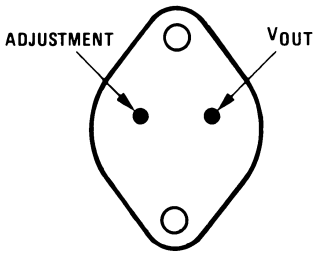
- **Output Voltage Adjustable from −37V to −1.2V**
- **1.5A Output Current Specified, −55°C to +150°C**
- **Line Regulation Typically 0.01%/V**
- **Load Regulation Typically 0.3%**
- **Excellent Thermal Regulation, 0.002%/W**
- **77 dB Ripple Rejection**
- **Excellent Rejection of Thermal Transients**
- **50 ppm/°C Temperature Coefficient**
- **Temperature-independent Current Limit**
- **Internal Thermal Overload Protection**
- **Standard 3-lead Transistor Package**
- **Output is Short Circuit Protected**

DESCRIPTION

The LM137 are adjustable 3-terminal negative voltage regulators capable of supplying in excess of −1.5A over an output voltage range of −37V to −1.2V. These regulators are exceptionally easy to apply, requiring only 2 external resistors to set the output voltage and 1 output capacitor for frequency compensation. The circuit design has been optimized for excellent regulation and low thermal transients. Further, the LM137 series features internal current limiting, thermal shutdown and safe-area compensation, making them virtually blowout-proof against overloads.

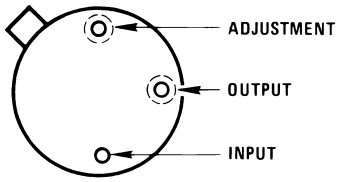
The LM137 serves a wide variety of applications including local on-card regulation, programmable-output voltage regulation or precision current regulation. The LM137 are ideal complements to the LM117 adjustable positive regulators.

Connection Diagram



Case is Input

Figure 1. TO-3
Metal Can Package
Bottom View
See Package Number K0002C



Case Is Input

Figure 2. TO
Metal Can Package
Bottom View
See Package Number NDT0003A

Table 1. LM137 Series Packages and Power Capability

Device	Package	Rated Power Dissipation	Design Load Current
LM137	TO-3 (K)	20W	1.5A
	TO (NDT)	2W	0.5A

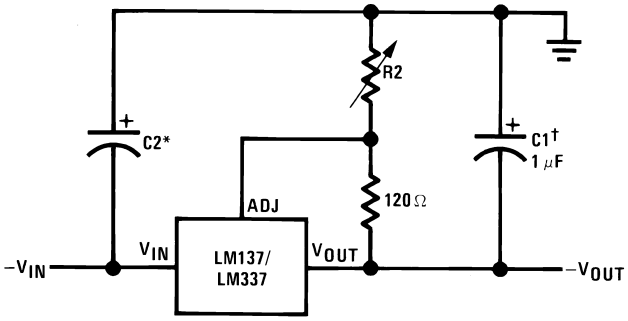
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Typical Applications



Full output current not available at high input-output voltages

$$-V_{OUT} = -1.25V\left(1 + \frac{R2}{120}\right) + \left(-I_{ADJ} \times R2\right)$$

†C1 = 1 μF solid tantalum or 10 μF aluminum electrolytic required for stability
*C2 = 1 μF solid tantalum is required only if regulator is more than 4″ from power-supply filter capacitor
Output capacitors in the range of 1 μF to 1000 μF of aluminum or tantalum electrolytic are commonly used to provide improved output impedance and rejection of transients

Figure 3. Adjustable Negative Voltage Regulator

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These devices have limited built-in ESD protection. The leads should be shorted together or the device placed in conductive foam during storage or handling to prevent electrostatic damage to the MOS gates.

Absolute Maximum Ratings(1)

Power Dissipation (2)		Internally Limited
Input-Output Voltage Differential		40V
Operating Ambient Temperature Range		$-55°C \le T_A \le +125°C$
Operating Junction Temperature Range		$-55°C \le T_J \le +150°C$
Maximum Junction Temperature		+150°C
Storage Temperature		$-65°C \le T_A \le +150°C$
Lead Temperature (Soldering, 10 sec.)		300°C
Minimum Input Voltage		−41.25V
Maximum Power Dissipation (@25°C)	TO–3	28 Watts
	TO	2.5Watts
Thermal Resistance		
θ_{JA}	TO–3 Metal Can (Still Air)	40°C/W
	TO–3 Metal Can (500LF/Min Air Flow)	14°C/W
	TO Metal Can (Still Air @ 0.5W)	174°C/W
	TO Metal Can (500LF/Min Air Flow @ 0.5W)	64°C/W
θ_{JC}	TO–3	4°C/W
	TO Metal Can (@ 1.0W)	15°C/W
Package Weight (typical)	TO–3	12,750mg
	TO Metal Can	955mg
ESD Rating (3)		4K Volts

(1) Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. Operating Ratings indicate conditions for which the device is functional, but do not ensure specific performance limits. For ensured specifications and test conditions, see the Electrical Characteristics. The ensured specifications apply only for the test conditions listed. Some performance characteristics may degrade when the device is not operated under the listed test conditions.

(2) The maximum power dissipation must be derated at elevated temperatures and is dictated by T_{Jmax} (maximum junction temperature), θ_{JA} (package junction to ambient thermal resistance), and T_A (ambient temperature). The maximum allowable power dissipation at any temperature is $P_{Dmax} = (T_{Jmax} - T_A)/\theta_{JA}$ or the number given in the Absolute Maximum Ratings, whichever is lower.

(3) Human body model, 100pF discharged through 1.5KΩ

Table 2. Recommended Operating Conditions

T_A	$-55°C \le T_A \le +125°C$
Input Voltage Range	−41.25V to −4.25V

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Quality Conformance Inspection

Mil-Std-883, Method 5005 — Group A

Subgroup	Description	Temp (°C)
1	Static tests at	+25
2	Static tests at	+125
3	Static tests at	-55
4	Dynamic tests at	+25
5	Dynamic tests at	+125
6	Dynamic tests at	-55
7	Functional tests at	+25
8A	Functional tests at	+125
8B	Functional tests at	-55
9	Switching tests at	+25
10	Switching tests at	+125
11	Switching tests at	-55

LM137H Electrical Characteristics DC Parameters

Symbol	Parameter	Conditions	Notes	Min	Max	Units	Sub-groups
V_{OUT}	Output Voltage	V_{IN} = -4.25V, I_L = 5mA		-1.275	-1.225	V	1
				-1.3	-1.2	V	2, 3
		V_{IN} = -4.25V, I_L = 500mA		-1.275	-1.225	V	1
				-1.3	-1.2	V	2, 3
		V_{IN} = -41.25V, I_L = 5mA		-1.275	-1.225	V	1
				-1.3	-1.2	V	2, 3
		V_{IN} = -41.25V, I_L = 50mA		-1.275	-1.225	V	1
				-1.3	-1.2	V	2, 3
$V_{R\ Line}$	Line Regulation	V_{IN} = -41.25V to -4.25V, I_L = 5mA		-9.0	9.0	mV	1
				-23	23	mV	2, 3
$V_{R\ Load}$	Load Regulation	V_{IN} = -6.25V, I_L = 5mA to 500mA		-12	12	mV	1
				-24	24	mV	2, 3
		V_{IN} = -41.25V, I_L = 5mA to 50mA		-6.0	6.0	mV	1
				-12	12	mV	2, 3
		V_{IN} = -6.25V, I_L = 5mA to 200mA		-6.0	6.0	mV	1
				-12	12	mV	2, 3
V_{Rth}	Thermal Regulation	V_{IN} = -14.6V, I_L = 500mA		-5.0	5.0	mV	1
I_{Adj}	Adjust Pin Current	V_{IN} = -4.25V, I_L = 5mA		25	100	µA	1, 2, 3
		V_{IN} = -41.25V, I_L = 5mA		25	100	µA	1, 2, 3
ΔI_{Adj} / V_{Line}	Adjust Pin Current Change vs. Line Voltage	V_{IN} = -41.25V to -4.25V, I_L = 5mA		-5.0	5.0	µA	1, 2, 3
ΔI_{Adj} / I_{Load}	Adjust Pin Current Change vs. Load Current	V_{IN} = -6.25V, I_L = 5mA to 500mA		-5.0	5.0	µA	1, 2, 3
I_{OS}	Output Short Circuit Current	V_{IN} = -4.25V		0.5	1.8	A	1, 2, 3
		V_{IN} = -40V		0.05	0.5	A	1, 2, 3
V_{OUT} Recovery	Output Voltage Recovery After Output Short Circuit Current	V_{IN} = -4.25V		-1.275	-1.225	V	1
				-1.3	-1.2	V	2, 3
		V_{IN} = -40V		-1.275	-1.225	V	1
				-1.3	-1.2	V	2, 3

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LM137H Electrical Characteristics DC Parameters (continued)

Symbol	Parameter	Conditions	Notes	Min	Max	Units	Sub-groups
I_Q	Minimum Load Current	V_{IN} = -4.25V		0.2	3.0	mA	1, 2, 3
		V_{IN} = -14.25V		0.2	3.0	mA	1, 2, 3
		V_{IN} = -41.25V		1.0	5.0	mA	1, 2, 3
V_{Start}	Voltage Start-up	V_{IN} = -4.25V, I_L = 500mA		-1.275	-1.225	V	1
				-1.3	-1.2	V	2, 3
V_{OUT}	Output Voltage	V_{IN} = -6.25V, I_L = 5mA (No Subgroup)	(1)	-1.3	-1.2	V	

(1) Tested at +125°C ; correlated to +150°C

LM137H Electrical Characteristics A Parameters

Symbol	Parameter	Conditions	Notes	Min	Max	Units	Sub-groups
ΔV_{IN} / ΔV_{OUT}	Ripple Rejection	V_{IN} = -6.25V, I_L = 125mA, e_I = $1V_{RMS}$ at 2400Hz		48		dB	4
V_{NO}	Output Noise Voltage	V_{IN} = -6.25V, I_L = 50mA			120	μV_{RMS}	
ΔV_{OUT} / ΔV_{IN}	Line Transient Response	V_{IN} = -6.25V, V_{Pulse} = -1V, I_L = 50mA			80	mV/V	7
ΔV_{OUT} / ΔI_L	Load Transient Response	V_{IN} = -6.25V, I_L = 50mA, ΔI_L = 200mA	(1)		60	mV	7

(1) Slash sheet limit of 0.3mV/mA is equivalent to 60mV

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LM137H Electrical Characteristics DC Parameters: Drift Values

Delta calculations performed on JAN S devices at group B, subgroup 5 only.

Symbol	Parameter	Conditions	Notes	Min	Max	Units	Sub-groups
V_{OUT}	Output Voltage	V_{IN} = -4.25V, I_L = 5mA		-0.01	0.01	V	1
		V_{IN} = -4.25V, I_L = 500mA		-0.01	0.01	V	1
		V_{IN} = -41.25V, I_L = 5mA		-0.01	0.01	V	1
		V_{IN} = -41.25V, I_L = 50mA		-0.01	0.01	V	1
$V_{R\ Line}$	Line Regulation	V_{IN} = 41.25V to -4.25V, I_L = 5mA		-4.0	4.0	mV	1
I_{Adj}	Adjust Pin Current	V_{IN} = -4.25V, I_L = 5mA		-10	10	µA	1
		V_{IN} = -41.25V, I_L = 5mA		-10	10	µA	1

LM137K Electrical Characteristics DC Parameters

Symbol	Parameter	Conditions	Notes	Min	Max	Units	Sub-groups
V_{OUT}	Output Voltage	V_{IN} = -4.25V, I_L = 5mA		-1.275	-1.225	V	1
				-1.3	-1.2	V	2, 3
		V_{IN} = -4.25V, I_L = 1.5A		-1.275	-1.225	V	1
				-1.3	-1.2	V	2, 3
		V_{IN} = -41.25V, I_L = 5mA		-1.275	-1.225	V	1
				-1.3	-1.2	V	2, 3
		V_{IN} = -41.25V, I_L = 200mA		-1.275	-1.225	V	1
				-1.3	-1.2	V	2, 3
$V_{R\ Line}$	Line Regulation	-41.25V ≤ V_{IN} ≤ -4.25V, I_L = 5mA		-9.0	9.0	mV	1
				-23	23	mV	2, 3
$V_{R\ Load}$	Load Regulation	V_{IN} = -6.25V, I_L = 5mA to 1.5A		-6.0	6.0	mV	1
				-12	12	mV	2, 3
		V_{IN} = -41.25V, I_L = 5mA to 200mA		-6.0	6.0	mV	1
				-12	12	mV	2, 3
V_{Rth}	Thermal Regulation	V_{IN} = -14.6V, I_L = 1.5A		-5.0	5.0	mV	1
I_{Adj}	Adjust Pin Current	V_{IN} = -4.25V, I_L = 5mA		25	100	µA	1, 2, 3
		V_{IN} = -41.25V, I_L = 5mA		25	100	µA	1, 2, 3
ΔI_{Adj} / V_{Line}	Adjust Pin Current Change vs. Line Voltage	-41.25V ≤ V_{IN} ≤ -4.25, I_L = 5mA		-5.0	5.0	µA	1, 2, 3
ΔI_{Adj} / I_{Load}	Adjust Pin Current Change vs. Load Current	V_{IN} = -6.25V, I_L = 5mA to 1.5A		-5.0	5.0	µA	1, 2, 3
I_{OS}	Output Short Circuit Current	V_{IN} = -4.25V		1.5	3.5	A	1, 2, 3
		V_{IN} = -40V		0.2	1.0	A	1, 2, 3
V_{OUT} Recovery	Output Voltage Recovery	V_{IN} = -4.25V		-1.275	-1.225	V	1
				-1.3	-1.2	V	2, 3
		V_{IN} = -40V		-1.275	-1.225	V	1
				-1.3	-1.2	V	2, 3
I_Q	Minimum Load Current	V_{IN} = -4.25V		0.2	3.0	mA	1, 2, 3
		V_{IN} = -14.25V		0.2	3.0	mA	1, 2, 3
		V_{IN} = -41.25V		1.0	5.0	mA	1, 2, 3
V_{Start}	Voltage Start-up	V_{IN} = 4.25V, I_L = 1.5A		-1.275	-1.225	V	1
		V_{IN} = 4.25V, I_L = 1.5A		-1.3	-1.2	V	2, 3
V_{OUT}	Output Voltage	V_{IN} = -6.25V, I_L = 5mA No Subgroup	(1)	-1.3	-1.2	V	

(1) Tested at +125°C ; correlated to +150°C

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LM137K Electrical Characteristics AC Parameters

Symbol	Parameter	Conditions	Notes	Min	Max	Units	Sub-groups
ΔV_{IN} /ΔV_{OUT}	Ripple Rejection	V_{IN} = -6.25V, I_L = 500mA, e_I = $1V_{RMS}$ at 2400Hz		50		dB	4
V_{NO}	Output Noise Voltage	V_{IN} = -6.25V, I_L = 100mA			120	μV_{RMS}	
ΔV_{OUT} / ΔV_{IN}	Line Transient Response	V_{IN} = -6.25V, I_L = 100mA, V_{Pulse} = -1V			80	mV/V	7
ΔV_{OUT} / ΔI_L	Load Transient Response	V_{IN} = -6.25V, I_L = 100mA, ΔI_L = 400mA	(1)		60	mV	7

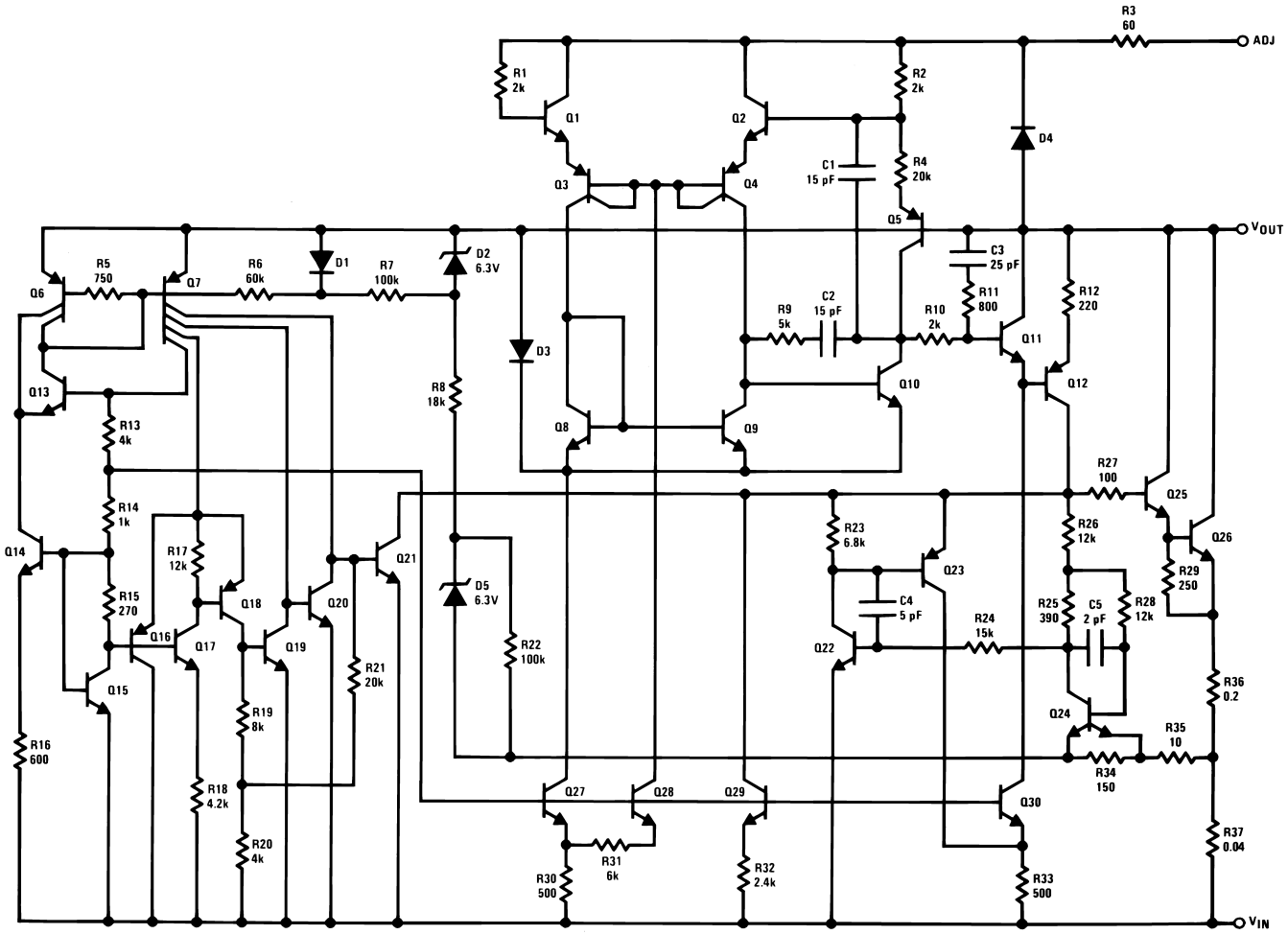
(1) Slash sheet limit of 0.15mV/mA is equivalent to 60mV

LM137K Electrical Characteristics DC Parameters: Drift Values

Delta calculations performed on JAN S devices at group B, subgroup 5 only.

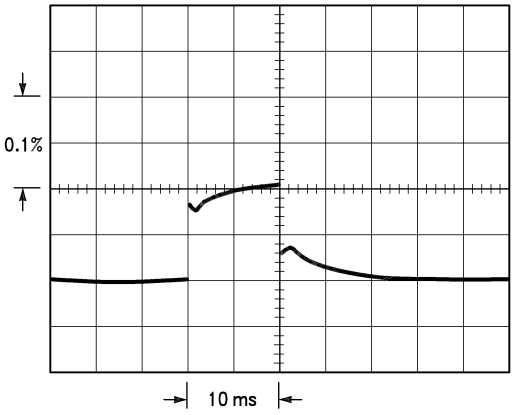
Symbol	Parameter	Conditions	Notes	Min	Max	Units	Sub-groups
V_{OUT}	Output Voltage	V_{IN} = -4.25V, I_L = 5mA		-0.01	0.01	V	1
$V_{R\ line}$	Line Regulation	V_{IN} = -41.25V to -4.25, I_L = 5mA		-4.0	4.0	mV	1
I_{Adj}	Adjust Pin Current	V_{IN} = -41.25V, I_L = 5mA		-10	10	µA	1

Schematic Diagram



Thermal Regulation

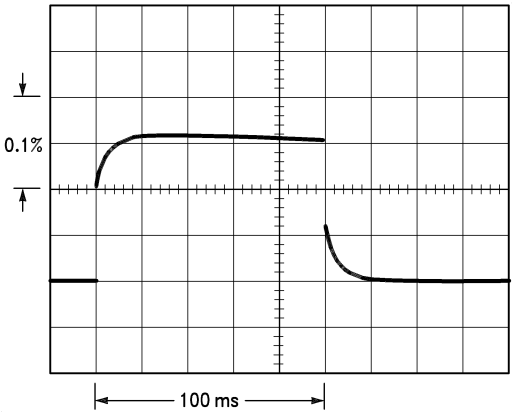
When power is dissipated in an IC, a temperature gradient occurs across the IC chip affecting the individual IC circuit components. With an IC regulator, this gradient can be especially severe since power dissipation is large. Thermal regulation is the effect of these temperature gradients on output voltage (in percentage output change) per Watt of power change in a specified time. Thermal regulation error is independent of electrical regulation or temperature coefficient, and occurs within 5 ms to 50 ms after a change in power dissipation. Thermal regulation depends on IC layout as well as electrical design. The thermal regulation of a voltage regulator is defined as the percentage change of V_{OUT}, per Watt, within the first 10 ms after a step of power is applied. The LM137's specification is 0.02%/W, max.



LM137, V_{OUT} = −10V
V_{IN} − V_{OUT} = −40V
I_{IL} = 0A → 0.25A → 0A
Vertical sensitivity, 5 mV/div

Figure 4.

In Figure 4, a typical LM137's output drifts only 3 mV (or 0.03% of V_{OUT} = −10V) when a 10W pulse is applied for 10 ms. This performance is thus well inside the specification limit of 0.02%/W × 10W = 0.2% max. When the 10W pulse is ended, the thermal regulation again shows a 3 mV step as the LM137 chip cools off. Note that the load regulation error of about 8 mV (0.08%) is additional to the thermal regulation error. In Figure 5, when the 10W pulse is applied for 100 ms, the output drifts only slightly beyond the drift in the first 10 ms, and the thermal error stays well within 0.1% (10 mV).

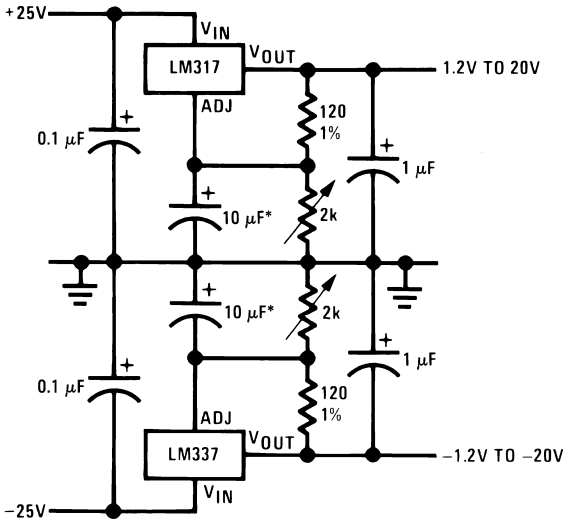


LM137, V_{OUT} = −10V
V_{IN} − V_{OUT} = −40V
I_L = 0A → 0.25A → 0A
Horizontal sensitivity, 20 ms/div

Figure 5.

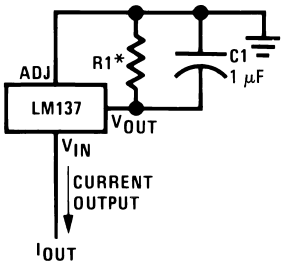
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Typical Applications



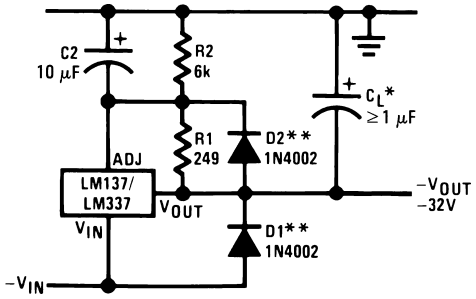
Full output current not available
at high input-output voltages
*The 10 μF capacitors are optional to improve ripple rejection

Figure 6. Adjustable Lab Voltage Regulator



$I_{OUT} = \frac{1.250V}{R1}$
*0.8Ω ≤ R1 ≤ 120Ω

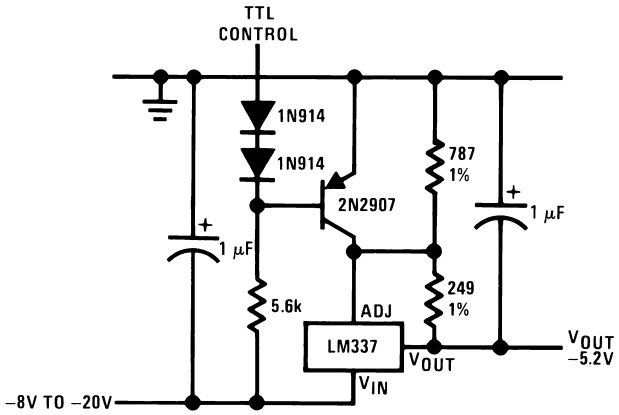
Figure 7. Current Regulator



*When C_L is larger than 20 μF, D1 protects the LM137 in case the input supply is shorted
**When C2 is larger than 10 μF and $-V_{OUT}$ is larger than −25V, D2 protects the LM137 in case the output is shorted

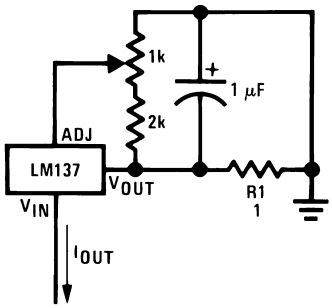
Figure 8. Negative Regulator with Protection Diodes

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*Minimum output ≃ −1.3V when control input is low

Figure 9. −5.2V Regulator with Electronic Shutdown*



$I_{OUT} = \left(\frac{1.5V}{R1}\right) \pm 15\%$ adjustable

Figure 10. Adjustable Current Regulator

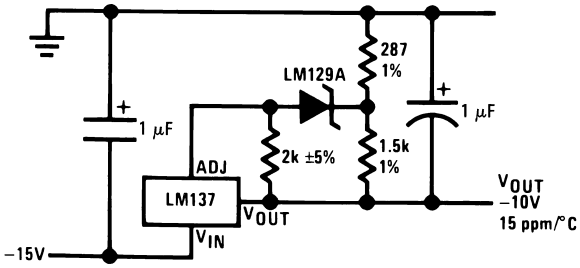


Figure 11. High Stability −10V Regulator

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Typical Performance Characteristics

(H & K Packages)

Load Regulation

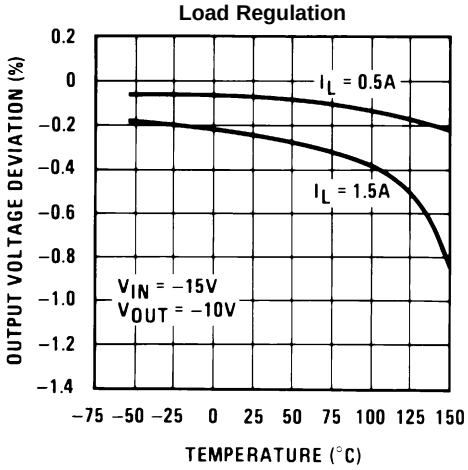


Figure 12.

Current Limit

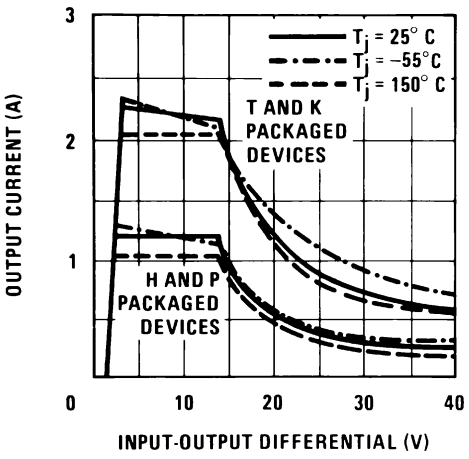


Figure 13.

Adjustment Current

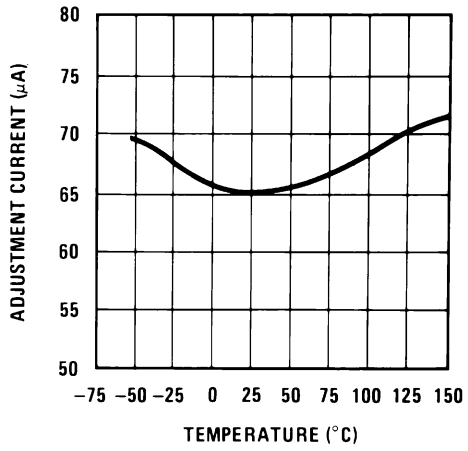


Figure 14.

Dropout Voltage

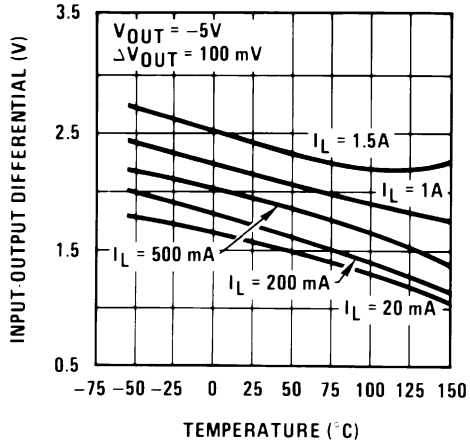


Figure 15.

Temperature Stability

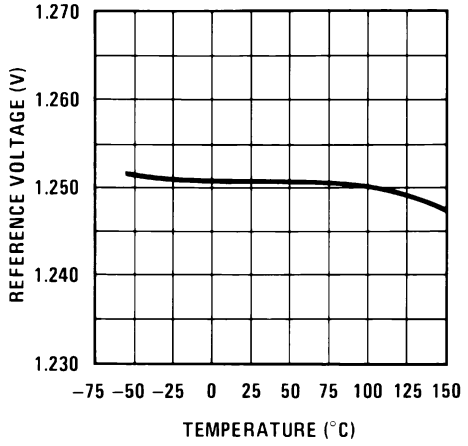


Figure 16.

Minimum Operating Current

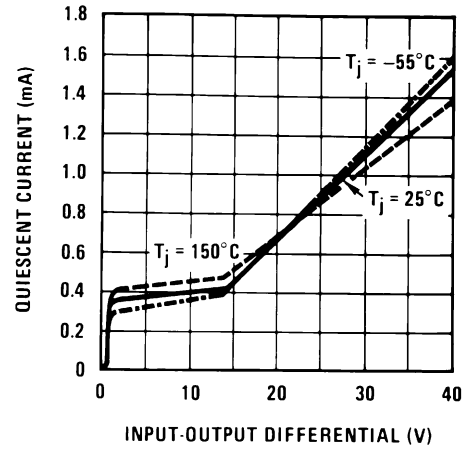


Figure 17.

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Typical Performance Characteristics (continued)

(H & K Packages)

Ripple Rejection

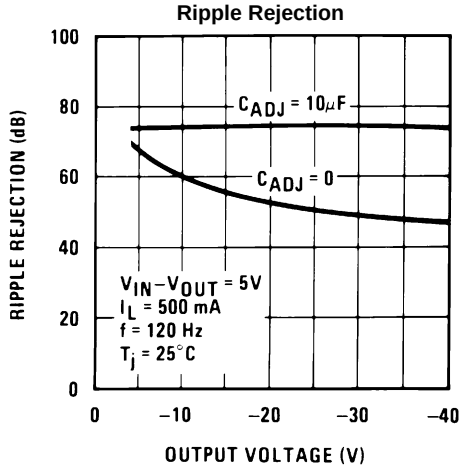


Figure 18.

Ripple Rejection

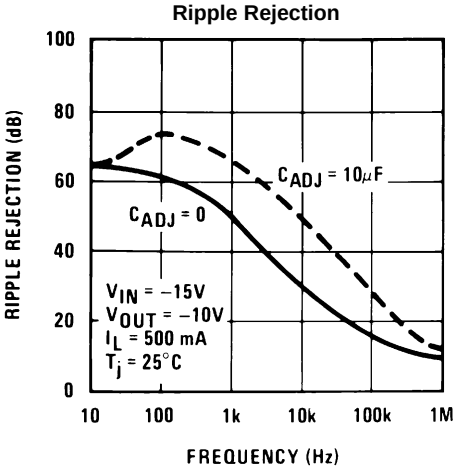


Figure 19.

Ripple Rejection

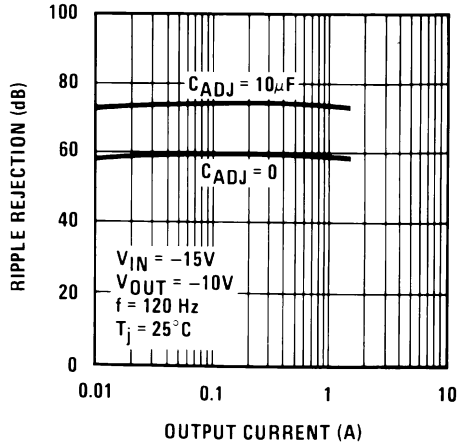


Figure 20.

Output Impedance

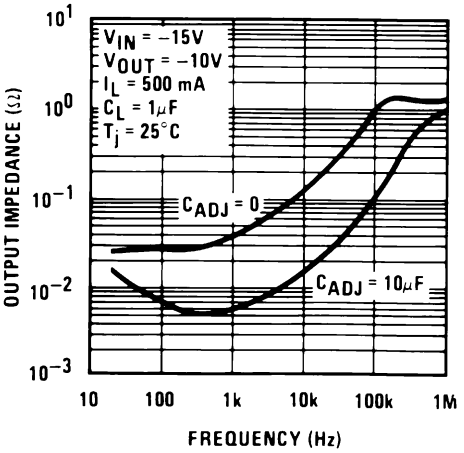


Figure 21.

Line Transient Response

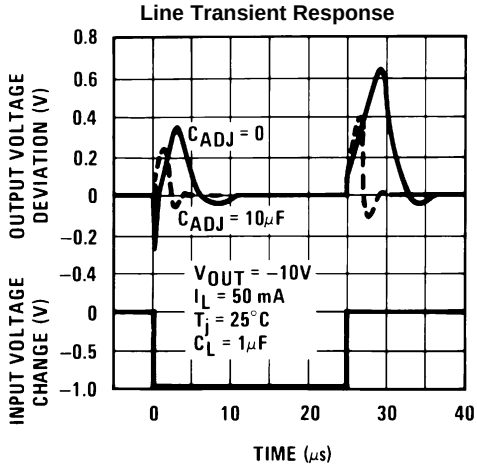


Figure 22.

Load Transient Response

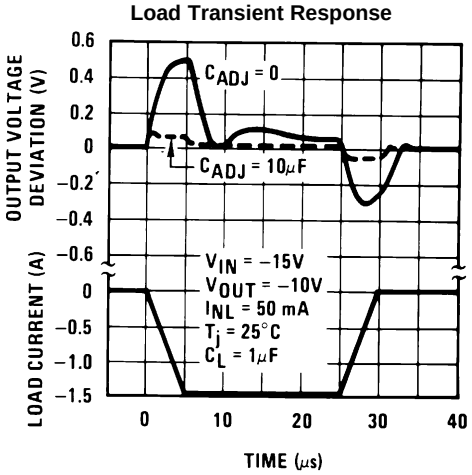


Figure 23.

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REVISION HISTORY

Date Released	Revision	Section	Changes
12/08/2010	A	New Release, Corporate format	2 MDS data sheets converted into one Corp. data sheet format. MJLM137-H Rev. 0A0, MJLM137-K Rev. 0A0. MDS data sheets will be archived.
03/20/2013	A	All	Changed layout of National Data Sheet to TI format

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PACKAGING INFORMATION

Orderable part number	Status (1)	Material type (2)	Package | Pins	Package qty | Carrier	RoHS (3)	Lead finish/ Ball material (4)	MSL rating/ Peak reflow (5)	Op temp (°C)	Part marking (6)
JL137BXA	Active	Production	TO (NDT) | 3	20 | TRAY NON-STD	No	Call TI	Level-1-NA-UNLIM	-55 to 150	JL137BXA JM38510/11803BXA Q ACO JM38510/11803BXA Q >T
JL137SXA	Active	Production	TO (NDT) | 3	20 | JEDEC TRAY (5+1)	No	Call TI	Level-1-NA-UNLIM	-55 to 150	JL137SXA JM38510/11803SXA Q ACO JM38510/11803SXA Q >T
JL137SYA	Active	Production	TO (K) | 2	50 | JEDEC TRAY (5+1)	No	Call TI	Level-1-NA-UNLIM	-55 to 150	JL137SYA Q JM38510/ 11804SYA ACO 11804SYA >T
JM38510/11803BXA	Active	Production	TO (NDT) | 3	20 | TRAY NON-STD	No	Call TI	Level-1-NA-UNLIM	-55 to 150	JL137BXA JM38510/11803BXA Q ACO JM38510/11803BXA Q >T
JM38510/11804SYA	Active	Production	TO (K) | 2	50 | JEDEC TRAY (5+1)	Yes	Call TI	Level-1-NA-UNLIM	-55 to 150	JL137SYA Q JM38510/ 11804SYA ACO 11804SYA >T
M38510/11803BXA	Active	Production	TO (NDT) | 3	20 | TRAY NON-STD	No	Call TI	Level-1-NA-UNLIM	-55 to 150	JL137BXA JM38510/11803BXA Q ACO JM38510/11803BXA Q >T
M38510/11803BXX	Active	Production	TO (NDT) | 3	20 | TRAY NON-STD	No	Call TI	Level-1-NA-UNLIM	-55 to 150	JL137BXA JM38510/11803BXA Q ACO JM38510/11803BXA Q >T
M38510/11804SYA	Active	Production	TO (K) | 2	50 | JEDEC TRAY (5+1)	Yes	Call TI	Level-1-NA-UNLIM	-55 to 150	JL137SYA Q JM38510/ 11804SYA ACO 11804SYA >T

(1) **Status:** For more details on status, see our product life cycle.

(2) **Material type:** When designated, preproduction parts are prototypes/experimental devices, and are not yet approved or released for full production. Testing and final process, including without limitation quality assurance, reliability performance testing, and/or process qualification, may not yet be complete, and this item is subject to further changes or possible discontinuation. If available for ordering, purchases will be subject to an additional waiver at checkout, and are intended for early internal evaluation purposes only. These items are sold without warranties of any kind.

(3) **RoHS values:** Yes, No, RoHS Exempt. See the TI RoHS Statement for additional information and value definition.

(4) **Lead finish/Ball material:** Parts may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead finish/Ball material values may wrap to two lines if the finish value exceeds the maximum column width.

(5) **MSL rating/Peak reflow:** The moisture sensitivity level ratings and peak solder (reflow) temperatures. In the event that a part has multiple moisture sensitivity ratings, only the lowest level per JEDEC standards is shown. Refer to the shipping label for the actual reflow temperature that will be used to mount the part to the printed circuit board.

(6) **Part marking:** There may be an additional marking, which relates to the logo, the lot trace code information, or the environmental category of the part.

Multiple part markings will be inside parentheses. Only one part marking contained in parentheses and separated by a "~" will appear on a part. If a line is indented then it is a continuation of the previous line and the two combined represent the entire part marking for that device.

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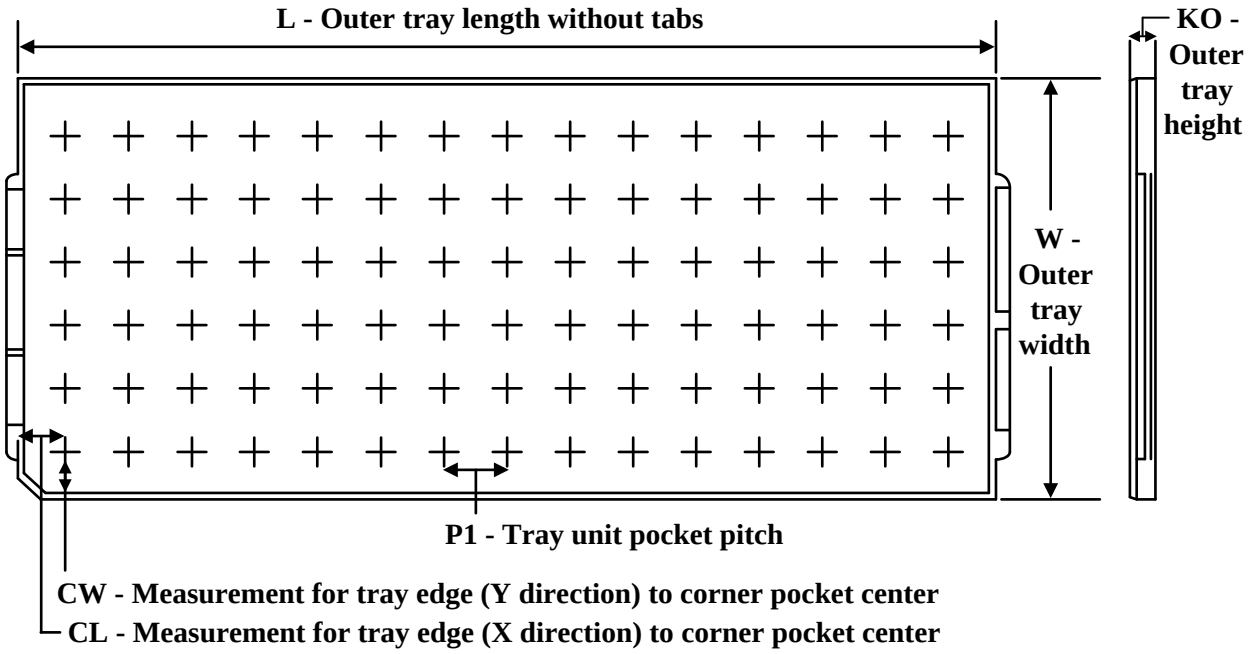
OTHER QUALIFIED VERSIONS OF LM137JAN, LM137JAN-SP :

- Military : LM137JAN
- Space : LM137JAN-SP

NOTE: Qualified Version Definitions:

- Military - QML certified for Military and Defense Applications
- Space - Radiation tolerant, ceramic packaging and qualified for use in Space-based application

TRAY

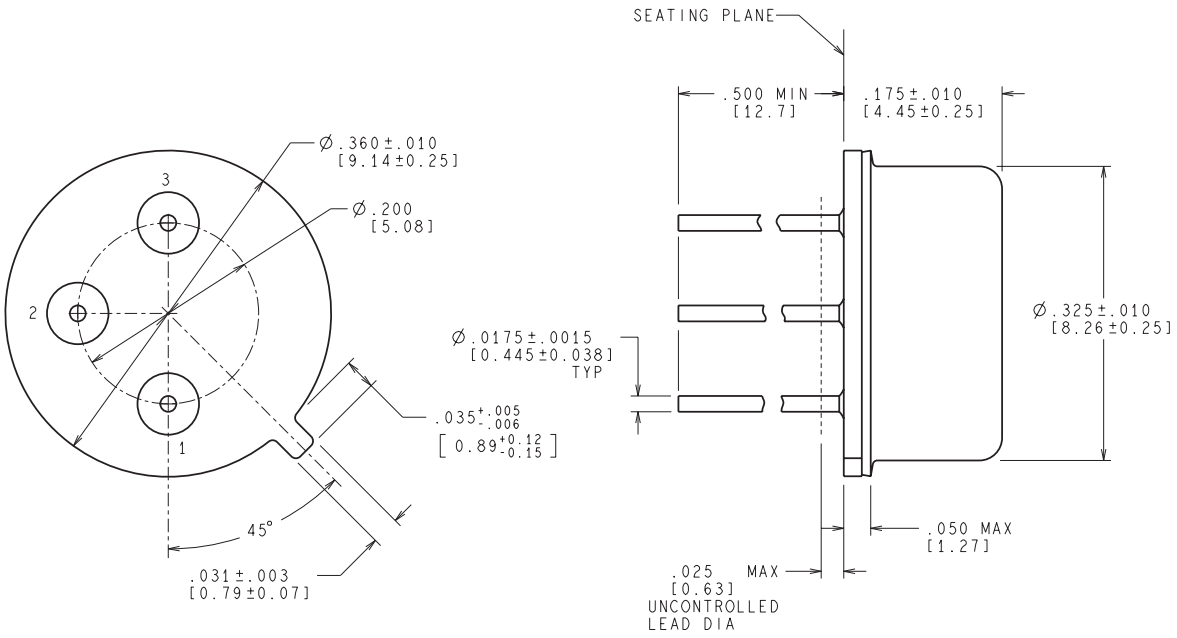


Chamfer on Tray corner indicates Pin 1 orientation of packed units.

*All dimensions are nominal

Device	Package Name	Package Type	Pins	SPQ	Unit array matrix	Max temperature (°C)	L (mm)	W (mm)	K0 (µm)	P1 (mm)	CL (mm)	CW (mm)
JL137BXA	NDT	TO-CAN	3	20	2 X 10	150	126.49	61.98	8890	11.18	12.95	18.54
JL137SXA	NDT	TO-CAN	3	20	2 X 10	150	126.49	61.98	8890	11.18	12.95	18.54
JL137SYA	K	TO-CAN	2	50	9 X 6	NA	292.1	215.9	25654	3.87	22.3	25.4
JM38510/11803BXA	NDT	TO-CAN	3	20	2 X 10	150	126.49	61.98	8890	11.18	12.95	18.54
JM38510/11804SYA	K	TO-CAN	2	50	9 X 6	NA	292.1	215.9	25654	3.87	22.3	25.4
M38510/11803BXA	NDT	TO-CAN	3	20	2 X 10	150	126.49	61.98	8890	11.18	12.95	18.54
M38510/11803BXX	NDT	TO-CAN	3	20	2 X 10	150	126.49	61.98	8890	11.18	12.95	18.54
M38510/11804SYA	K	TO-CAN	2	50	9 X 6	NA	292.1	215.9	25654	3.87	22.3	25.4

NDT0003A



CONTROLLING DIMENSION IS INCH
VALUES IN [ ] ARE MILLIMETERS

MIL-PRF-38535
CONFIGURATION CONTROL

H03A (Rev D)

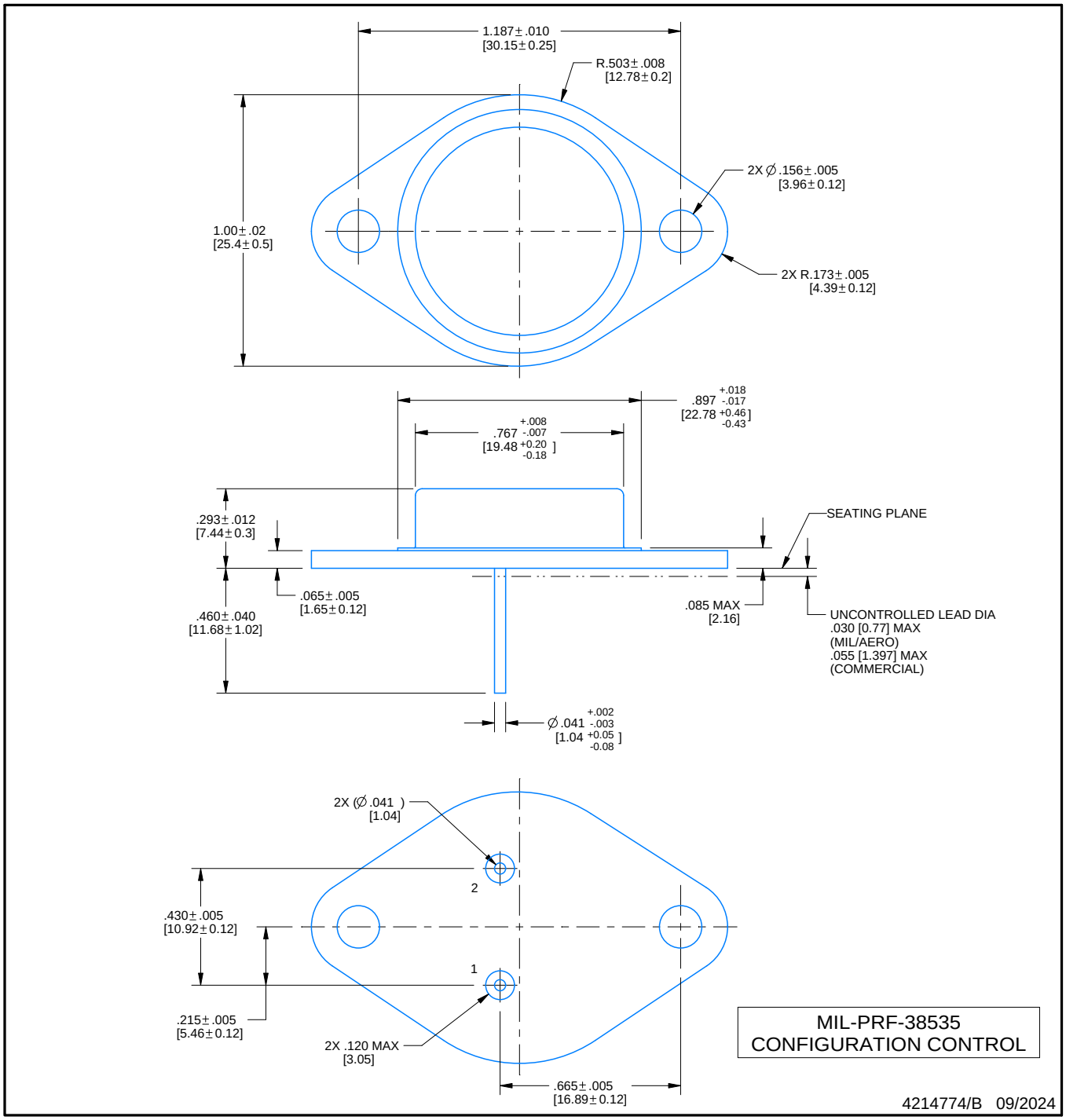
K0002A



PACKAGE OUTLINE

TO-CAN - 7.747 mm max height

TRANSISTOR OUTLINE



NOTES:

1. Linear dimensions are in inches [millimeters]. Dimensions in parenthesis are for reference only. Controlling dimensions are in inches. Dimensioning and tolerancing per ASME Y14.5M.
2. This drawing is subject to change without notice.
3. Leads not to be bent greater than 15°.

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