

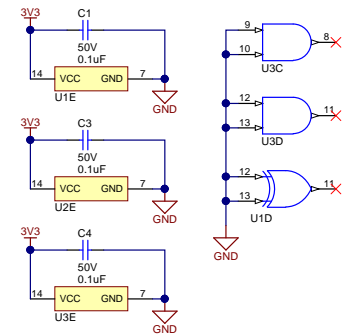
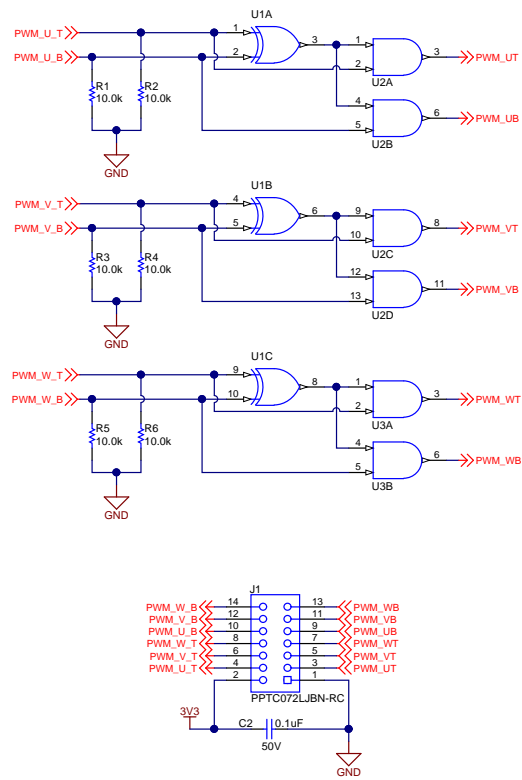
1		2		3		4		5		6	
										Revision History	
Rev		ECN #		Approved Date		Approved by				Notes	
N/A		N/A		N/A		N/A				N/A	

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Orderable: NA	Designed for: Public Release	Mod. Date: 11/30/2017
TID #: 01349	Project Title: PWM Interlock Card	
Number: TIDA-01349	Rev: E1	Sheet Title: Coversheet
SVN Rev: Version control disabled	Assembly Variant: [No Variations]	Sheet: 1 of 3
Drawn By: PN	File: TIDA-01349-Interlock-Card_CoverSheet.SchDd Size: B	
Engineer: PN	Contact: http://www.ti.com/support	



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Orderable: NA	Designed for: Public Release	Mod. Date: 11/30/2017
TID #: 01349	Project Title: PWM Interlock Card	
Number: TIDA-01349 Rev: E1	Sheet Title: Interlock Circuit	
SVN Rev: Version control disabled	Assembly Variant: [No Variations]	Sheet: 2 of 3
Drawn By:	File: TIDA-01349-Interlock-Card-SchDoc	Size: B
Engineer: PN	Contact: http://www.ti.com/support	



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PCB
LOGO
Pb-Free Symbol

PCB
LOGO
FCC disclaimer

ZZ1
Label Assembly Note
This Assembly Note is for PCB labels only

ZZ2
Assembly Note
These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3
Assembly Note
These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4
Assembly Note
These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

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Orderable: NA
TID #: 01349
Number: TIDA-01349 | Rev: E1
SVN Rev: Version control disabled
Drawn By:
Engineer: PN

Designed for: Public Release
Project Title: PWM Interlock Card
Sheet Title: EVM Hardware
Assembly Variant: [No Variations]
File: TIDA-01349-Interlock-Card_EVM_Hardware_Schematic
Contact: <http://www.ti.com/support>

Mod. Date: 11/30/2017
Sheet: 3 of 3
Size: B
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A

B

C

D