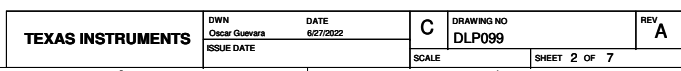


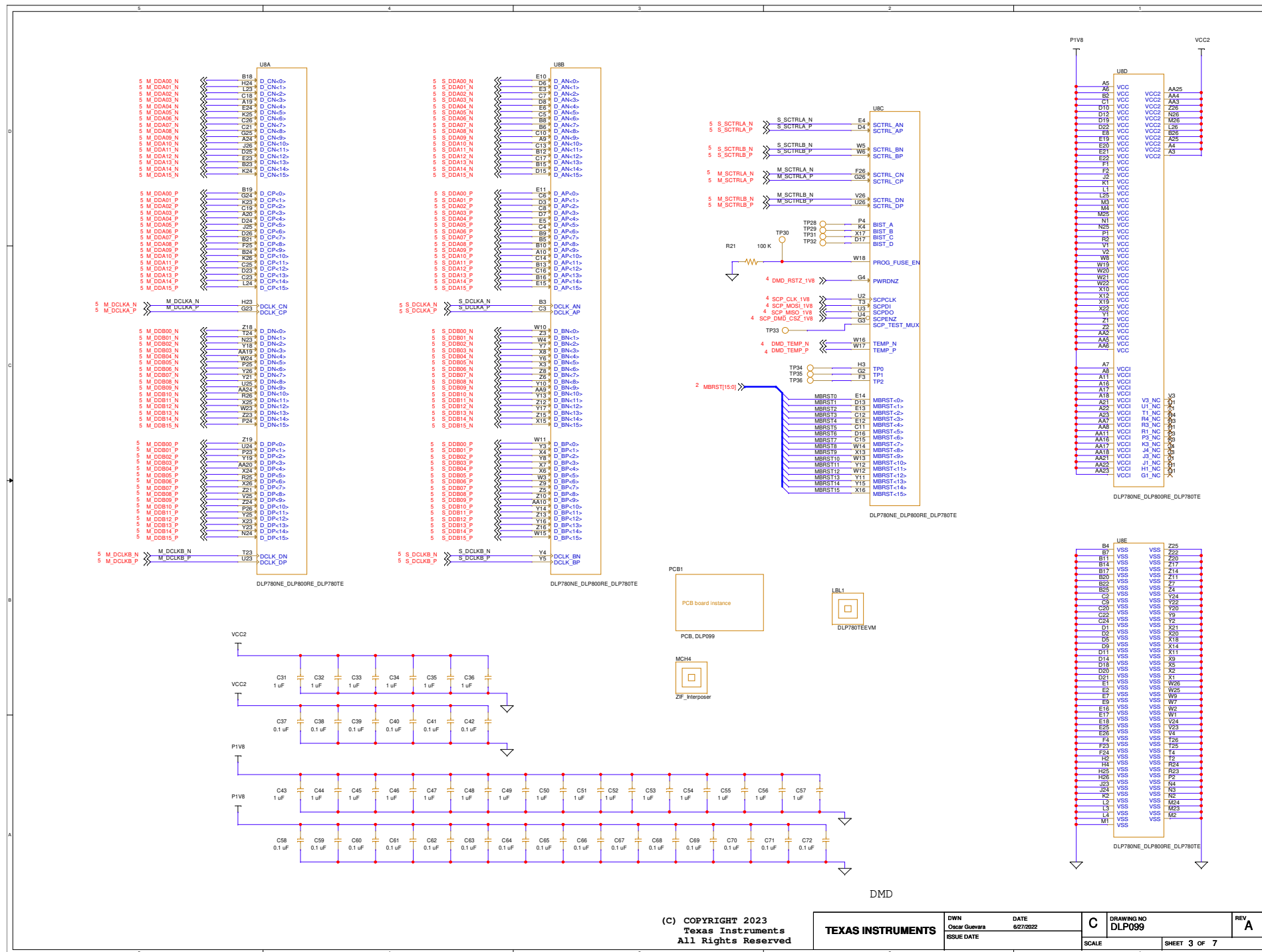


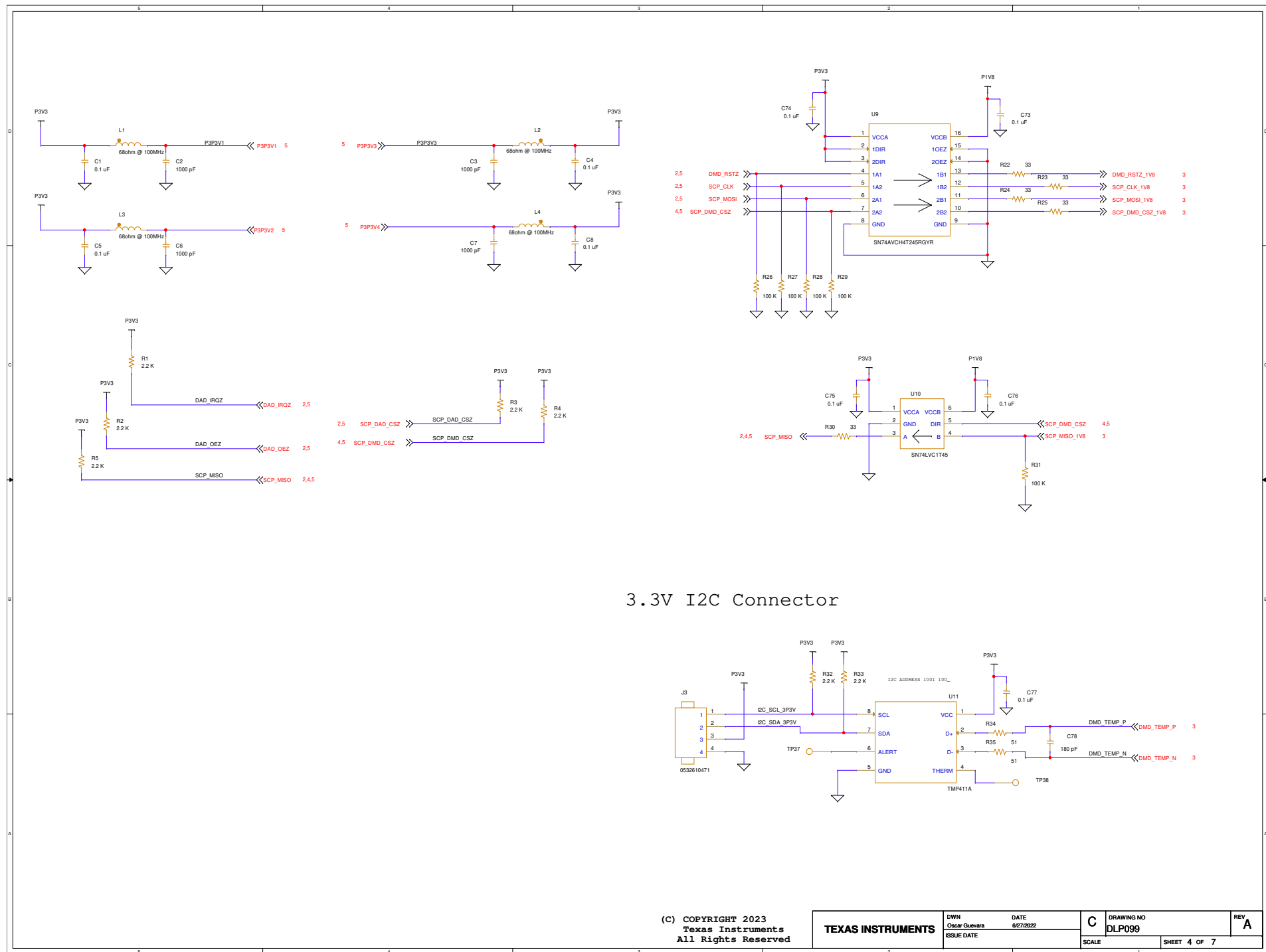
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		ENGR			
		SYST			
		PRJ			
		QA		TITLE DLP780TE DMD Board	
NEXT ASSY	USED ON			C	DRAWING NO DLP099
APPLICATION		SW	Cadence Capture V:17.2	SCALE	SHEET 1 of 7
					REV C







REVISIONS:

REV A: INITIAL ENGINEERING RELEASE

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