

LM851772-Q1 Buck-Boost Controller Evaluation Module for USB-PD



Description

The [LM851772Q1EVM-PD](#) demonstrates a flexible, high power buck-boost design using the [LM851772-Q1](#). The evaluation module is configured to operate from an input voltage range of 9V to 60V and to produce a regulated 5V to 48V output with up to a 5A load current. Most of the device settings are easily adjusted or set through jumpers.

Get Started

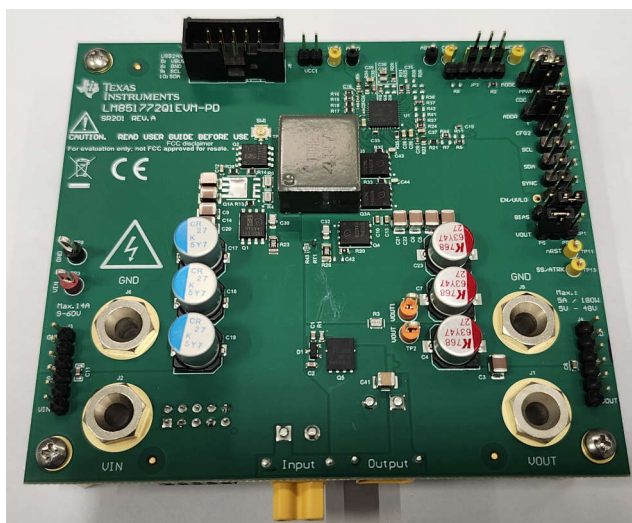
1. Connect the EVM to the power supply and the load.
2. Use the [USB2ANY](#) adapter to configure the GUI and I2C operation.
3. Install the [LM851772-Q1 configuration GUI](#).

Features

- Wide input voltage range and Ultra-high (> 95%) peak power conversion efficiency
- Adjustable output voltage using feedback resistor divider or I2C interface
- Easy configuration of current monitor or limiter
- Support for cable drop compensation
- Output disconnect support
- Programmable input undervoltage lockout (UVLO) threshold and hysteresis
- Output constant voltage (CV) and constant current (CC) options
- I2C interface with USB2ANY and GUI

Applications

- USB Type-C® power delivery
 - [Docking station](#)
 - [PC monitor](#)
 - [Desktop PC](#)
- Wireless charging
- [Industrial PC](#) and [rugged PC](#)
- [DC/DC modules](#)



LM851772Q1EVM-PD

1 Evaluation Module Overview

1.1 Introduction

The [LM851772Q1EVM-PD](#) evaluation module (EVM) is designed to conveniently evaluate the performance of the [LM851772-Q1](#) wide-VIN buck-boost controller.

The EVM is optimized for USB-PD applications and configured to operate from an input voltage range of 9V to 60V and to produce a regulated 5V to 48V output with up to a 5A load current. The EVM operates with a switching frequency of 320kHz. Most of the device settings are easily adjusted or set through jumpers, like operation mode (PSM or fPWM), bias supply, and external clock synchronization.

Through the usage of the I2C interface, the EVM covers additional parameters for configuration and adjustments of the switch mode power supply operation.

The EVM can be directly connected to the Type-C Power Delivery (PD) controller, [TPS26742EQ1EVM](#) evaluation module, for a complete USB-PD application.

1.2 Kit Contents

- One [LM851772Q1EVM-PD](#) PCB
- EVM Disclaimer Read Me

The EVM does not include the USB2ANY interface. Order the USB2ANY interface at [USB2ANY interface adapter](#).

1.3 Specification

Table 1-1. Board Specifications

Parameter	Value
Input voltage	9.0V to 60V
Output voltage	5V to 36V 5V to 48V (Vin > 16V)
Maximum output current	5A
Default switching frequency	320kHz
Operating temperature	25°C
Board size (four layers)	4.1 inch × 3.6 inch

1.4 Device Information

The [LM851772-Q1](#) is a four switch buck-boost controller. The device provides a regulated output voltage if the input voltage is higher, equal to or lower as the adjusted output voltage. In power safe mode, the device supports a superb efficiency over the full range of the output.

- Wide input range from 3.5V to 85V
- Output voltage 3.3V to 48V
- Peak current regulation scheme
- Dynamic output voltage tracking
 - Digital PWM tracking input
 - Analog tracking input
 - Via I2C interface programming
- Minimum quiescent current
 - Low shut down Iq of 3μA
 - Low operating Iq of 25μA
- Operation mode selection for high light load efficiency
 - Power save burst mode
 - μSleep power save mode
- Integrated high voltage supply LDO

2 Hardware Connector, Test Point, and Selection Switch Descriptions

This section provides the I/O connectors, jumpers, and test points of the EVM.

The power supply must be connected to input connectors J2 and J6.

The load must be connected to output connectors J1 and J5.

2.1 Connector Descriptions

Table 2-1. Connectors

Reference Designator	Description
J1	Output voltage positive connection
J2	Input voltage positive connection
J3	Input voltage positive and input voltage return test point
J4	Output voltage positive and output voltage return test point
J5	Output voltage return connection
J6	Input voltage return connection
J7	USB-PD controller EVM control connector
J8	I2C / USB2ANY connector
J9	USB-PD controller EVM power in connector
J10	USB-PD controller EVM power out connector
J11	CDC output connection

Table 2-2. USB-PD Controller EVM Control Connector: J7

Pin	Function	Pin	Function
1	CDC: current monitor signal	2	PTC: temp sensor
3	PDCTRL_GOOD: temp sensor supply	4	nFLT signal
5	EN/UVLO signal	6	I2C: SDA
7	GND	8	I2C: SCL
9	VCC1 (connected through jumper JP12)	10	GND

2.2 Jumper Descriptions

Table 2-3. Jumpers

Reference Designator	Pins	Description	Default Connection
JP1	Pin 1 to Pin 2	Connect VOUT before sense resistor	*
	Pin 2 to Pin 3	Connect VOUT after sense resistor	
JP2	Pin 1 to Pin 3	Can be used for Bode plot signal injection when external voltage divider circuit is used	
	Pin 4 to Pin 5 (FB)	Connect FB to VCC2 (if R2 is not assembled) to use internal voltage divider circuit	
JP3	Pin 1 to Pin 2 (VOUT1)	Connect BIAS to VOUT1 (output of power stage)	
	Pin 2 to Pin 3 (VIN)	Connect BIAS to VIN CAUTION Do not set the jumper to VIN when VIN is higher than 55V.	
JP4	Pin 1 to Pin 2 (GND)	Connect EN/UVLO to GND	
	Pin 2 to Pin 3 (VIN)	Connect EN/UVLO to VIN CAUTION Only set the jumper to VIN if also BIAS is connected to VIN	

Table 2-3. Jumpers (continued)

Reference Designator	Pins	Description	Default Connection
JP5	Pin 1 to Pin 2 (GND)	Set MODE low: PSM mode	*
	Pin 2 to Pin 3 (VCC2)	Set MODE high: FPWM mode	
JP6	Pin 1 to Pin 2 (SYNC)	Set SYNC to GND	
JP7	Pin 1 to Pin 2 (GND)	Connect ILIMCOMP to GND	
	Pin 2 to Pin 3 (VCC2)	Connect ILIMCOMP to VCC2 (disable current limiter)	
	Open	Enable current limiter function	
JP8	Pin 2 to Pin 3 (CFG2)	Set CFG2 to GND	
JP9	Pin 1 to Pin 2 (ADDR/AGND)	Set I2C ENABLED address 0x6A	*
	Pin 2 to Pin 3 (ADDR/VCC2)	Set I2C ENABLED address 0x6B	
JP10	Pin 2 to Pin 3 (CFG3/SDA)	Set CFG3/SDA to GND	
JP11	Pin 1 to Pin 2 (CFG4/SCL)	Set CFG4/SCL to GND	
JP12	Pin 1 to Pin 2 (VCC1)	Connect VCC1 to interface header J7	

2.3 Test Point Descriptions

Table 2-4. Test Points

Reference Designator	Description
TP1	ISNSP test point
TP2	ISNSN test point
TP3 (VIN)	Input voltage positive test point
TP4	AGND test point
TP5	SW1
TP6	SW2
TP7	AGND test point
TP8 (GND)	Input voltage return test point
TP9	VCC2 test point
TP10	VCC1 test point
TP11	nRST test point
TP12	COMP test point
TP13	SS/ATRK test point

3 Implementation Results

3.1 Test Setup

Figure 3-1 shows a typical test setup to evaluate the LM851772Q1EVM-PD.

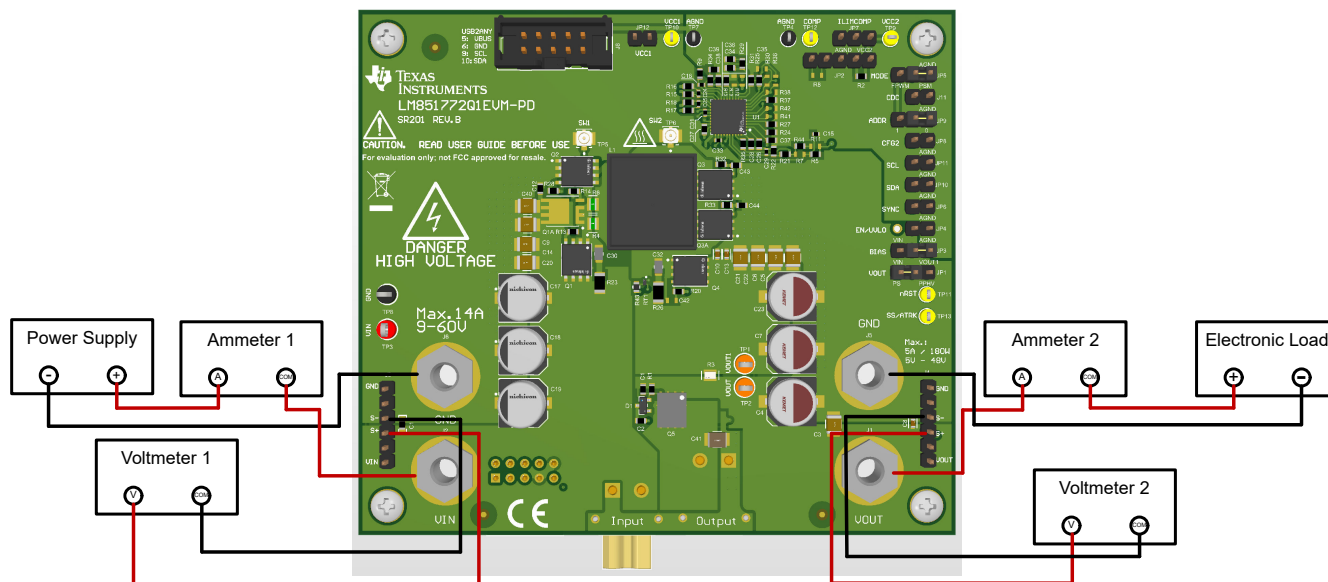


Figure 3-1. Typical EVM Connection Diagram

3.2 Test Procedure

1. Set the power supply current limit to 15A.
2. Turn off the power supply.
3. Connect the positive output of the power supply to J2 and the negative output to J6.
4. Connect the load to J1 for the positive connection and J5 for the negative connection.
5. Set the power supply voltage to 8V and the electronic load to 0.1A. Make sure the electronic load voltage is in regulation with a nominal 5V output.
6. Slowly increase the load while monitoring the output voltage between J4-VOUT and J4-GND. The output voltage must remain in regulation with a nominal 4V output as the load is increased up to 3A.
7. Slowly sweep the input voltage from 8V to 60V. The output voltage must remain in regulation with a nominal 5V output.
8. Set the output voltage to 48V via I2C and GUI
9. Increase the load to 5A.
10. Slowly sweep the input voltage from 36V to 20V. Make sure the output voltage remains in regulation with a nominal 48V output.
11. Decrease the input voltage down to 0V to shut down the buck-boost converter, and then turn off the load.

3.3 Precautions



WARNING

Prolonged operation with low input at full power causes heating of the FETs (Q1 to Q8). Board surface is hot. Do not touch. Contact can cause burns.



CAUTION

Do not leave EVM powered when unattended.

3.4 Test Data and Performance Curves

3.4.1 Thermal Performance

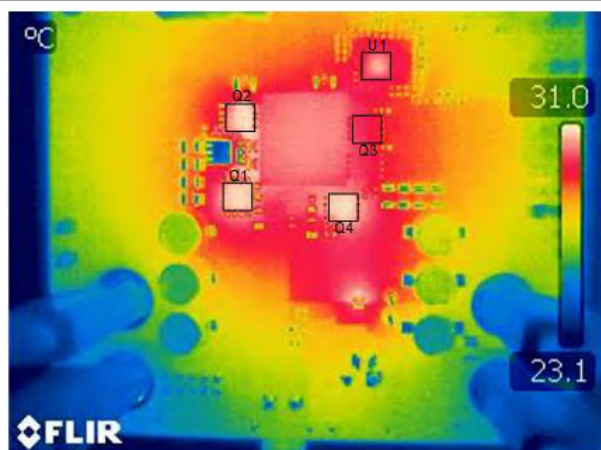


Figure 3-2. Thermal Image: $V_{IN} = 12.0V$, $V_{OUT} = 5.0V$, $I_{OUT} = 5.0A$, No Forced Air Cooling

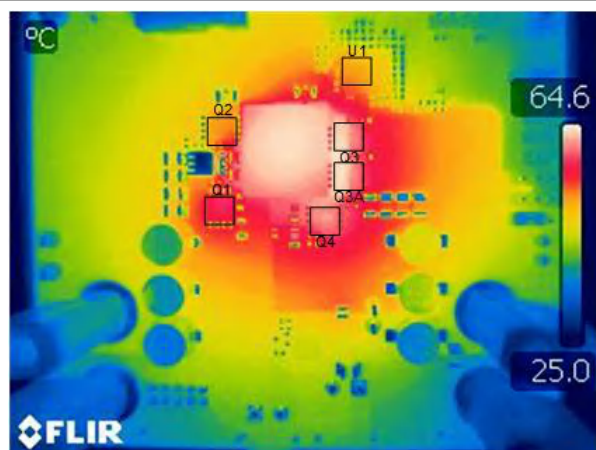


Figure 3-3. Thermal Image: $V_{IN} = 12.0V$, $V_{OUT} = 28.0V$, $I_{OUT} = 5.0A$, No Forced Air Cooling

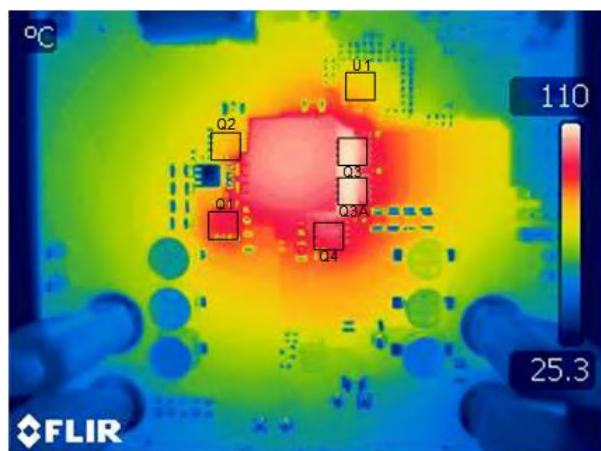


Figure 3-4. Thermal Image: $V_{IN} = 12.0V$, $V_{OUT} = 36.0V$, $I_{OUT} = 5.0A$, No Forced Air Cooling

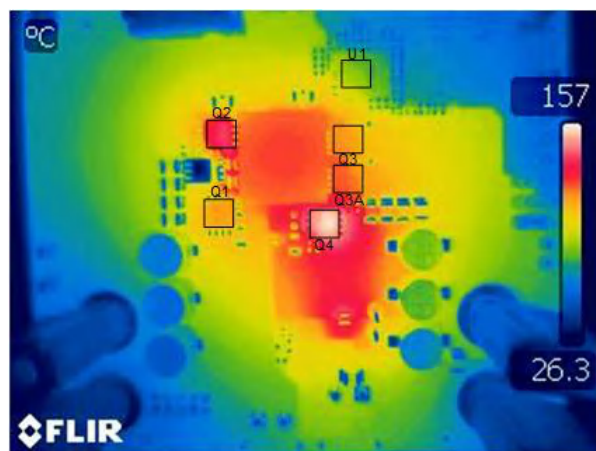


Figure 3-5. Thermal Image: $V_{IN} = 12.0V$, $V_{OUT} = 36.0V$, Load = Short, No Forced Air Cooling

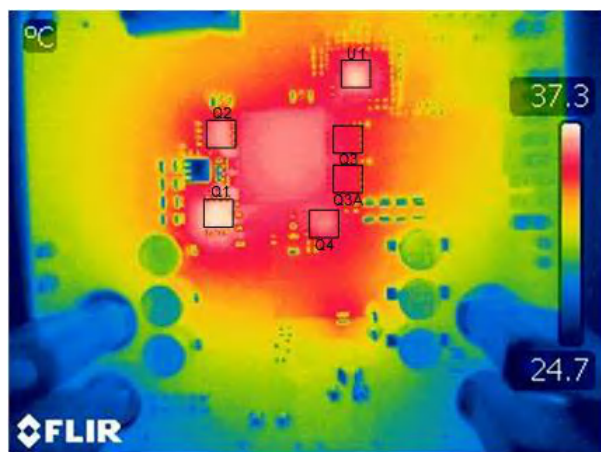


Figure 3-6. Thermal Image: $V_{IN} = 20.0V$, $V_{OUT} = 5.0V$, $I_{OUT} = 5.0A$, No Forced Air Cooling

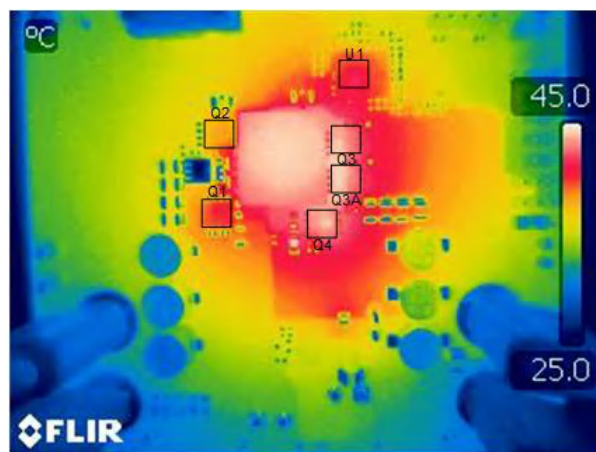


Figure 3-7. Thermal Image: $V_{IN} = 20.0V$, $V_{OUT} = 28.0V$, $I_{OUT} = 5.0A$, No Forced Air Cooling

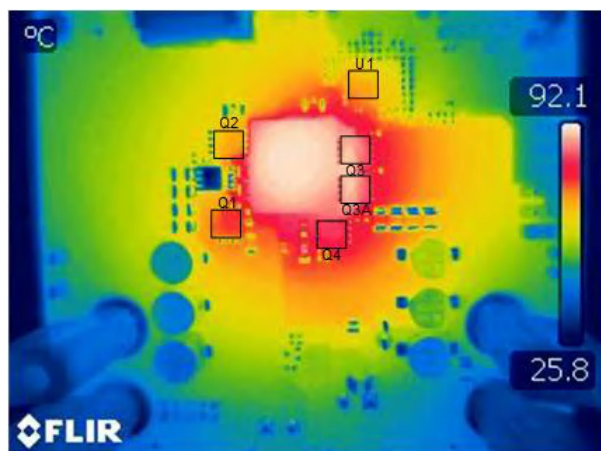


Figure 3-8. Thermal Image: $V_{IN} = 20.0V$, $V_{OUT} = 48.0V$, $I_{OUT} = 5.0A$, No Forced Air Cooling

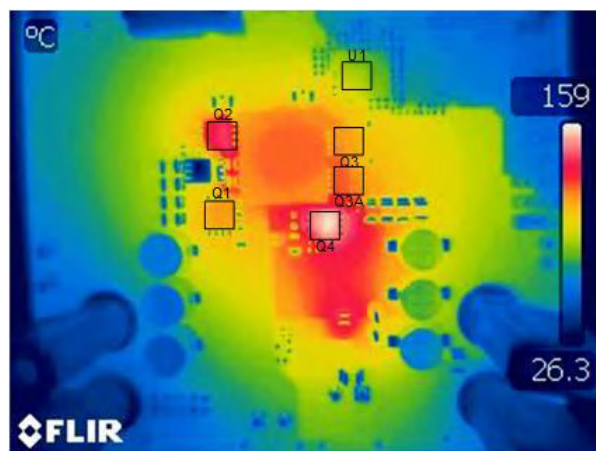


Figure 3-9. Thermal Image: $V_{IN} = 20.0V$, $V_{OUT} = 48.0V$, Load = Short, No Forced Air Cooling

3.4.2 Efficiency

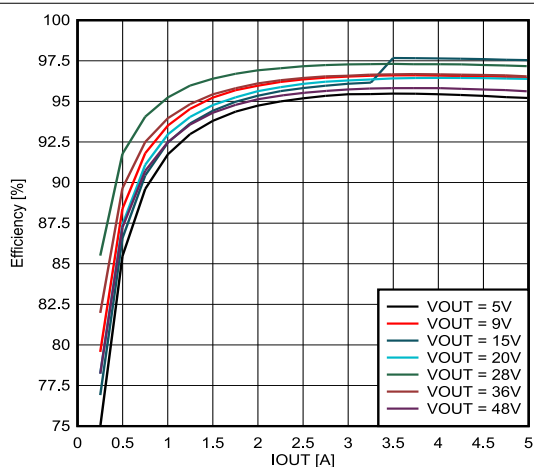


Figure 3-10. Efficiency vs Output Current at $V_{in} = 12V$, FPWM MODE

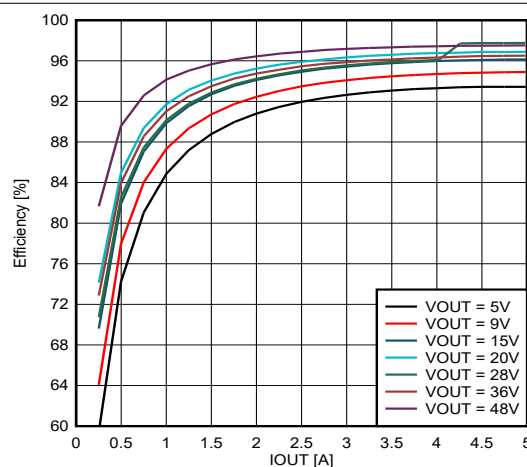


Figure 3-11. Efficiency vs Output Current at $V_{in} = 20V$, FPWM MODE

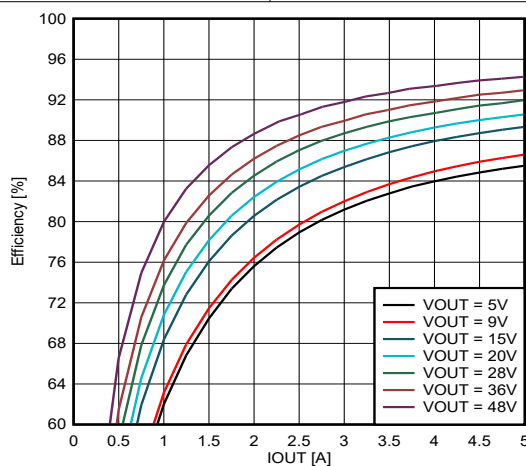


Figure 3-12. Efficiency vs Output Current at $V_{in} = 48V$, FPWM MODE

3.4.3 Steady State Waveforms

Note

All measurements done in FPWM mode unless otherwise noted.

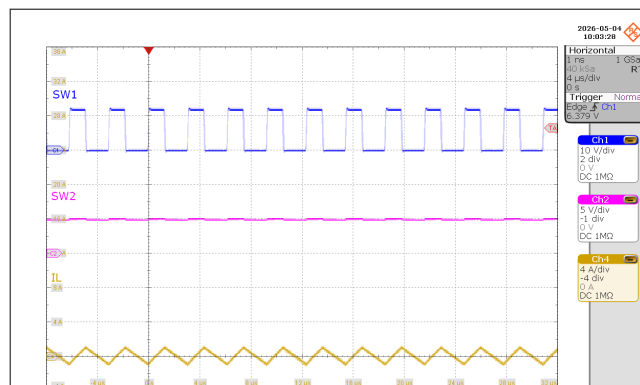


Figure 3-13. SW1, SW2, I_L ($V_{IN} = 12V$, $V_{OUT} = 5V$, $I_{OUT} = 0A$)

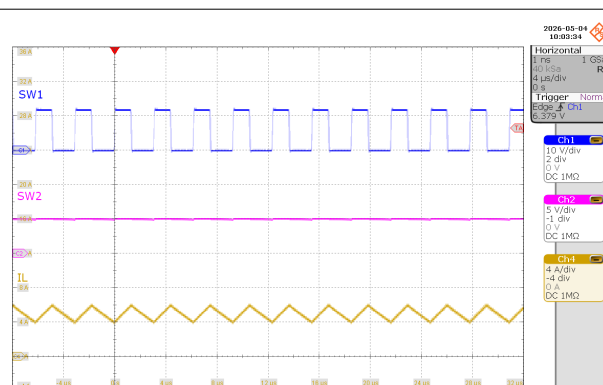


Figure 3-14. SW1, SW2, I_L ($V_{IN} = 12V$, $V_{OUT} = 5V$, $I_{OUT} = 5A$)

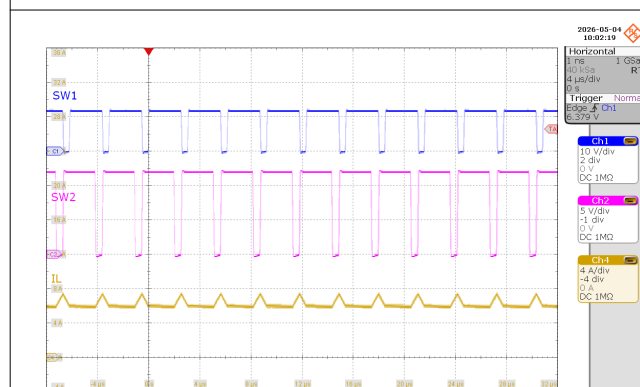


Figure 3-15. SW1, SW2, I_L ($V_{IN} = 12V$, $V_{OUT} = 12V$, $I_{OUT} = 0A$)

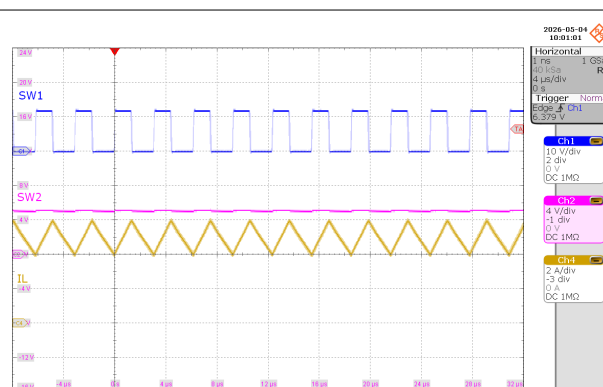


Figure 3-16. SW1, SW2, I_L ($V_{IN} = 12V$, $V_{OUT} = 12V$, $I_{OUT} = 5A$)

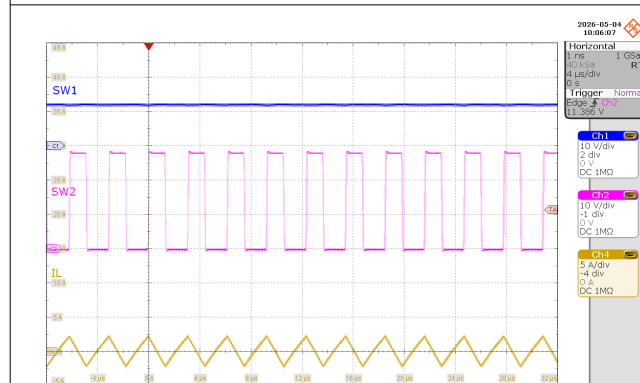


Figure 3-17. SW1, SW2, I_L ($V_{IN} = 12V$, $V_{OUT} = 28V$, $I_{OUT} = 0A$)

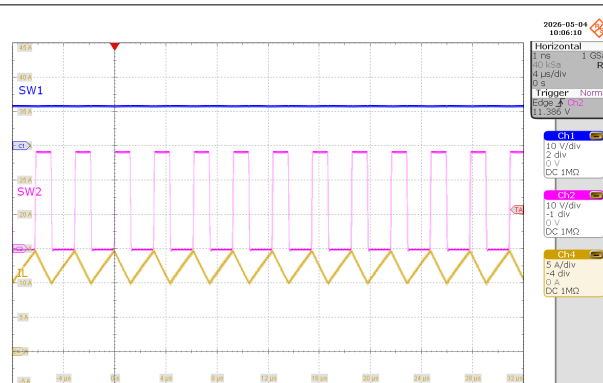


Figure 3-18. SW1, SW2, I_L ($V_{IN} = 12V$, $V_{OUT} = 28V$, $I_{OUT} = 5A$)

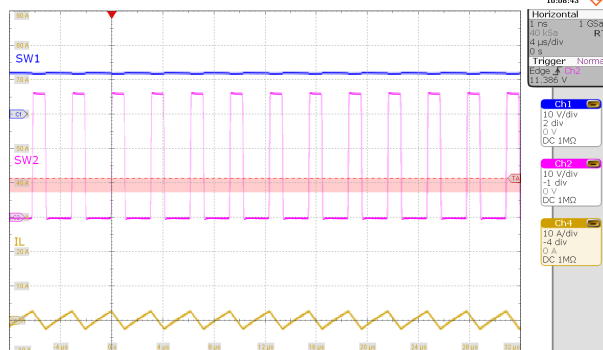


Figure 3-19. SW1, SW2, I_L ($V_{IN} = 12V$, $V_{OUT} = 36V$, $I_{OUT} = 5A$)

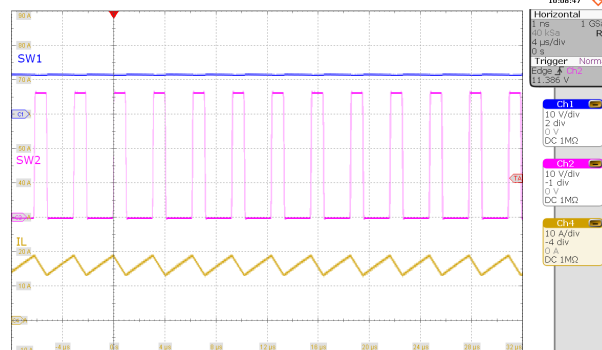


Figure 3-20. SW1, SW2, I_L ($V_{IN} = 12V$, $V_{OUT} = 36V$, $I_{OUT} = 5A$)

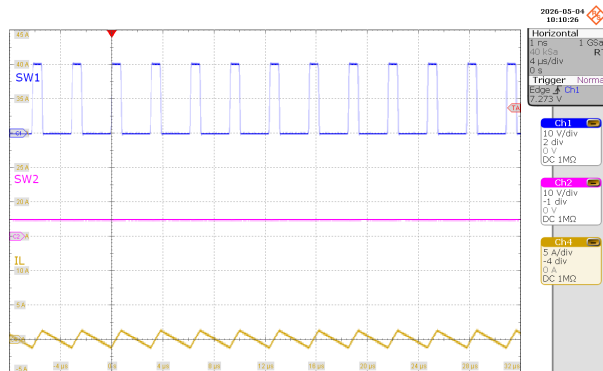


Figure 3-21. SW1, SW2, I_L ($V_{IN} = 20V$, $V_{OUT} = 5V$, $I_{OUT} = 0A$)

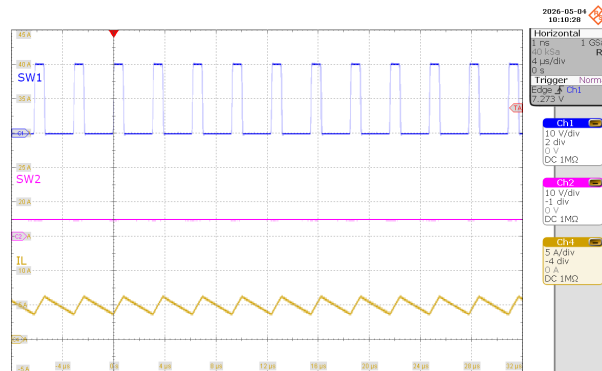


Figure 3-22. SW1, SW2, I_L ($V_{IN} = 20V$, $V_{OUT} = 5V$, $I_{OUT} = 5A$)

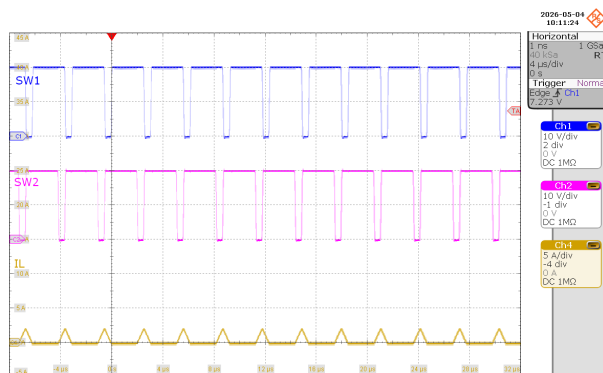


Figure 3-23. SW1, SW2, I_L ($V_{IN} = 20V$, $V_{OUT} = 20V$, $I_{OUT} = 0A$)

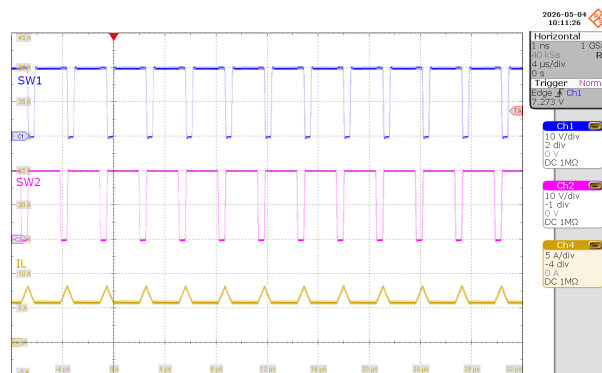


Figure 3-24. SW1, SW2, I_L ($V_{IN} = 20V$, $V_{OUT} = 20V$, $I_{OUT} = 5A$)

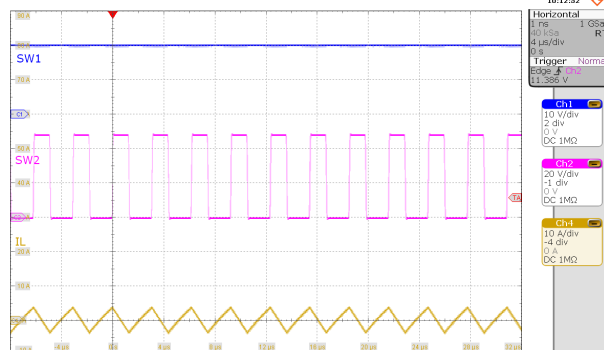


Figure 3-25. SW1, SW2, I_L ($V_{IN} = 20V$, $V_{OUT} = 48V$, $I_{OUT} = 0A$)

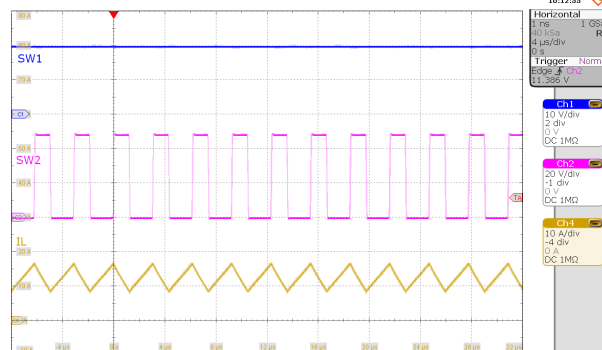


Figure 3-26. SW1, SW2, I_L ($V_{IN} = 20V$, $V_{OUT} = 48V$, $I_{OUT} = 5A$)

3.4.4 Step Load Response

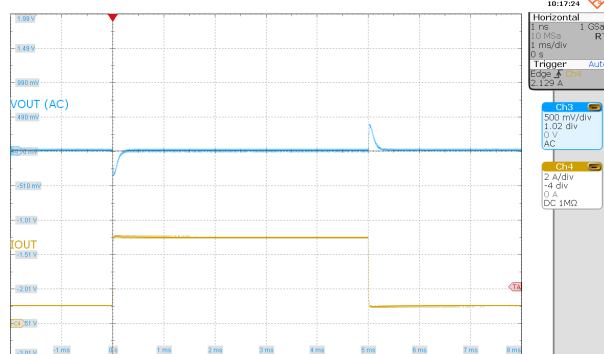


Figure 3-27. Load Step ($V_{IN} = 12V$, $V_{OUT} = 5V$, $I_{OUT} = 1A - 5A$)

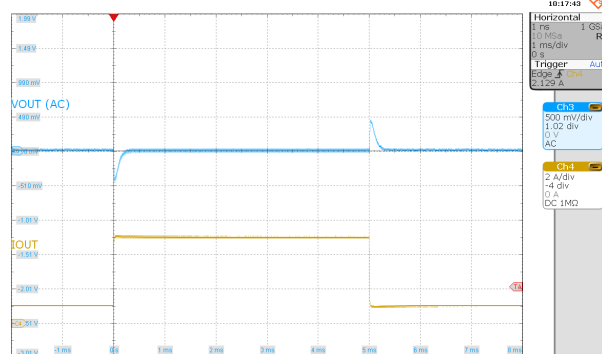


Figure 3-28. Load Step ($V_{IN} = 12V$, $V_{OUT} = 12V$, $I_{OUT} = 1A - 5A$)

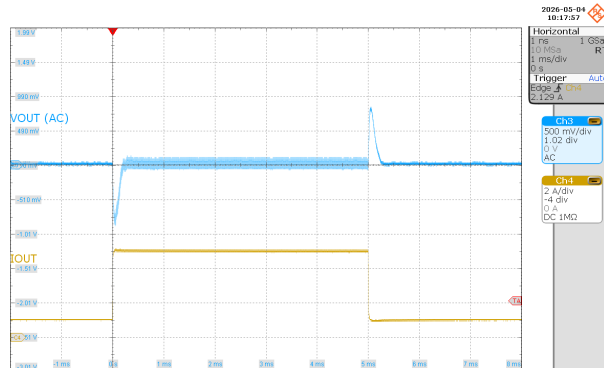


Figure 3-29. Load Step ($V_{IN} = 12V$, $V_{OUT} = 28V$, $I_{OUT} = 1A - 5A$)

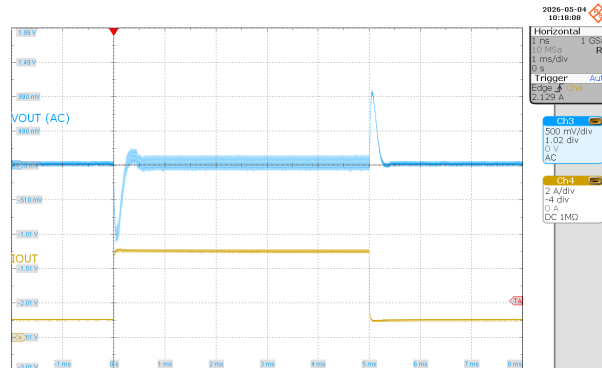


Figure 3-30. Load Step ($V_{IN} = 12V$, $V_{OUT} = 36V$, $I_{OUT} = 1A - 5A$)

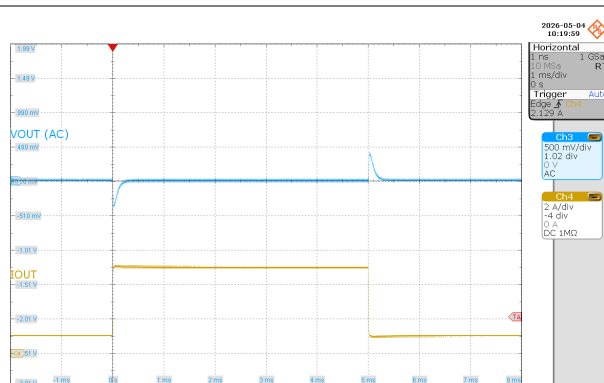


Figure 3-31. Load Step ($V_{IN} = 20V$, $V_{OUT} = 5V$, $I_{OUT} = 1A - 5A$)

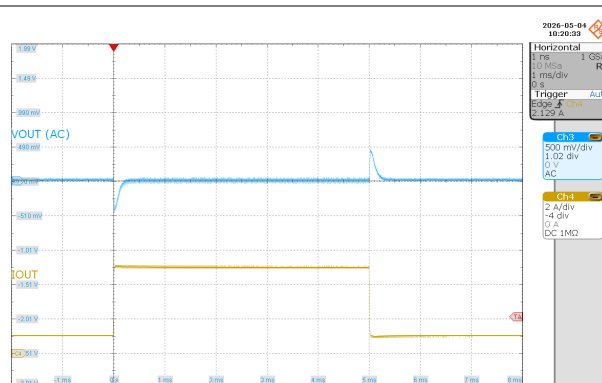


Figure 3-32. Load Step ($V_{IN} = 20V$, $V_{OUT} = 20V$, $I_{OUT} = 1A - 5A$)

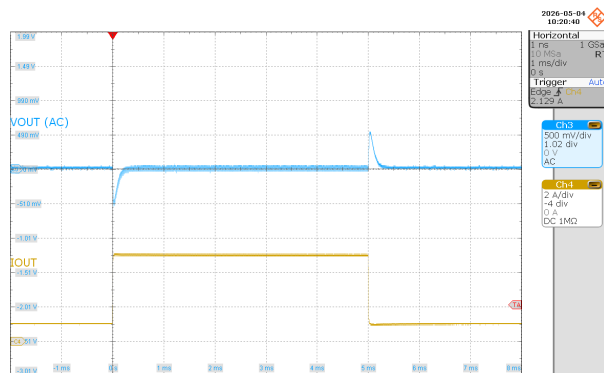


Figure 3-33. Load Step ($V_{IN} = 20V$, $V_{OUT} = 28V$, $I_{OUT} = 1A - 5A$)

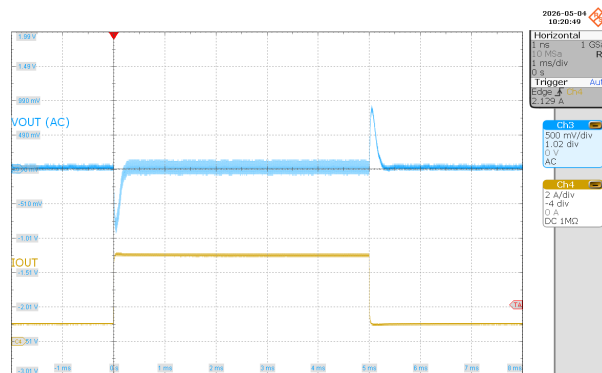


Figure 3-34. Load Step ($V_{IN} = 20V$, $V_{OUT} = 48V$, $I_{OUT} = 1A - 5A$)

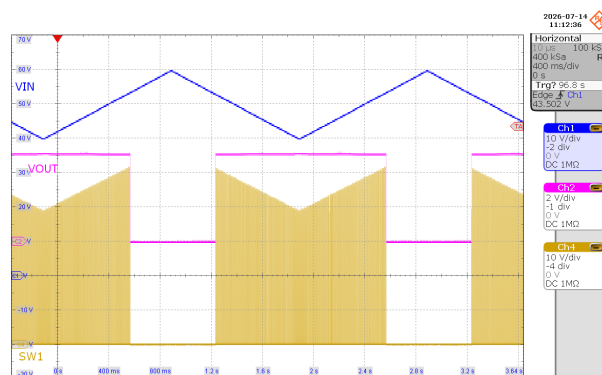


Figure 3-35. VIN ramp 40V to 60V - stop switching with IVP Trigger at 55V

3.4.5 AC Loop Response Curves

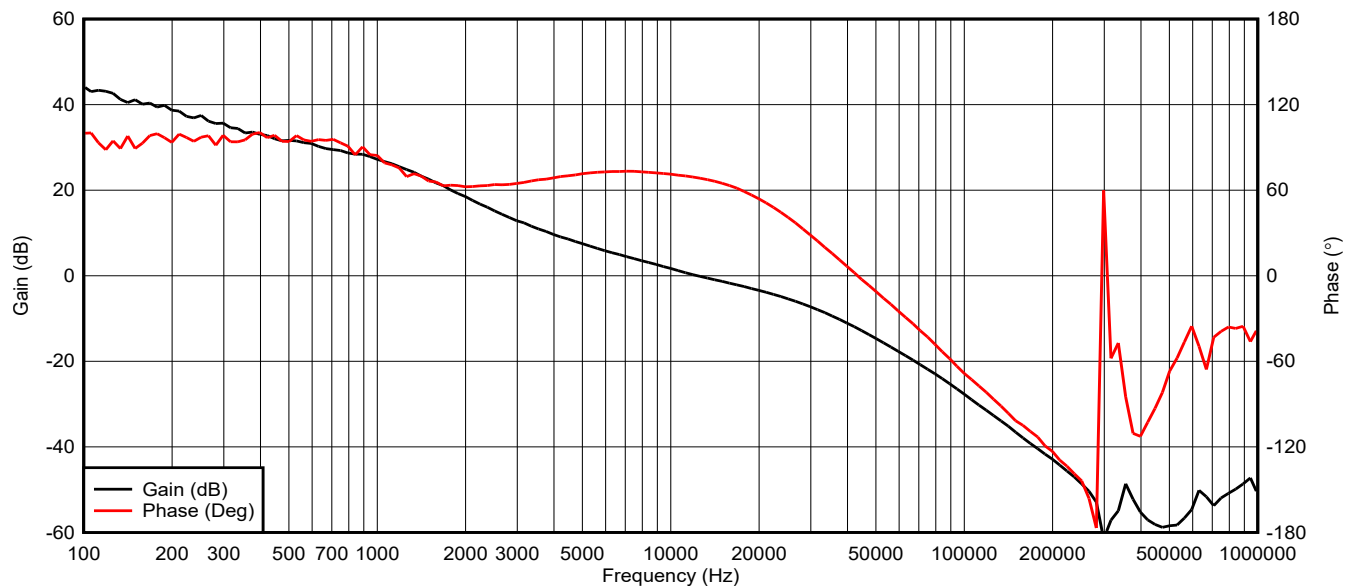


Figure 3-36. Control Loop Response, $V_{IN} = 12.0V$, $V_{OUT} = 5.0V$, $I_{OUT} = 5.0A$

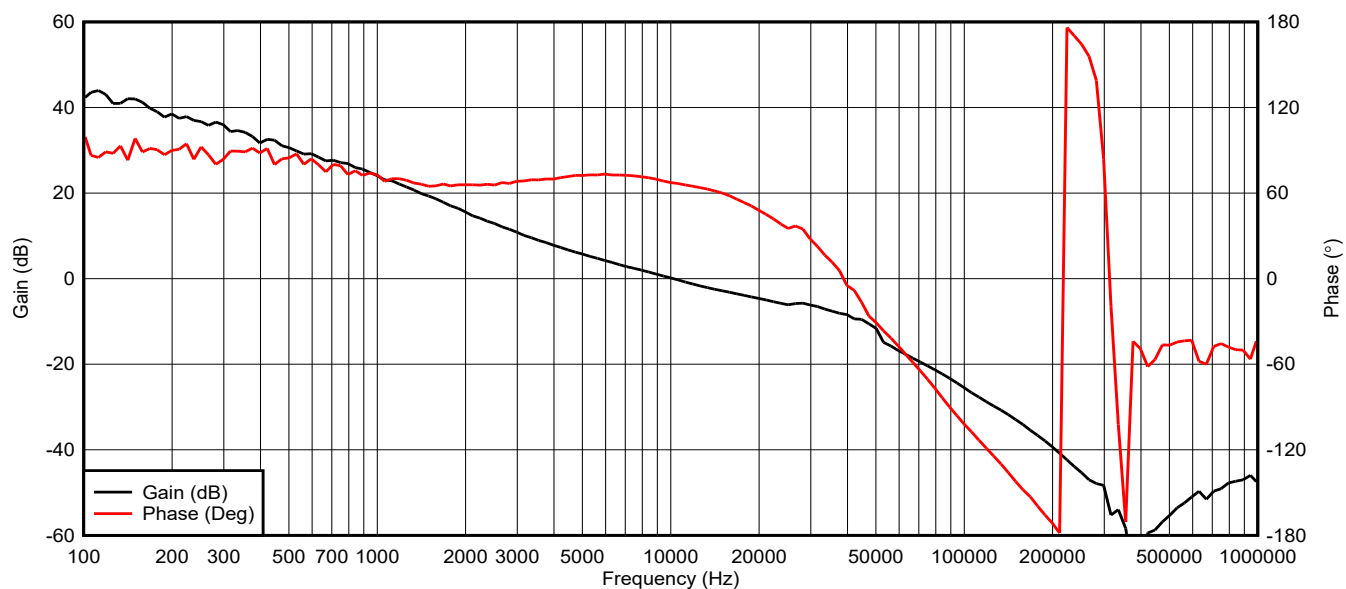


Figure 3-37. Control Loop Response, $V_{IN} = 12.0V$, $V_{OUT} = 12.0V$, $I_{OUT} = 5.0A$

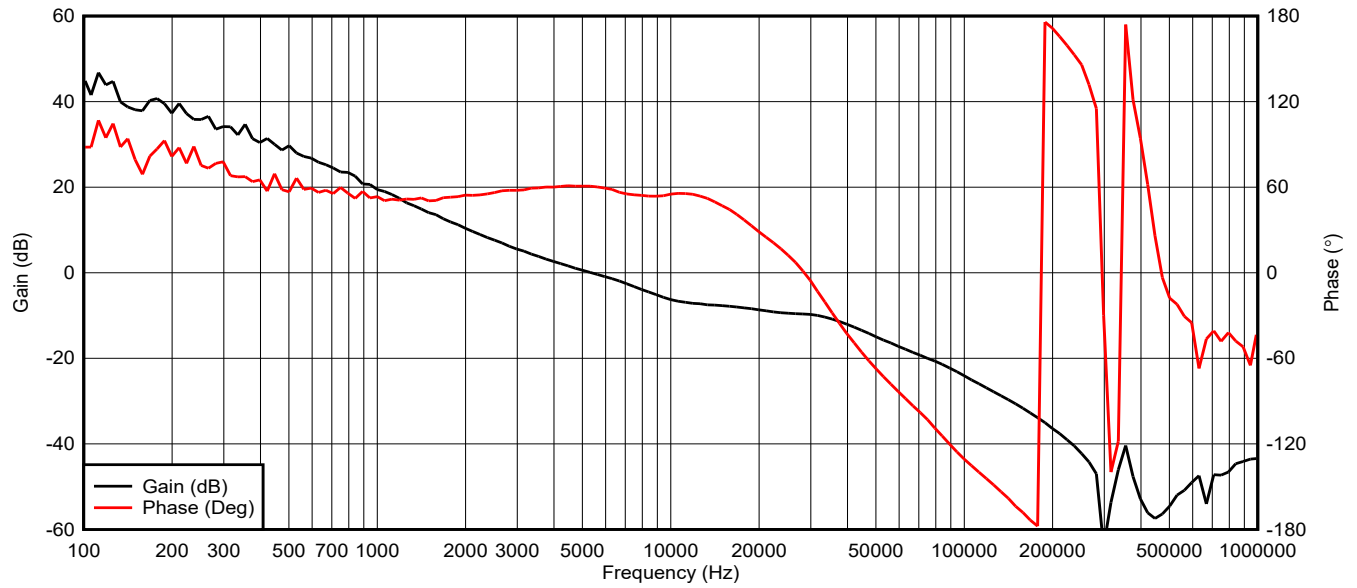


Figure 3-38. Control Loop Response, $V_{IN} = 12.0V$, $V_{OUT} = 28V$, $I_{OUT} = 5.0A$

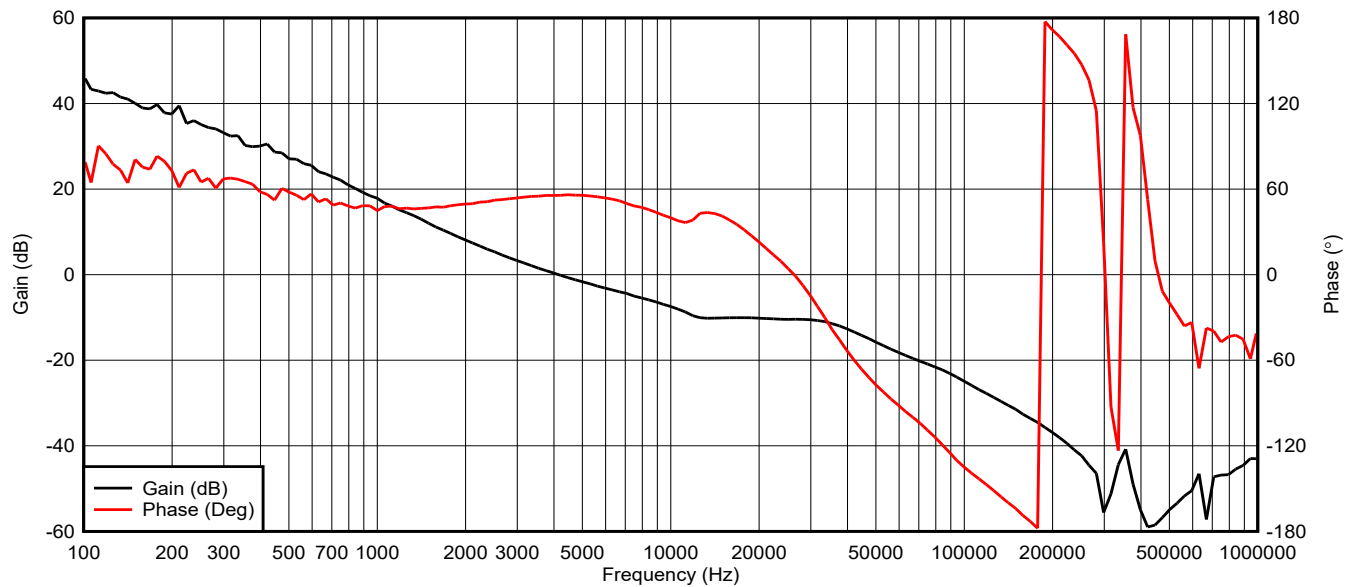


Figure 3-39. Control Loop Response, $V_{IN} = 12.0V$, $V_{OUT} = 36V$, $I_{OUT} = 5.0A$

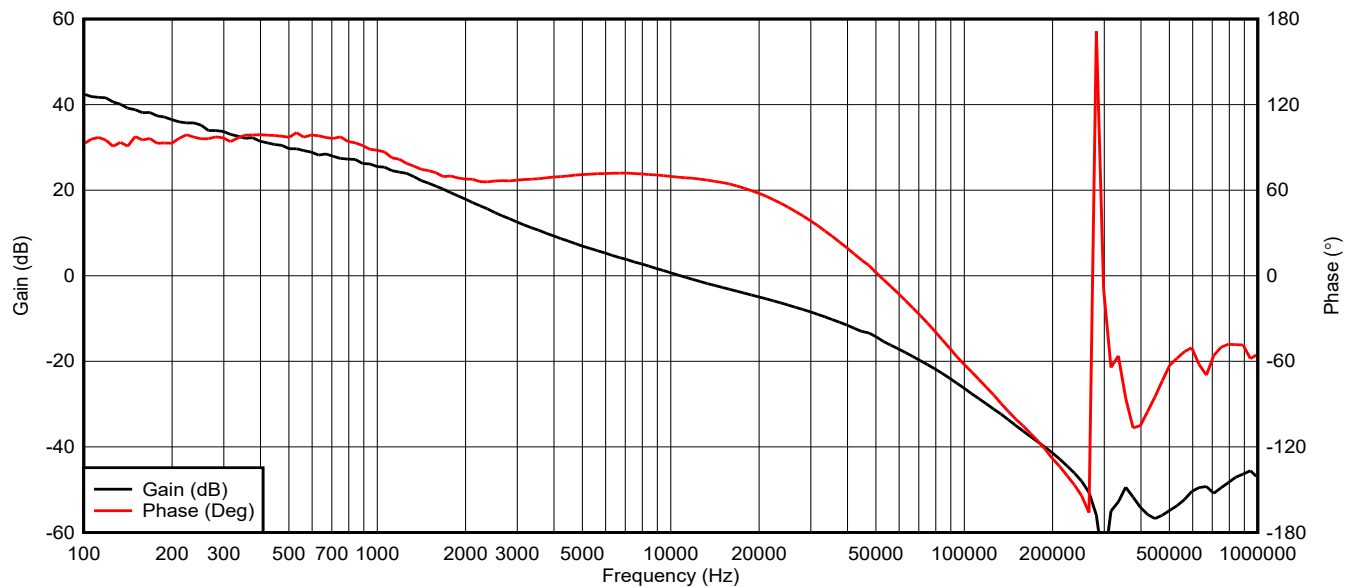


Figure 3-40. Control Loop Response, $V_{IN} = 20.0V$, $V_{OUT} = 5.0V$, $I_{OUT} = 5.0A$

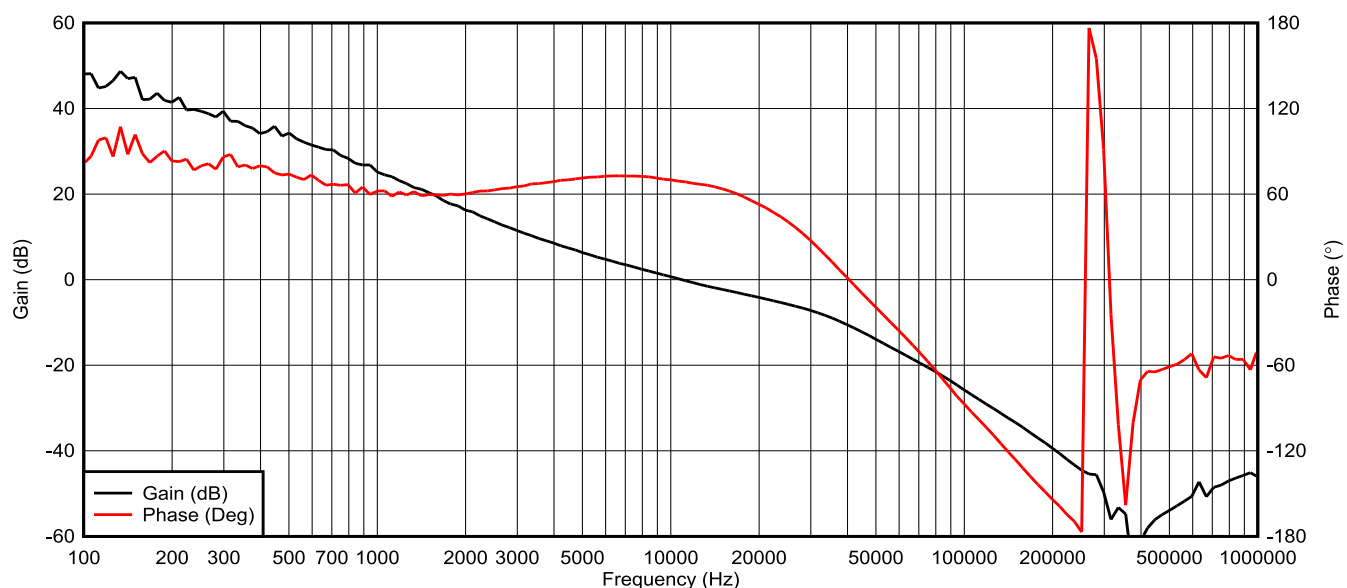


Figure 3-41. Control Loop Response, $V_{IN} = 20.0V$, $V_{OUT} = 20V$, $I_{OUT} = 5.0A$

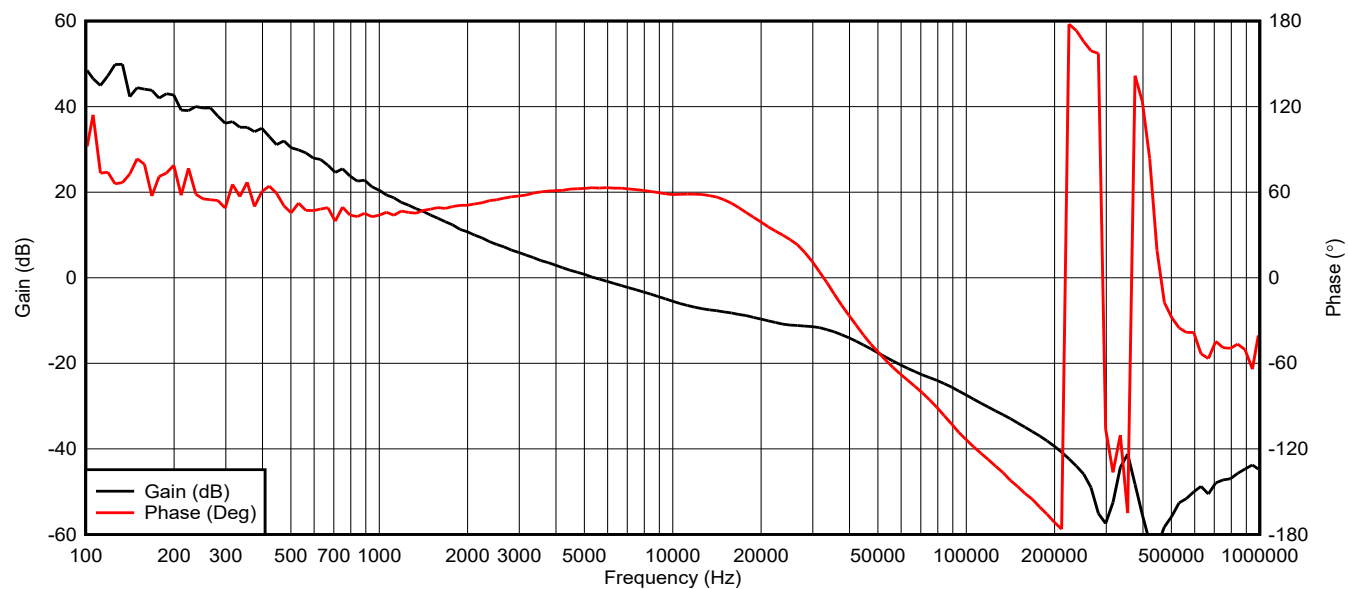


Figure 3-42. Control Loop Response, $V_{IN} = 20.0V$, $V_{OUT} = 48.0V$, $I_{OUT} = 5.0A$

4 Hardware Design Files

4.1 Schematic

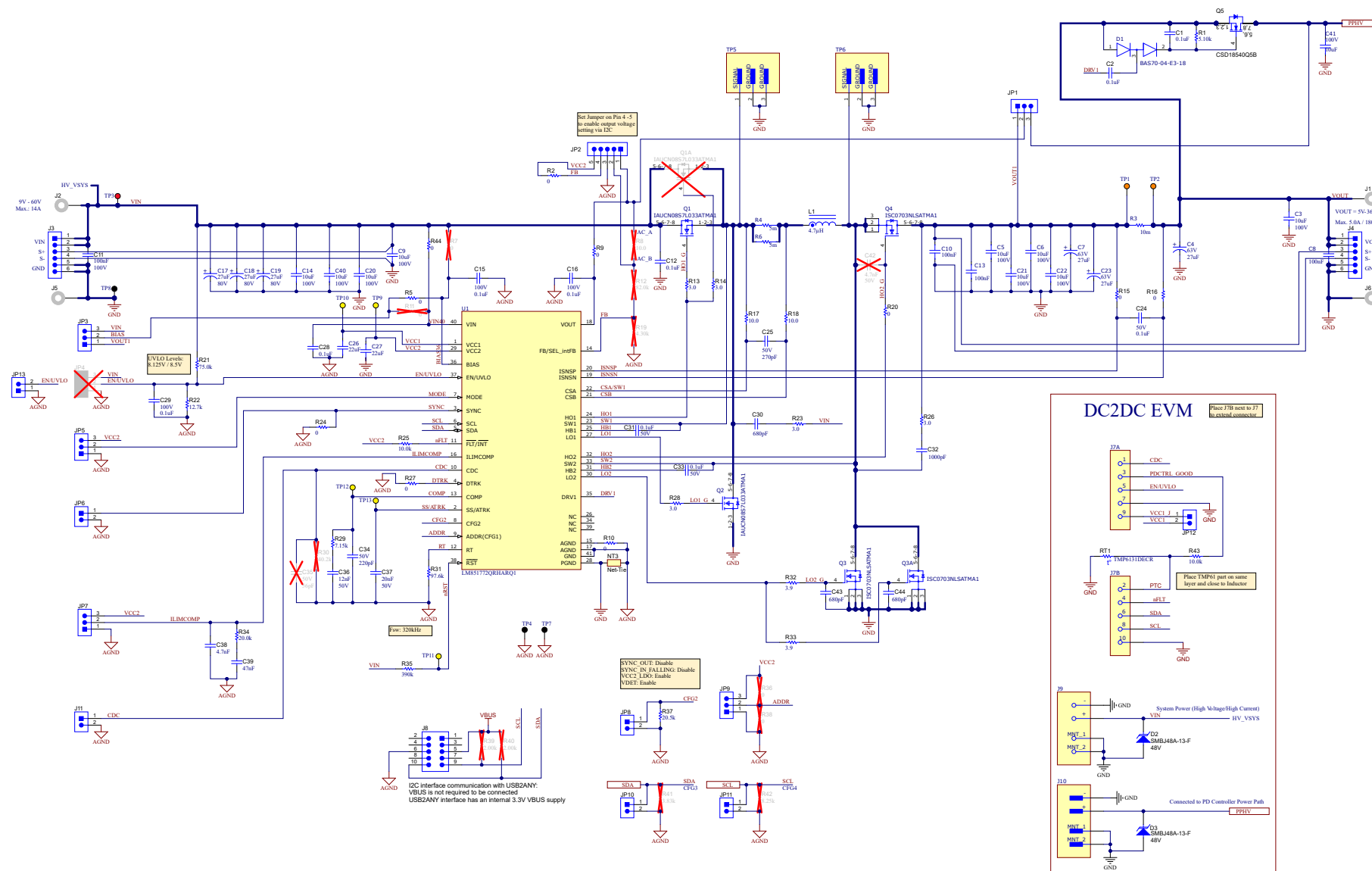


Figure 4-1. 4-Switch Buck-Boost Converter Schematic

4.2 Board Layout

Figure 4-2 through Figure 4-7 show the LM851772Q1EVM-PD PCB design.

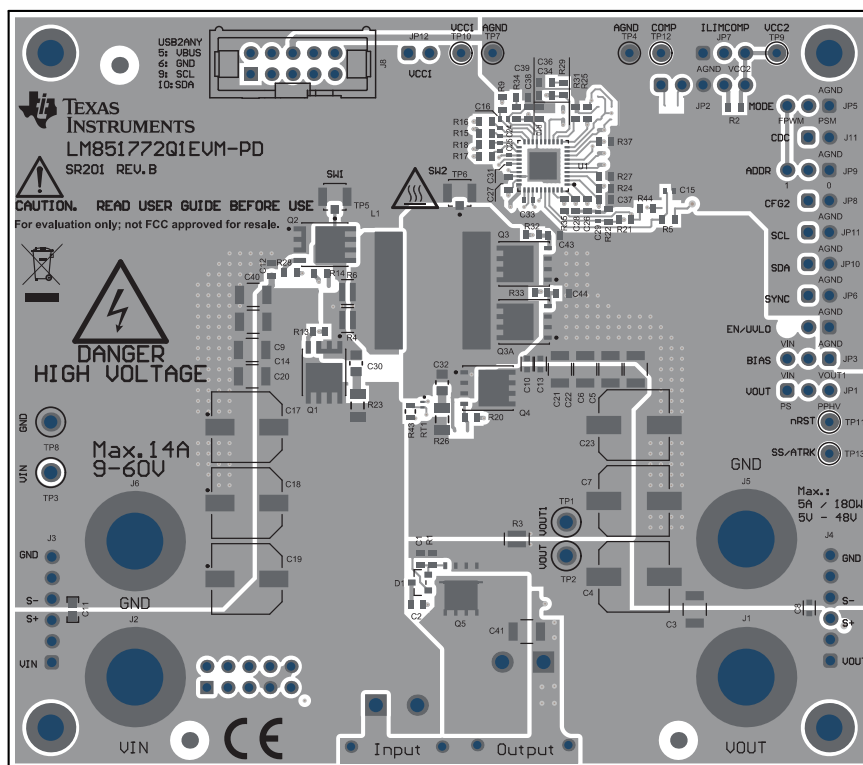


Figure 4-2. Top Silkscreen

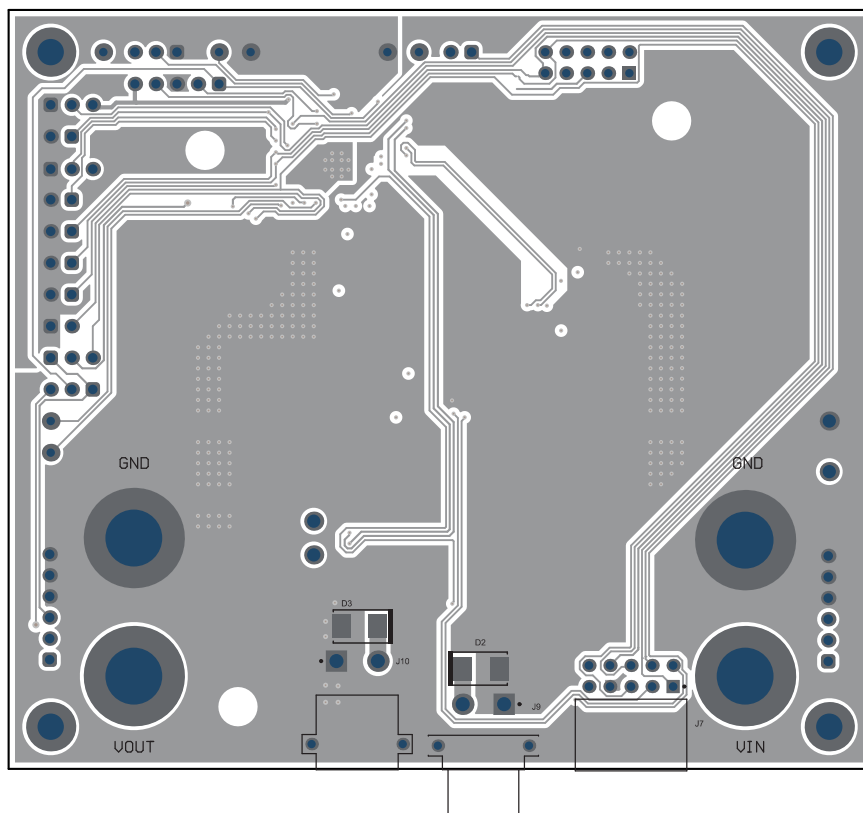


Figure 4-3. Bottom Silkscreen

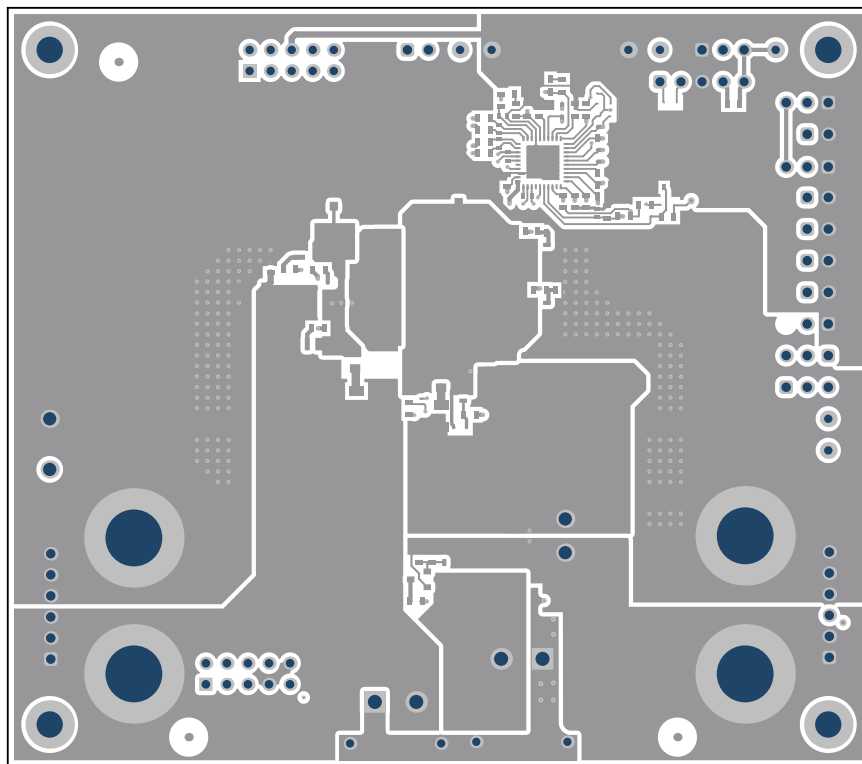


Figure 4-4. Top Layer

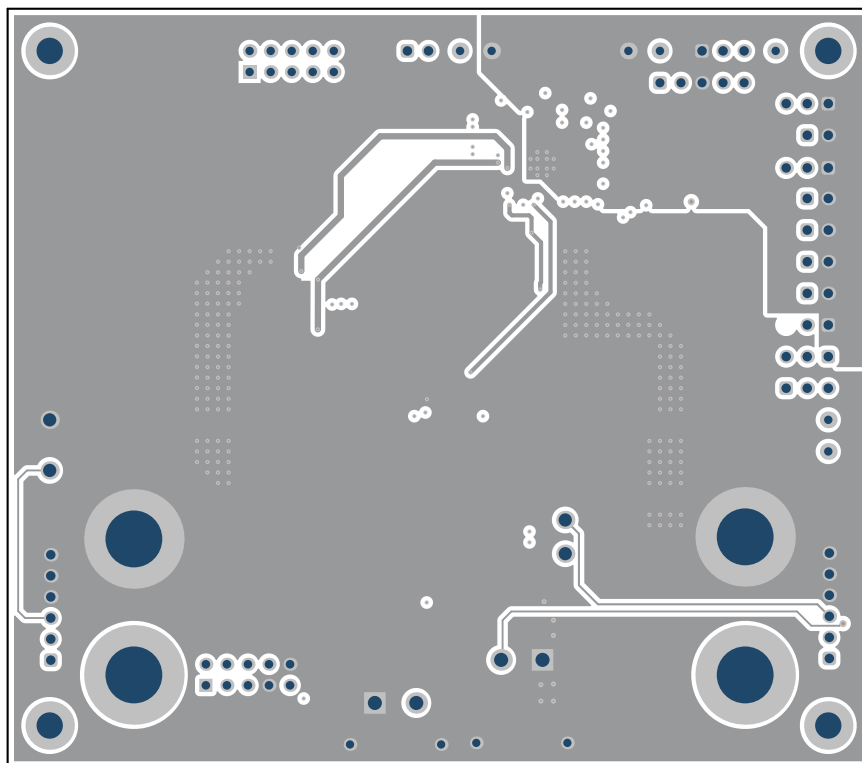


Figure 4-5. Mid-Layer 1

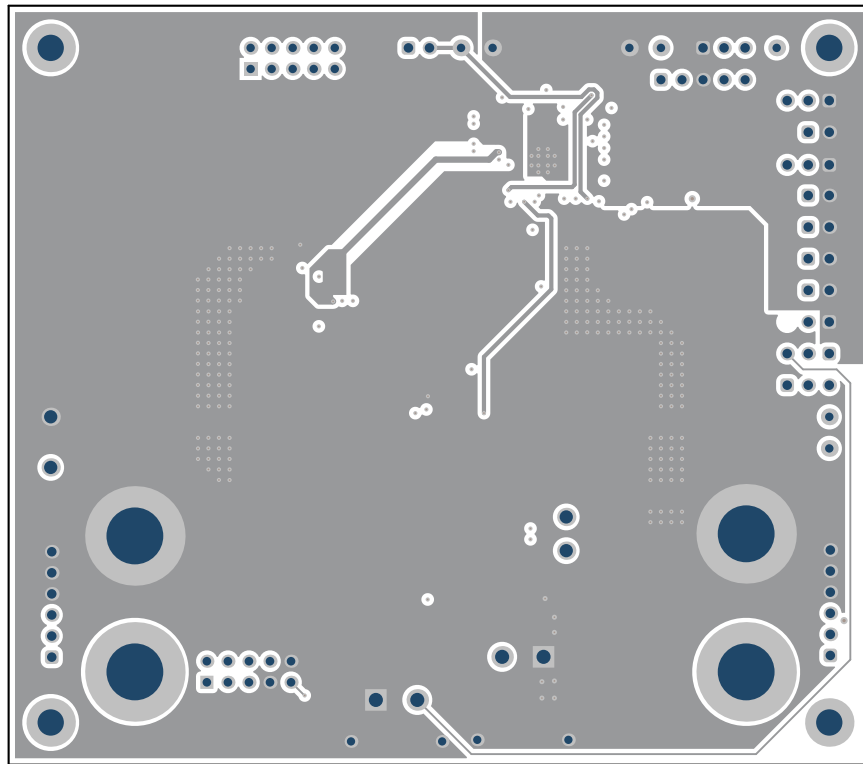


Figure 4-6. Mid-Layer 2

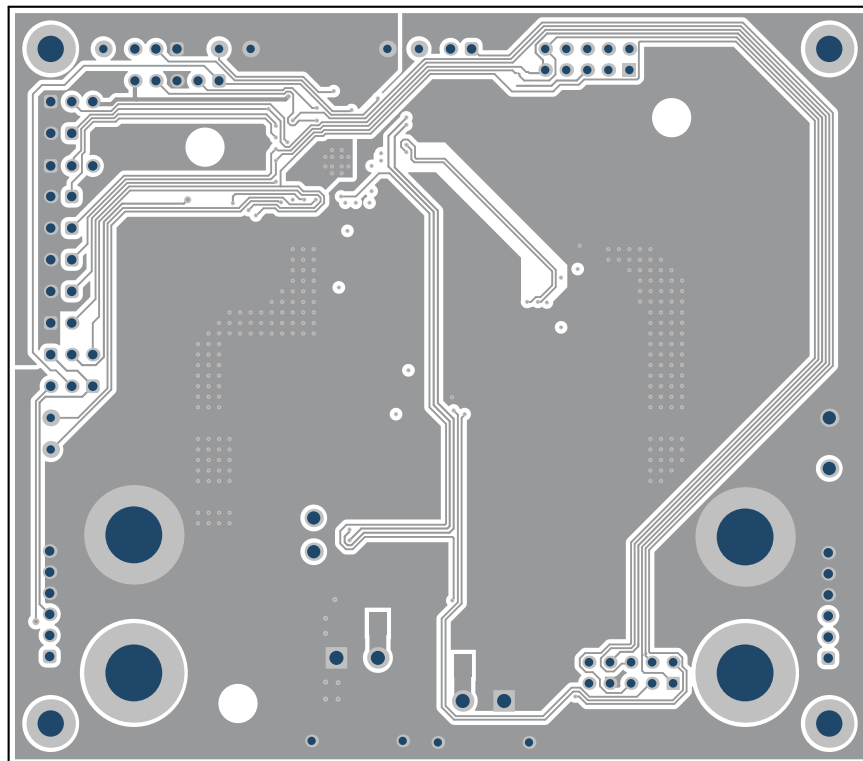


Figure 4-7. Bottom Layer

4.3 Bill of Materials

Designator	Quantity	Value	Part Number	Manufacturer	Description
C1, C2, C12, C28	4	0.1uF	CGA3E3X7S2A104K080AB	TDK	CAP, CERM, 0.1uF, 100V, +/- 10%, X7S, AEC-Q200 Grade 1, 0603
C3, C5, C6, C9, C14, C20, C21, C22, C40	9	10µF	CGA6P1X7R2A106K250AC	TDK	10µF ±10% 100V Ceramic Capacitor X7R 1210 (3225 Metric)
C4, C7, C23	3	27µF	A768KE276M1JLAE054	KEMET	Cap Aluminum Polymer 27uF 63V 20% Solder Cylindrical 54m Ohm 1175mA 2000 hr 125°C T/R
C8, C10, C13	3		GRM188R72A104KA35D	Murata	0.1µF ±10% 100V Ceramic Capacitor X7R 0603 (1608 Metric)
C11	1	0.1uF	GCJ188R72A104KA01D	MuRata	CAP, CERM, 0.1uF, 100V, +/- 10%, X7R, AEC-Q200 Grade 1, 0603
C15, C16, C29	3	0.1uF	GRM155R62A104KE14D	MuRata	CAP, CERM, 0.1uF, 100V, +/- 10%, X5R, 0402
C17, C18, C19	3	27µF	PCR1K270MCL1GS	Nichicon	27µF 80V Aluminum - Polymer Capacitors Radial, Can - SMD 38mOhm 4000 Hrs @ 125°C
C24	1	0.1uF	GRM155R71H104ME14D	MuRata	CAP, CERM, 0.1uF, 50V, +/- 20%, X7R, 0402
C25	1	270pF	GRM1555C1H271JA01D	MuRata	CAP, CERM, 270pF, 50V, +/- 5%, C0G/NP0, 0402
C26, C27	2	22µF	GRT188R61A226ME13D	Murata	Multi-Layer Ceramic Capacitor 22uF 10V X5R ±20% 0603 Paper T/R
C30	1	680pF	08051A681JAT2A	AVX	CAP, CERM, 680pF, 100V, +/- 5%, C0G/NP0, 0805
C31, C33	2	0.1uF	GCM155R71H104KE02D	MuRata	CAP, CERM, 0.1uF, 50V, +/- 10%, X7R, AEC-Q200 Grade 1, 0402
C32	1	1000pF	C0805C102J1GACTU	Kemet	CAP, CERM, 1000pF, 100V, +/- 5%, C0G/NP0, 0805
C34	1	220pF	06035A221FAT2A	AVX	CAP, CERM, 220pF, 50V, +/- 1%, C0G/NP0, 0603
C36	1	0.012uF	C0603C123K5RACTU	Kemet	CAP, CERM, 0.012µF, 50V, +/- 10%, X7R, AEC-Q200 Grade 1, 0603
C37	1	0.02uF	CC0603KRX7R9BB203	Yageo	CAP, CERM, 0.02µF, 50V, +/- 10%, X7R, 0603
C38	1	4700pF	06031C472JAT2A	AVX	CAP, CERM, 4700pF, 100V, +/- 5%, X7R, 0603
C39	1	0.047uF	06035C473JAT2A	AVX	CAP, CERM, 0.047µF, 50V, +/- 5%, X7R, 0603
C41	1	10µF	C3225X7R2A106K250AC	TDK	10µF ±10% 100V Ceramic Capacitor X7R 1210 (3225 Metric)
C43, C44	2	680pF	C0603C681J5GACTU	Kemet	CAP, CERM, 680pF, 50V, +/- 5%, C0G/NP0, 0603
D1	1		BAS70-04E3-18	Vishay	Diode Array 1 Pair Series Connection Schottky 70V 200mA (DC) Surface Mount TO-236-3, SC-59, SOT-23-3
D2, D3	2	48V	SMBJ48A-13F	Diodes Inc.	Diode, TVS, Uni, 48V, 77.4Vc, SMB
J1, J2, J5, J6	4		108-0740-001	Cinch Connectivity	Standard Banana Jack, Uninsulated, 15A
J3, J4	2		61300611121	Wurth Elektronik	Header, 2.54mm, 6x1, Gold, TH
J7	1		SSW-105-02G-D-RA	Samtec	10 Position Receptacle Connector 0.100" (2.54mm) Through Hole, Right Angle Gold
J8	1		N2510-6002-RB	3M	Header (shrouded), 100mil, 5x2, High-Temperature, Gold, TH

Designator	Quantity	Value	Part Number	Manufacturer	Description
J9	1		XT30PW-F	Amass	Socket, DC supply, XT30, female, PIN: 2, on PCBs, THT, yellow, 15A
J10	1		XT30PW-M	Amass	Socket, DC supply, XT30, male, PIN: 2, on PCBs, THT, yellow, 15A, 500V
J11, JP6, JP8, JP10, JP11, JP12	6		61300211121	Würth Elektronik	Header, 2.54mm, 2x1, Gold, TH
JP1, JP3, JP4, JP5, JP7, JP9	6		61300311121	Würth Elektronik	Header, 2.54mm, 3x1, Gold, TH
JP2	1		61300511121	Würth Elektronik	Header, 2.54mm, 5x1, Gold, TH
L1	1	4.7uH	74439370047	Würth Elektronik	Inductor, Shielded, Metal Composite, 4.7μH, 17A, 0.00385ohm, SMD
Q1, Q2	2		IAUCN08S7L033ATMA1	Infineon	MOSFET N-Channel 80V 130A (Tj) 118W (Tc) Surface Mount PG-TDSON-8-34
Q3, Q3A, Q4 ⁽¹⁾	3		ISC0703NLSATMA1	Infineon	MOSFET N-Channel 60V 23A (Ta), 135A (Tc) 3W (Ta), 100W (Tc) Surface Mount PG-TDSON-8
Q5	1	60V	CSD18540Q5B	Texas Instruments	MOSFET, N-CH, 60V, 100A, DNK0008A (VSON-CLIP-8)
R1	1	5.10k	RC0603FR-075K1L	Yageo	RES, 5.10k, 1%, 0.1W, 0603
R2, R5, R9, R10, R15, R16, R20, R24, R27, R44	10	0	RMCF0603ZT0R00	Stackpole Electronics Inc	RES, 0, 1%, 0.1W, AEC-Q200 Grade 0, 0603
R3	1	10m	KRL2012E-C-R010F-T05	Susumu	10 mOhms ±1% 1W Chip Resistor Wide 0805 (2012 Metric), 0508 Automotive AEC-Q200, Current Sense Metal Foil
R4, R6	2	5m	KRL2012E-M-R005F-T5	Susumu	5 mOhms ±1% 1W Chip Resistor Wide 0805 (2012 Metric), 0508 Automotive AEC-Q200, Current Sense Metal Foil
R13, R14, R28	3	3	CRCW06033R00JNEA	Vishay-Dale	RES, 3.0, 5%, 0.1W, AEC-Q200 Grade 0, 0603
R17, R18	2	10	CRCW060310R0FKEAHP	Vishay-Dale	RES, 10.0, 1%, 0.25W, AEC-Q200 Grade 0, 0603
R21	1	75.0k	RC0603FR-0775KL	Yageo	RES, 75.0k, 1%, 0.1W, 0603
R22	1	12.7k	RC0603FR-0712K7L	Yageo	RES, 12.7k, 1%, 0.1W, 0603
R23, R26	2	3	CRCW12063R00JNEA	Vishay-Dale	RES, 3.0, 5%, 0.25W, AEC-Q200 Grade 0, 1206
R25	1	10.0k	RT0603BRD0710KL	Yageo America	RES, 10.0k, 0.1%, 0.1W, 0603
R29	1	7.15k	CRCW06037K15FKEA	Vishay-Dale	RES, 7.15k, 1%, 0.1W, AEC-Q200 Grade 0, 0603
R31	1	97.6k	RC0603FR-0797K6L	Yageo	RES, 97.6k, 1%, 0.1W, 0603
R32, R33	2	3.9	CRCW06033R90JNEA	Vishay-Dale	RES, 3.9, 5%, 0.1W, AEC-Q200 Grade 0, 0603
R34	1	20.0k	CRCW060320K0FKEA	Vishay-Dale	RES, 20.0k, 1%, 0.1W, AEC-Q200 Grade 0, 0603
R35	1	390k	RC0603FR-07390KL	Yageo	RES, 390k, 1%, 0.1W, 0603
R37	1	20.5k	CRCW060320K5FKEA	Vishay-Dale	RES, 20.5 k, 1%, 0.1W, AEC-Q200 Grade 0, 0603
R43	1	10.0k	TNPW060310K0BEEA	Vishay-Dale	RES, 10.0k, 0.1%, 0.1W, AEC-Q200 Grade 1, 0603
RT1	1	10k	TMP6131DECR	Texas Instruments	±1% tolerance 10kΩ linear thermistor available in 0402 and 0603 package options 2X1SON -40 to 125

Designator	Quantity	Value	Part Number	Manufacturer	Description
U1	1		LM851772QRHARQ1	Texas Instruments	80V VIN 4 Switch Buck-Boost Controller with I2C
C35	0	56pF	06035A560FAT2A	AVX	CAP, CERM, 56pF, 50V, +/- 1%, COG/NP0, 0603
C42	0	4700pF	C0603C472K5RACTU	Kemet	CAP, CERM, 4700pF, 50V, +/- 10%, X7R, 0603
Q1A	0		IAUCN08S7L033ATMA1	Infineon	MOSFET N-Channel 80V 130A (Tj) 118W (Tc) Surface Mount PG-TDSON-8-34
R7, R11, R36, R38	0	0	RMCF0603ZT0R00	Stackpole Electronics Inc	RES, 0, 1%, 0.1W, AEC-Q200 Grade 0, 0603
R8	0	10	RC0603FR-0710RL	Yageo	RES, 10.0, 1%, 0.1W, 0603
R12	0	82.0k	RC0603FR-0782KL	Yageo	RES, 82.0k, 1%, 0.1W, 0603
R19	0	4.30k	RC0603FR-074K3L	Yageo	RES, 4.30k, 1%, 0.1W, 0603
R30	0	40.2k	CRCW060340K2FKEA	Vishay-Dale	RES, 40.2k, 1%, 0.1W, AEC-Q200 Grade 0, 0603
R39, R40	0	2.00k	RC0603FR-072KL	Yageo	RES, 2.00k, 1%, 0.1W, 0603
R41	0	3.83k	CRCW06033K83FKEA	Vishay-Dale	RES, 3.83k, 1%, 0.1W, AEC-Q200 Grade 0, 0603
R42	0	8.25k	RC0603FR-078K25L	Yageo	RES, 8.25k, 1%, 0.1W, 0603

(1) Alternate part for Q3, Q3A, Q4: IAUC60N06S5L073

NOTE: EVM Rev B updates:

- R37 set to 20.5k
- Silk screen update on warning signs

5 Additional Information

5.1 General Texas Instruments High Voltage Evaluation (TI HV EVM) User Safety Guidelines



Always follow TI's set-up and application instructions, including use of all interface components within the recommended electrical rated voltage and power limits. Always use electrical safety precautions to verify that your personal safety and those working around you. For further information, contact TI's Product Information Center <http://ti.com/customer support>.

Save all warnings and instructions for future reference.

WARNING

Failure to follow warnings and instructions can result in personal injury, property damage or death due to electrical shock and burn hazards.

The term TI HV EVM refers to an electronic device typically provided as an open framed, unenclosed printed circuit board assembly. It is *intended strictly for use in development laboratory environments, solely for qualified professional users having training, expertise and knowledge of electrical safety risks in development and application of high voltage electrical circuits. Any other use and/or application are strictly prohibited by Texas Instruments.* If you are not suitably qualified, then immediately stop from further use of the HV EVM.

1. Work Area Safety:
 - a. Keep work area clean and orderly.
 - b. Qualified observers must be present anytime circuits are energized.
 - c. Effective barriers and signage must be present in the area where the TI HV EVM and the interface electronics are energized, indicating operation of accessible high voltages can be present, for the purpose of protecting inadvertent access.
 - d. All interface circuits, power supplies, evaluation modules, instruments, meters, scopes, and other related apparatus used in a development environment exceeding 50Vrms/75VDC must be electrically located within a protected Emergency Power Off EPO protected power strip.
 - e. Use stable and non-conductive work surface.
 - f. Use adequately insulated clamps and wires to attach measurement probes and instruments. No freehand testing whenever possible.
2. Electrical Safety:
 - a. As a precautionary measure, a good engineering practice is to assume that the entire EVM can have fully accessible and active high voltages.
 - b. De-energize the TI HV EVM and all the inputs, outputs and electrical loads before performing any electrical or other diagnostic measurements. Revalidate that TI HV EVM power has been safely de-energized.
 - c. With the EVM confirmed de-energized, proceed with required electrical circuit configurations, wiring, measurement equipment hook-ups and other application needs, while still assuming the EVM circuit and measuring instruments are electrically live.
 - d. Once EVM readiness is complete, energize the EVM as intended.

WARNING

While the EVM is energized, never touch the EVM or the electrical circuits, as the EVM or the electrical circuits can be at high voltages capable of causing electrical shock hazard.

3. Personal Safety
 - a. Wear personal protective equipment, for example, latex gloves or safety glasses with side shields or protect EVM in an adequate lucent plastic box with interlocks from accidental touch.

Limitation for safe use:

EVMS are not to be used as all or part of a production unit.

5.2 Trademarks

USB Type-C® is a registered trademark of USB Implementers Forum, Inc..

All trademarks are the property of their respective owners.

6 Revision History

NOTE: Page numbers for previous revisions may differ from page numbers in the current version.

Changes from Revision * (July 2026) to Revision A (July 2026)	Page
• Added operating temperature specification in Table 1-1	2
• Updated the schematic: R37 value for setting of CFG2.....	16
• Updated R37 to 20.5k in the <i>Bill of Materials</i>	20

STANDARD TERMS FOR EVALUATION MODULES

1. **Delivery:** TI delivers TI evaluation boards, kits, or modules, including any accompanying demonstration software, components, and/or documentation which may be provided together or separately (collectively, an "EVM" or "EVMs") to the User ("User") in accordance with the terms set forth herein. User's acceptance of the EVM is expressly subject to the following terms.
 - 1.1 EVMs are intended solely for product or software developers for use in a research and development setting to facilitate feasibility evaluation, experimentation, or scientific analysis of TI semiconductors products. EVMs have no direct function and are not finished products. EVMs shall not be directly or indirectly assembled as a part or subassembly in any finished product. For clarification, any software or software tools provided with the EVM ("Software") shall not be subject to the terms and conditions set forth herein but rather shall be subject to the applicable terms that accompany such Software
 - 1.2 EVMs are not intended for consumer or household use. EVMs may not be sold, sublicensed, leased, rented, loaned, assigned, or otherwise distributed for commercial purposes by Users, in whole or in part, or used in any finished product or production system.
2. **Limited Warranty and Related Remedies/Disclaimers:**
 - 2.1 These terms do not apply to Software. The warranty, if any, for Software is covered in the applicable Software License Agreement.
 - 2.2 TI warrants that the TI EVM will conform to TI's published specifications for ninety (90) days after the date TI delivers such EVM to User. Notwithstanding the foregoing, TI shall not be liable for a nonconforming EVM if (a) the nonconformity was caused by neglect, misuse or mistreatment by an entity other than TI, including improper installation or testing, or for any EVMs that have been altered or modified in any way by an entity other than TI, (b) the nonconformity resulted from User's design, specifications or instructions for such EVMs or improper system design, or (c) User has not paid on time. Testing and other quality control techniques are used to the extent TI deems necessary. TI does not test all parameters of each EVM. User's claims against TI under this Section 2 are void if User fails to notify TI of any apparent defects in the EVMs within ten (10) business days after delivery, or of any hidden defects with ten (10) business days after the defect has been detected.
 - 2.3 TI's sole liability shall be at its option to repair or replace EVMs that fail to conform to the warranty set forth above, or credit User's account for such EVM. TI's liability under this warranty shall be limited to EVMs that are returned during the warranty period to the address designated by TI and that are determined by TI not to conform to such warranty. If TI elects to repair or replace such EVM, TI shall have a reasonable time to repair such EVM or provide replacements. Repaired EVMs shall be warranted for the remainder of the original warranty period. Replaced EVMs shall be warranted for a new full ninety (90) day warranty period.

WARNING

Evaluation Kits are intended solely for use by technically qualified, professional electronics experts who are familiar with the dangers and application risks associated with handling electrical mechanical components, systems, and subsystems.

User shall operate the Evaluation Kit within TI's recommended guidelines and any applicable legal or environmental requirements as well as reasonable and customary safeguards. Failure to set up and/or operate the Evaluation Kit within TI's recommended guidelines may result in personal injury or death or property damage. Proper set up entails following TI's instructions for electrical ratings of interface circuits such as input, output and electrical loads.

NOTE:

EXPOSURE TO ELECTROSTATIC DISCHARGE (ESD) MAY CAUSE DEGRADATION OR FAILURE OF THE EVALUATION KIT; TI RECOMMENDS STORAGE OF THE EVALUATION KIT IN A PROTECTIVE ESD BAG.

3 Regulatory Notices:

3.1 United States

3.1.1 Notice applicable to EVMs not FCC-Approved:

FCC NOTICE: This kit is designed to allow product developers to evaluate electronic components, circuitry, or software associated with the kit to determine whether to incorporate such items in a finished product and software developers to write software applications for use with the end product. This kit is not a finished product and when assembled may not be resold or otherwise marketed unless all required FCC equipment authorizations are first obtained. Operation is subject to the condition that this product not cause harmful interference to licensed radio stations and that this product accept harmful interference. Unless the assembled kit is designed to operate under part 15, part 18 or part 95 of this chapter, the operator of the kit must operate under the authority of an FCC license holder or must secure an experimental authorization under part 5 of this chapter.

3.1.2 For EVMs annotated as FCC – FEDERAL COMMUNICATIONS COMMISSION Part 15 Compliant:

CAUTION

This device complies with part 15 of the FCC Rules. Operation is subject to the following two conditions: (1) This device may not cause harmful interference, and (2) this device must accept any interference received, including interference that may cause undesired operation.

Changes or modifications not expressly approved by the party responsible for compliance could void the user's authority to operate the equipment.

FCC Interference Statement for Class A EVM devices

NOTE: This equipment has been tested and found to comply with the limits for a Class A digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. Operation of this equipment in a residential area is likely to cause harmful interference in which case the user will be required to correct the interference at his own expense.

FCC Interference Statement for Class B EVM devices

NOTE: This equipment has been tested and found to comply with the limits for a Class B digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates, uses and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following measures:

- *Reorient or relocate the receiving antenna.*
- *Increase the separation between the equipment and receiver.*
- *Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.*
- *Consult the dealer or an experienced radio/TV technician for help.*

3.2 Canada

3.2.1 For EVMs issued with an Industry Canada Certificate of Conformance to RSS-210 or RSS-247

Concerning EVMs Including Radio Transmitters:

This device complies with Industry Canada license-exempt RSSs. Operation is subject to the following two conditions:

(1) this device may not cause interference, and (2) this device must accept any interference, including interference that may cause undesired operation of the device.

Concernant les EVMs avec appareils radio:

Le présent appareil est conforme aux CNR d'Industrie Canada applicables aux appareils radio exempts de licence. L'exploitation est autorisée aux deux conditions suivantes: (1) l'appareil ne doit pas produire de brouillage, et (2) l'utilisateur de l'appareil doit accepter tout brouillage radioélectrique subi, même si le brouillage est susceptible d'en compromettre le fonctionnement.

Concerning EVMs Including Detachable Antennas:

Under Industry Canada regulations, this radio transmitter may only operate using an antenna of a type and maximum (or lesser) gain approved for the transmitter by Industry Canada. To reduce potential radio interference to other users, the antenna type and its gain should be so chosen that the equivalent isotropically radiated power (e.i.r.p.) is not more than that necessary for successful communication. This radio transmitter has been approved by Industry Canada to operate with the antenna types listed in the user guide with the maximum permissible gain and required antenna impedance for each antenna type indicated. Antenna types not included in this list, having a gain greater than the maximum gain indicated for that type, are strictly prohibited for use with this device.

Concernant les EVMs avec antennes détachables

Conformément à la réglementation d'Industrie Canada, le présent émetteur radio peut fonctionner avec une antenne d'un type et d'un gain maximal (ou inférieur) approuvé pour l'émetteur par Industrie Canada. Dans le but de réduire les risques de brouillage radioélectrique à l'intention des autres utilisateurs, il faut choisir le type d'antenne et son gain de sorte que la puissance isotrope rayonnée équivalente (p.i.r.e.) ne dépasse pas l'intensité nécessaire à l'établissement d'une communication satisfaisante. Le présent émetteur radio a été approuvé par Industrie Canada pour fonctionner avec les types d'antenne énumérés dans le manuel d'usage et ayant un gain admissible maximal et l'impédance requise pour chaque type d'antenne. Les types d'antenne non inclus dans cette liste, ou dont le gain est supérieur au gain maximal indiqué, sont strictement interdits pour l'exploitation de l'émetteur.

3.3 Japan

3.3.1 *Notice for EVMs delivered in Japan:* Please see http://www.tij.co.jp/sds/ti_ja/general/eStore/notice_01.page 日本国内に輸入される評価用キット、ボードについては、次のところをご覧ください。

<https://www.ti.com/ja-jp/legal/notice-for-evaluation-kits-delivered-in-japan.html>

3.3.2 *Notice for Users of EVMs Considered "Radio Frequency Products" in Japan:* EVMs entering Japan may not be certified by TI as conforming to Technical Regulations of Radio Law of Japan.

If User uses EVMs in Japan, not certified to Technical Regulations of Radio Law of Japan, User is required to follow the instructions set forth by Radio Law of Japan, which includes, but is not limited to, the instructions below with respect to EVMs (which for the avoidance of doubt are stated strictly for convenience and should be verified by User):

1. Use EVMs in a shielded room or any other test facility as defined in the notification #173 issued by Ministry of Internal Affairs and Communications on March 28, 2006, based on Sub-section 1.1 of Article 6 of the Ministry's Rule for Enforcement of Radio Law of Japan,
2. Use EVMs only after User obtains the license of Test Radio Station as provided in Radio Law of Japan with respect to EVMs, or
3. Use of EVMs only after User obtains the Technical Regulations Conformity Certification as provided in Radio Law of Japan with respect to EVMs. Also, do not transfer EVMs, unless User gives the same notice above to the transferee. Please note that if User does not follow the instructions above, User will be subject to penalties of Radio Law of Japan.

【無線電波を送信する製品の開発キットをお使いになる際の注意事項】 開発キットの中には技術基準適合証明を受けていないものがあります。技術適合証明を受けていないもののご使用に際しては、電波法遵守のため、以下のいずれかの措置を取っていただく必要がありますのでご注意ください。

1. 電波法施行規則第6条第1項第1号に基づく平成18年3月28日総務省告示第173号で定められた電波暗室等の試験設備でご使用いただく。
2. 実験局の免許を取得後ご使用いただく。
3. 技術基準適合証明を取得後ご使用いただく。

なお、本製品は、上記の「ご使用にあたっての注意」を譲渡先、移転先に通知しない限り、譲渡、移転できないものとします。

上記を遵守頂けない場合は、電波法の罰則が適用される可能性があることをご留意ください。 日本テキサス・インスツルメンツ株式会社
東京都新宿区西新宿 6 丁目 2 4 番 1 号
西新宿三井ビル

3.3.3 *Notice for EVMs for Power Line Communication:* Please see http://www.tij.co.jp/sds/ti_ja/general/eStore/notice_02.page

電力線搬送波通信についての開発キットをお使いになる際の注意事項については、次のところをご覧ください。<https://www.ti.com/ja-jp/legal/notice-for-evaluation-kits-for-power-line-communication.html>

3.4 European Union

3.4.1 *For EVMs subject to EU Directive 2014/30/EU (Electromagnetic Compatibility Directive):*

This is a class A product intended for use in environments other than domestic environments that are connected to a low-voltage power-supply network that supplies buildings used for domestic purposes. In a domestic environment this product may cause radio interference in which case the user may be required to take adequate measures.

4 *EVM Use Restrictions and Warnings:*

4.1 EVMS ARE NOT FOR USE IN FUNCTIONAL SAFETY AND/OR SAFETY CRITICAL EVALUATIONS, INCLUDING BUT NOT LIMITED TO EVALUATIONS OF LIFE SUPPORT APPLICATIONS.

4.2 User must read and apply the user guide and other available documentation provided by TI regarding the EVM prior to handling or using the EVM, including without limitation any warning or restriction notices. The notices contain important safety information related to, for example, temperatures and voltages.

4.3 *Safety-Related Warnings and Restrictions:*

4.3.1 User shall operate the EVM within TI's recommended specifications and environmental considerations stated in the user guide, other available documentation provided by TI, and any other applicable requirements and employ reasonable and customary safeguards. Exceeding the specified performance ratings and specifications (including but not limited to input and output voltage, current, power, and environmental ranges) for the EVM may cause personal injury or death, or property damage. If there are questions concerning performance ratings and specifications, User should contact a TI field representative prior to connecting interface electronics including input power and intended loads. Any loads applied outside of the specified output range may also result in unintended and/or inaccurate operation and/or possible permanent damage to the EVM and/or interface electronics. Please consult the EVM user guide prior to connecting any load to the EVM output. If there is uncertainty as to the load specification, please contact a TI field representative. During normal operation, even with the inputs and outputs kept within the specified allowable ranges, some circuit components may have elevated case temperatures. These components include but are not limited to linear regulators, switching transistors, pass transistors, current sense resistors, and heat sinks, which can be identified using the information in the associated documentation. When working with the EVM, please be aware that the EVM may become very warm.

4.3.2 EVMs are intended solely for use by technically qualified, professional electronics experts who are familiar with the dangers and application risks associated with handling electrical mechanical components, systems, and subsystems. User assumes all responsibility and liability for proper and safe handling and use of the EVM by User or its employees, affiliates, contractors or designees. User assumes all responsibility and liability to ensure that any interfaces (electronic and/or mechanical) between the EVM and any human body are designed with suitable isolation and means to safely limit accessible leakage currents to minimize the risk of electrical shock hazard. User assumes all responsibility and liability for any improper or unsafe handling or use of the EVM by User or its employees, affiliates, contractors or designees.

4.4 User assumes all responsibility and liability to determine whether the EVM is subject to any applicable international, federal, state, or local laws and regulations related to User's handling and use of the EVM and, if applicable, User assumes all responsibility and liability for compliance in all respects with such laws and regulations. User assumes all responsibility and liability for proper disposal and recycling of the EVM consistent with all applicable international, federal, state, and local requirements.

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Last updated 10/2025