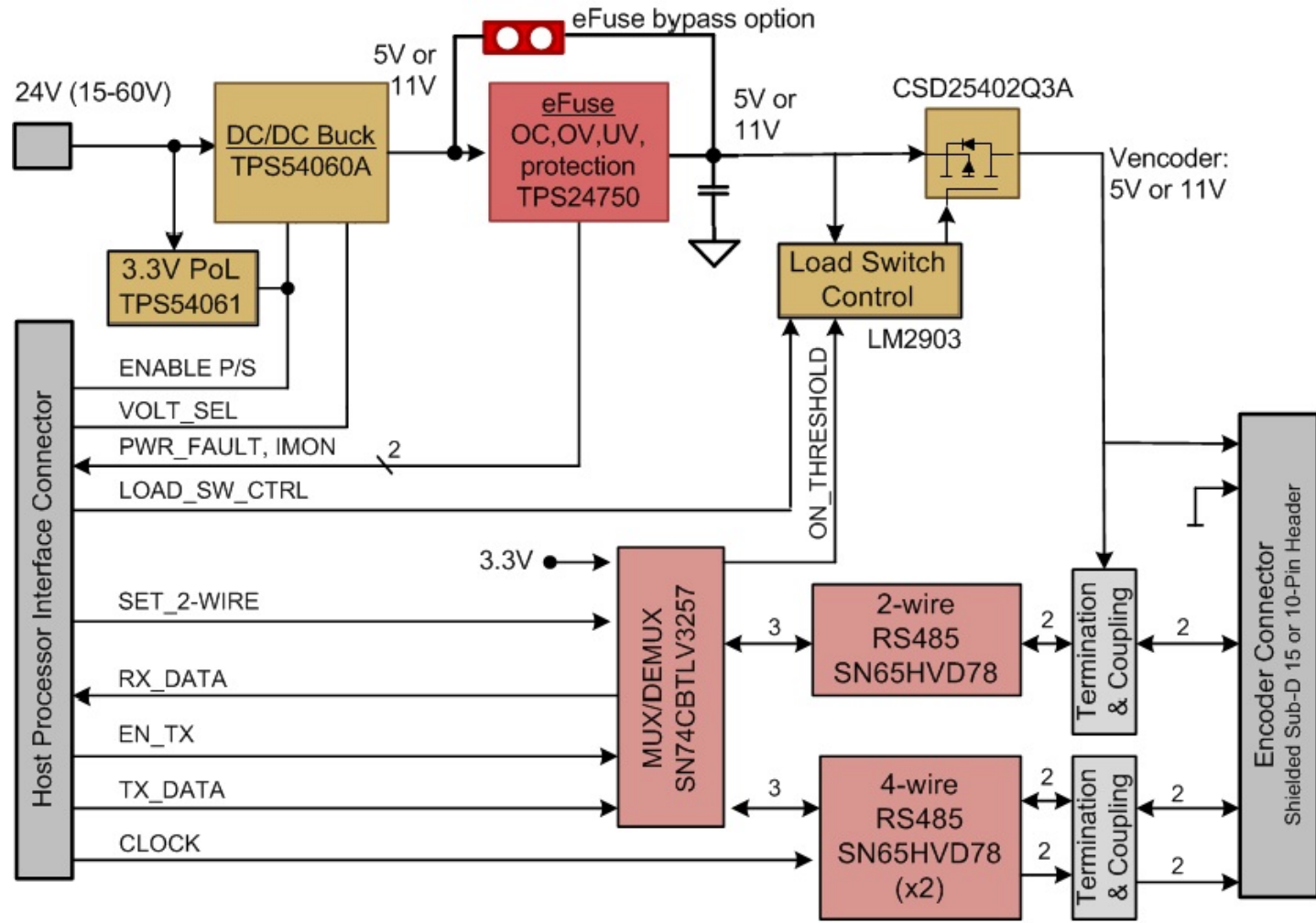
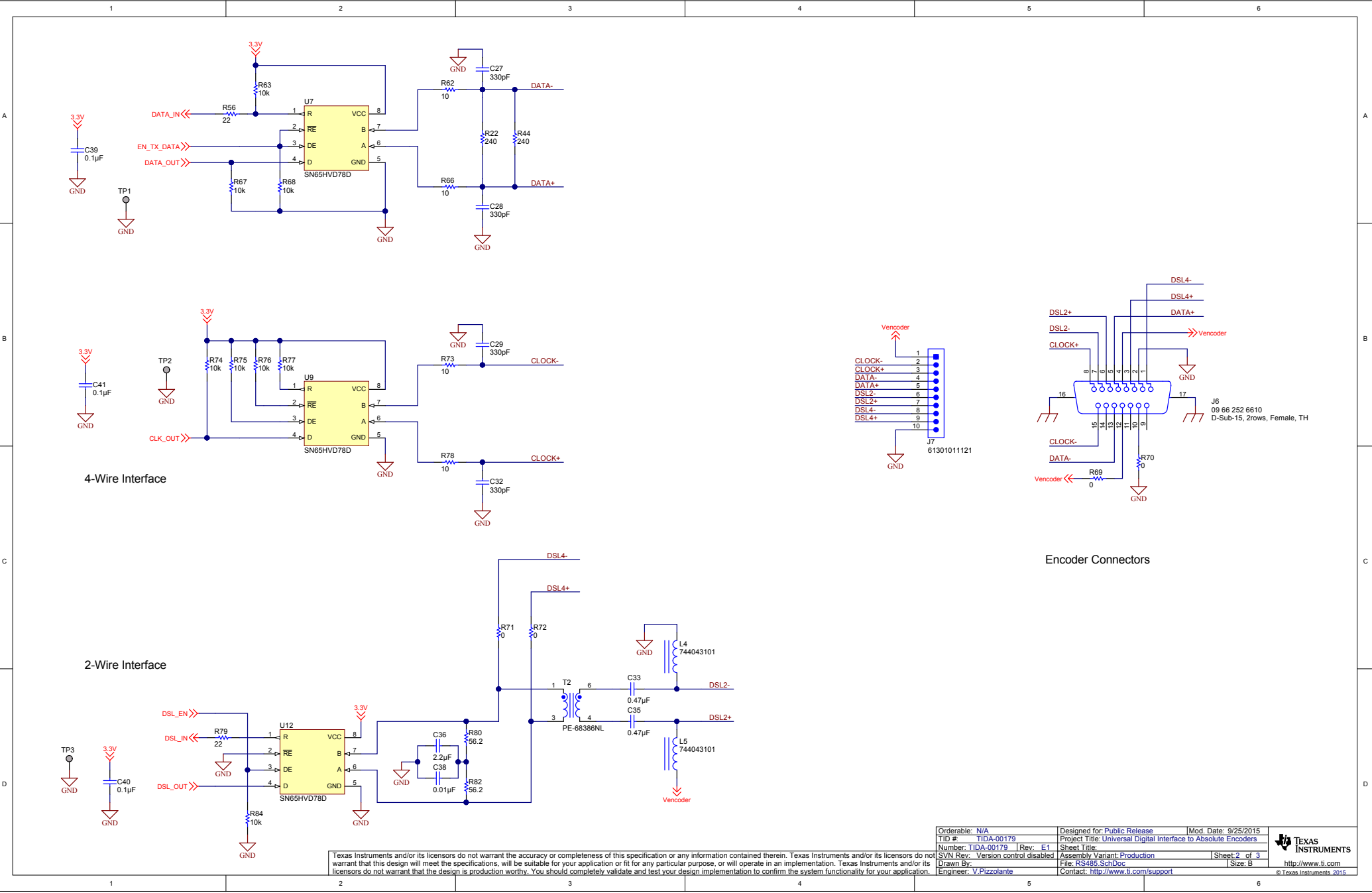


Revision History	
Revision	Notes



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TID #: TIDA-00179	Project Title: Universal Digital Interface to Absolute Encoders	
Number: TIDA-00179	Rev: E1	Sheet Title:
SVN Rev: Version control disabled	Assembly Variant: Production	Sheet: 1 of 3
Drawn By:	File: CoverSheet_SchDoc	Size: B
Engineer: V.Pizzolante	Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>	

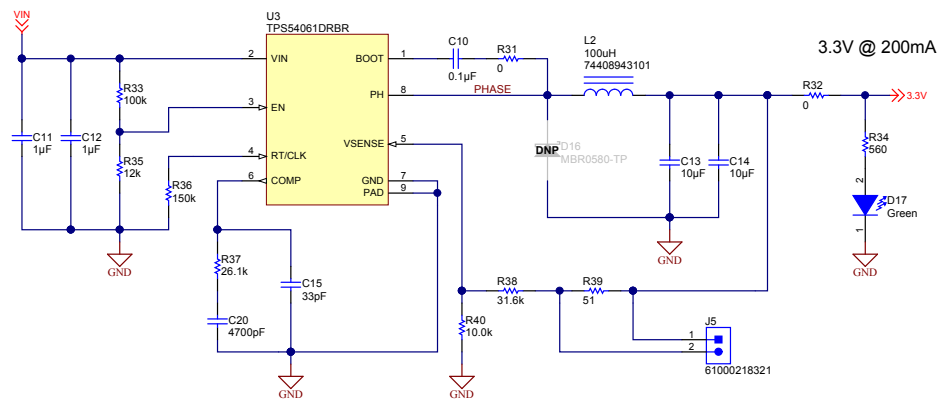


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TID #: TIDA-00179	Project Title: Universal Digital Interface to Absolute Encoders	
Number: TIDA-00179	Rev: E1	Sheet Title:
SVN Rev: Version control disabled	Assembly Variant: Production	Sheet: 2 of 3
Drawn By:	File: RS485_SchDoc	Size: B
Engineer: V.Pizzolante	Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>	<a href="http://www.ti.com">http://www.ti.com</a>

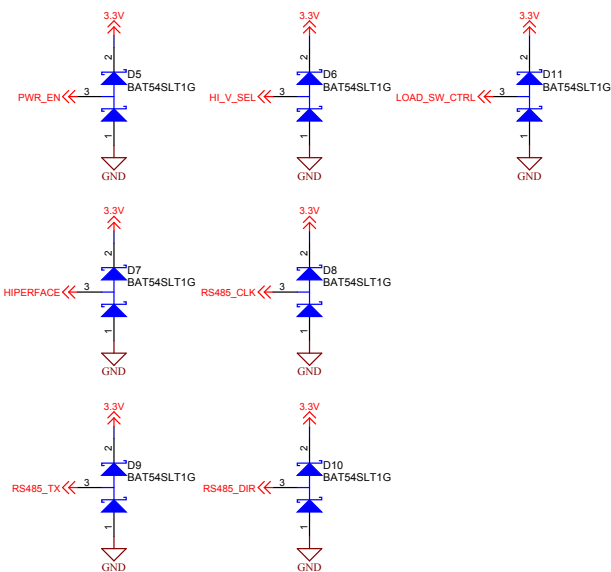
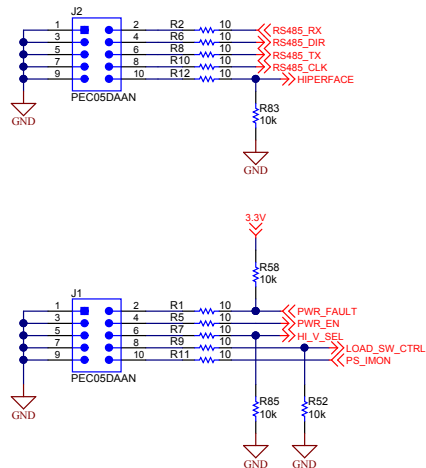




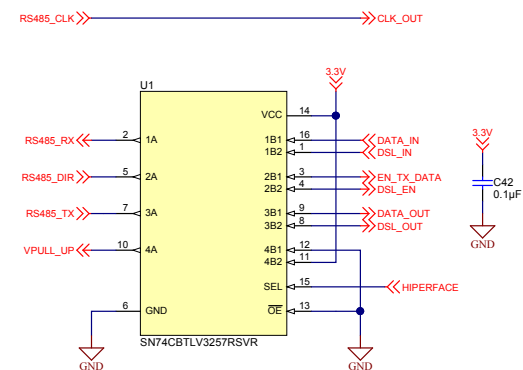


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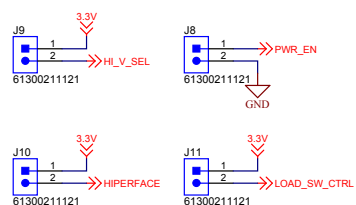
## Host Processor Interface Connectors



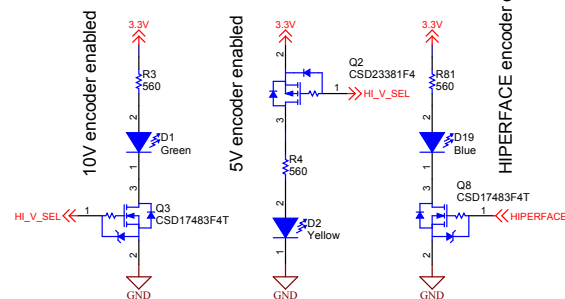
## Input ESD protection



## Configuration Settings



## LED status indicators



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TID #: TIDA-00179	Project Title: Universal Digital Interface to Absolute Encoders	
Number: TIDA-00179   Rev: E1	Sheet Title:	Sheet: 2 of 3
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Drawn By:	File: Host Interface_MUX_SchDoc	Size: B
Engineer: V.Pizzolante	Contact: http://www.ti.com/support	

