

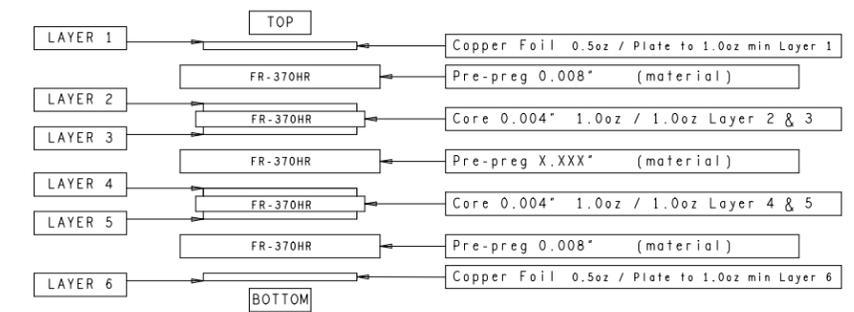
REVISIONS			
ZONE	LTR	DESCRIPTION	DATE

UNLESS OTHERWISE SPECIFIED, ALL NOTES ARE APPLICABLE.
 NOTES PRECEDED BY AN UNMARKED "□" ARE NOT APPLICABLE.

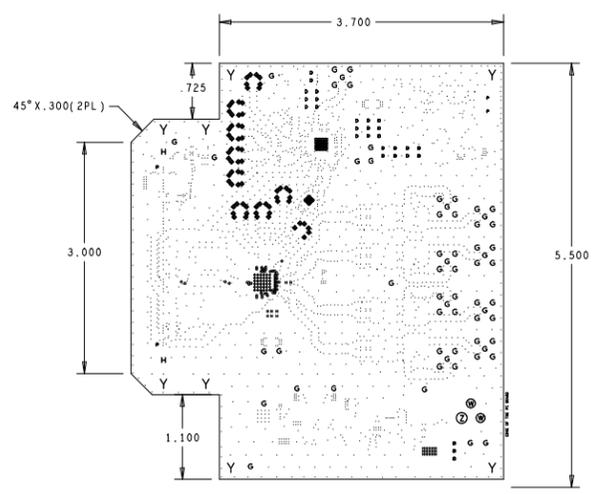
- APPLICATION DESIGN MANUFACTURING AND INSPECTION DOCUMENTS.
 IPC-2221A & IPC-2222 / DESIGN STANDARD FOR RIGID PRINTED CIRCUIT BOARDS AND RIGID PRINTED BOARD ASSEMBLIES.
 IPC-6012B / QUALIFICATION AND PERFORMANCE SPECIFICATION FOR RIGID PRINTED BOARD.
 IPC-A-600G / ACCEPTABILITY OF PRINTED BOARDS.
- HOLE SIZE APPLY AFTER PLATING. TOLERANCE TO BE +/- .003.
- REGISTRATION TOLERANCE: ARTWORK +/- .002
 ALL HOLE CENTERS +/- .005 FROM DIMENSION DATUM.
- MINIMUM COPPER WALL THICKNESS SHALL BE .001 INCH.
 FOR ALL PLATED THROUGH HOLES. BREAKOUT NOT ALLOWED.
- PROCESS AND MATERIAL MUST CONFORM TO UL 796. MATERIAL MUST MEET OR EXCEED UL FLAMMABILITY RATING 94V-0.
 MATERIAL: □ SINGLE SIDED, □ DOUBLE SIDED, MULTI-LAYER (SEE DETAIL "A")
 SEE LAYER STACKUP FOR ALL PRE-PREG & CORE THICKNESSES, COPPER OZ AND MATERIAL. FINISHED BOARD THICKNESS: .062 +/- 10%
- MANUFACTURE'S UL MARKING, FLAMMABILITY RATING, LOGO AND DATE CODE TO BE PLACED IN SILKSCREEN ON BOTTOM SIDE OF THE BOARD.
- SOLDERMASK BOTH SIDES USING TAIYO (OR EQUIVALENT).
 COLOR-RED (0.001 TO 0.002" THICK OVER METAL).
- SILKSCREEN □ TOP SIDE, BOTH SIDES, USING □ YELLOW, WHITE NPI LEADFREE.
 REGISTRATION TOLERANCE TO BE +/- .005. INK IS NOT ALLOWED ON EXPOSED PLATED AREA.
- P.C. BOARD TO BE FREE OF DIRT, OIL, FINGER PRINTS, ETC.
- BOARD WARPAGE: WARP AND TWIST SHALL NOT EXCEED .007 INCH PER INCH.
 MEASURED AT ANY LOCATION OR DIRECTION ON THE BOARD.
- BOARD MUST BE 100% ELECTRICALLY TESTED TO ENSURE NO SHORTS OR OPEN CIRCUITS AT 20V.
- ALL INNER LAYER UNCONNECTED PADS SHALL BE REMOVED.

- ALL OUTER LAYERS USING A 12.0 mil TRACE WIDTH SHALL BE 50ohms SINGLE ENDED, +/- 10% TOLERANCE.
 ALL OUTER LAYERS USING A _____ mil TRACE WIDTH SHALL BE 100ohms SINGLE ENDED, +/- 10% TOLERANCE.
 ALL OUTER LAYERS USING A 6.0 mil TRACE WIDTH AND 5.0 mil SPACING SHALL BE 100 ohms DIFFERENTIAL, +/- 10% TOLERANCE.
 ALL INNER LAYERS USING A _____ mil TRACE WIDTH AND _____ mil SPACING SHALL BE 100 ohms DIFFERENTIAL, +/- 10% TOLERANCE.
- MINIMUM COPPER CONDUCTOR WIDTH IS: 5MIL.
 MINIMUM COPPER SPACING IS: 4.5MIL.
- SMOBC/IMMERSION GOLD: 3-8 uIN OVER 100-200 uIN NICKEL PLATING.
- GROUND ETCH ON ALL LAYERS TO BOARD EDGE IS INTENTIONAL. DO NOT PULL BACK.
- NO CHANGES TO ANY ARTWORK ARE PERMITTED WITHOUT WRITTEN AUTHORIZATION.
- HOLES SIZE APPLIES AFTER PLATING. TOLERANCE TO BE +/- .003/- .010.
- FILL ALL VIAS WITH NON CONDUCTIVE MATERIAL. PLUGGED VIAS TO BE PLATED AND POLISHED FLAT.
- FINISHED BOARDS MUST BE RoHS COMPLIANT AND SURVIVE A LEAD FREE ASSEMBLY, MAXIMUM REFLOW TEMPERATURE OF 260 DEGREE C (6 PASSES).

DETAIL "A"



DRILL CHART: TOP to BOTTOM			
ALL UNITS ARE IN MILS			
FIGURE	SIZE	PLATED	QTY
•	6.0	PLATED	52
•	8.0	PLATED	36
•	10.0	PLATED	2173
•	12.0	PLATED	23
•	12.0	PLATED	16
•	13.0	PLATED	9
•	15.0	PLATED	70
•	38.0	PLATED	28
o	59.0	PLATED	1
o	62.0	PLATED	24
o	67.0	PLATED	36
H	68.0	PLATED	2
⊙	120.0	PLATED	2
⊙	140.0	PLATED	1
•	35.0	NON-PLATED	2
•	40.0	NON-PLATED	2
Y	125.0	NON-PLATED	8



UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ARE: FRACTIONS DECIMALS ANGLES +/- .XX +/- .01 +/- +/- .XXX +/- .005 +/-	CONTRACT NO.		TEXAS INSTRUMENTS INC.	
	APPROVALS	DATE	FABRICATION DRAWING DAC348X EVM	
	DRAWN L. NGUYEN	08/06/14		
MATERIAL	ENGR K. HSIA	SIZE CODE IDENT NO. DRAWING NO. REV. B H		
FINISH	SEE NOTES 7, 8, 9	SCALE NONE SHEET 1 OF 1		
DO NOT SCALE DRAWING				