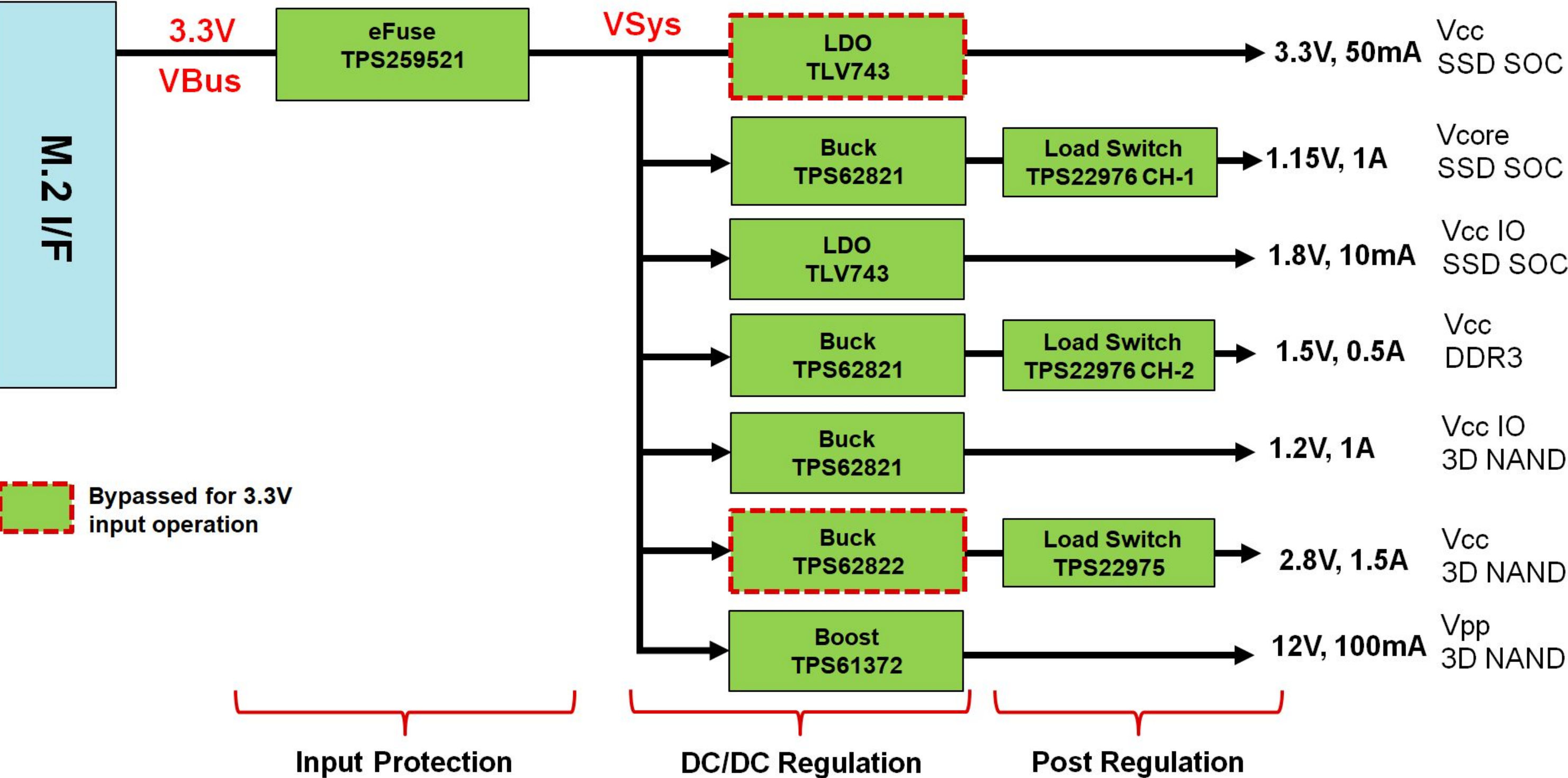
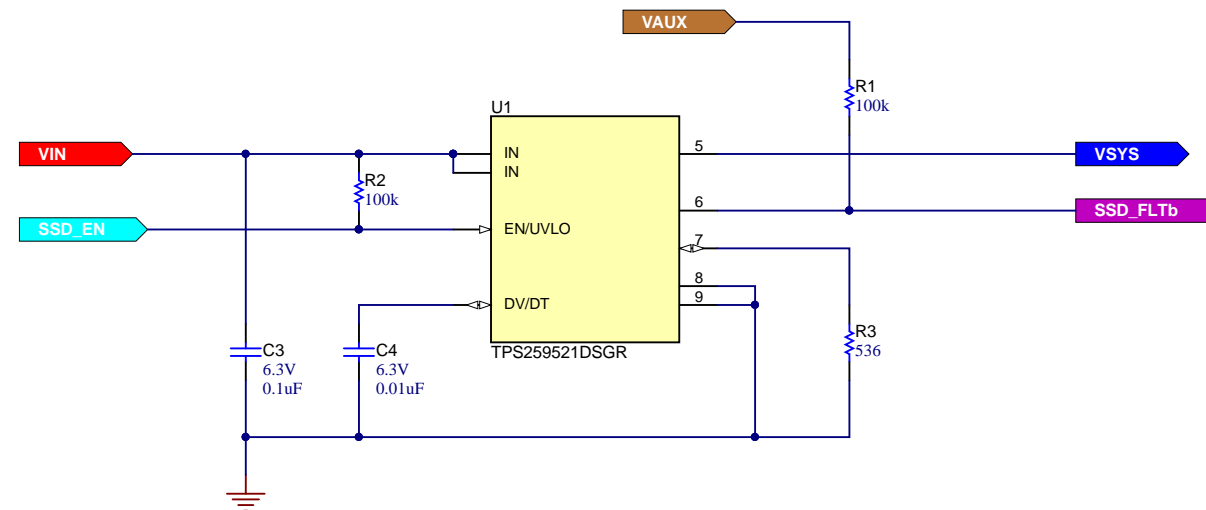
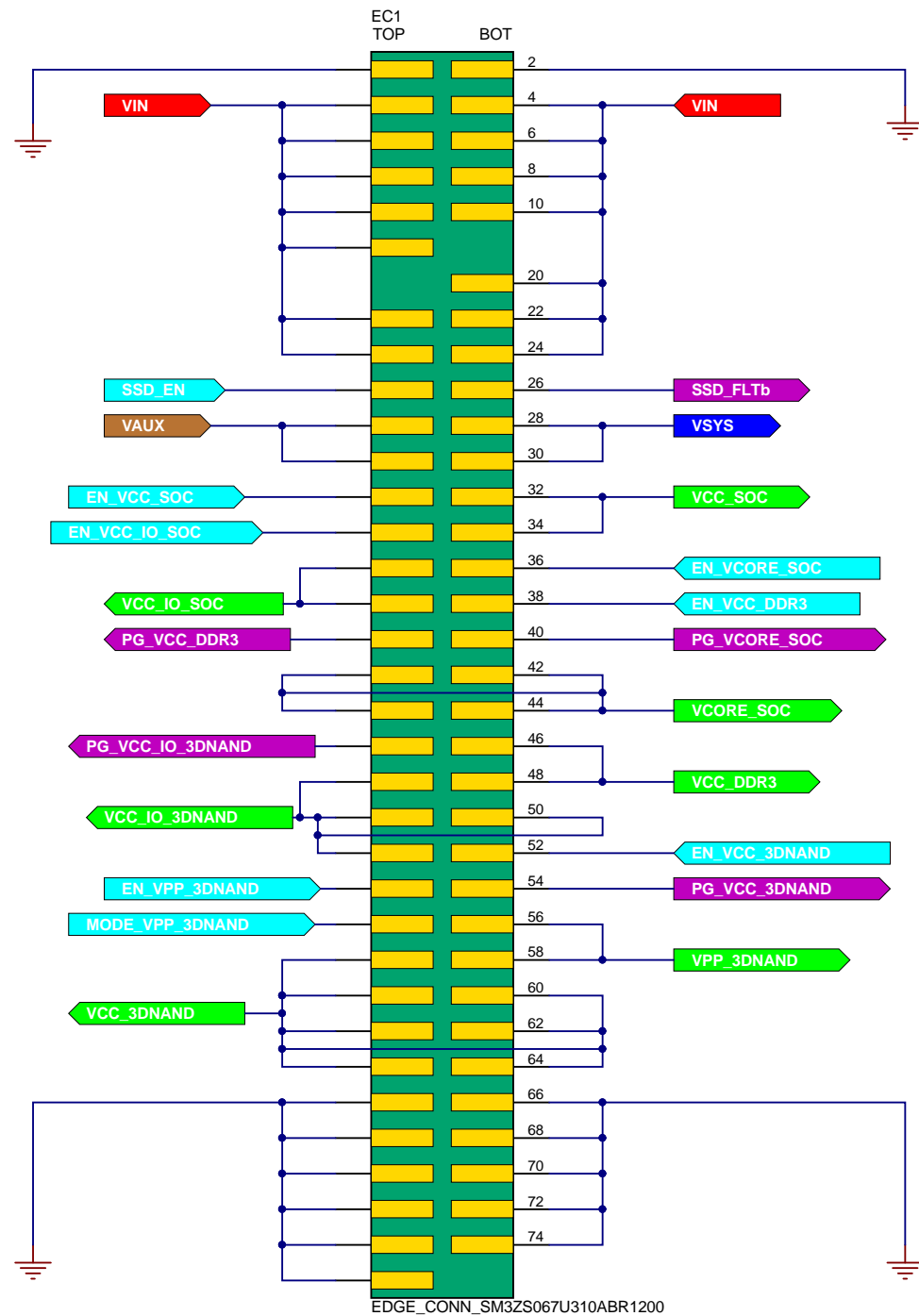



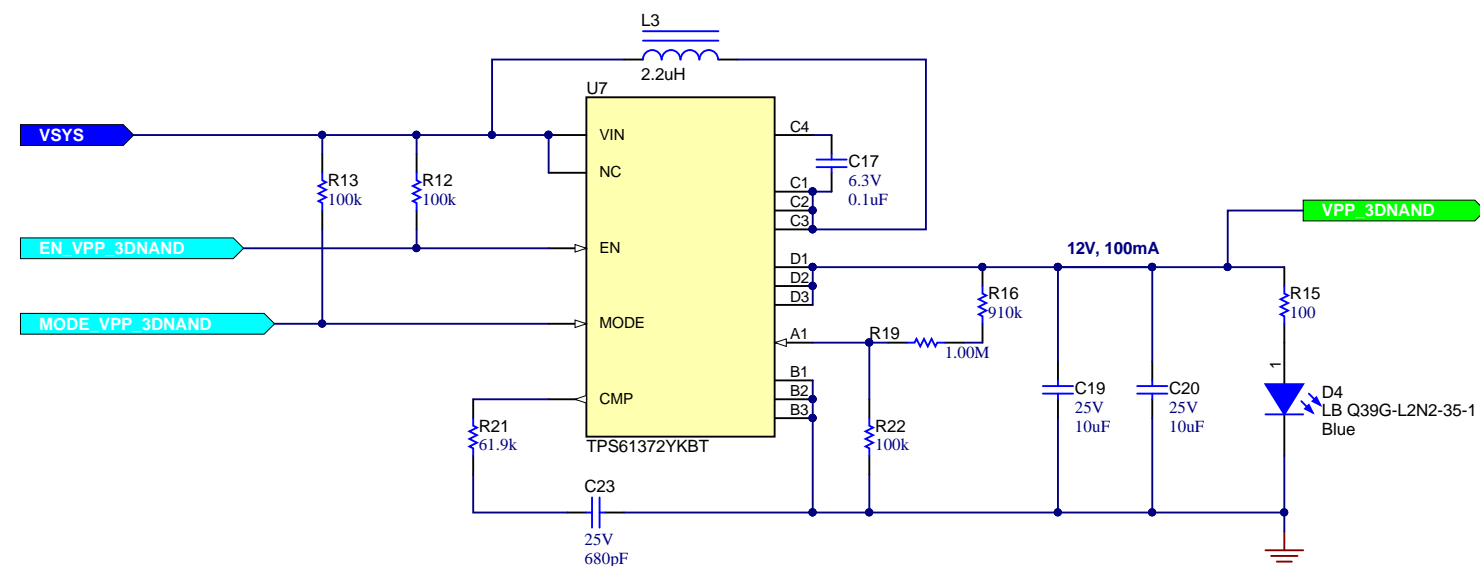
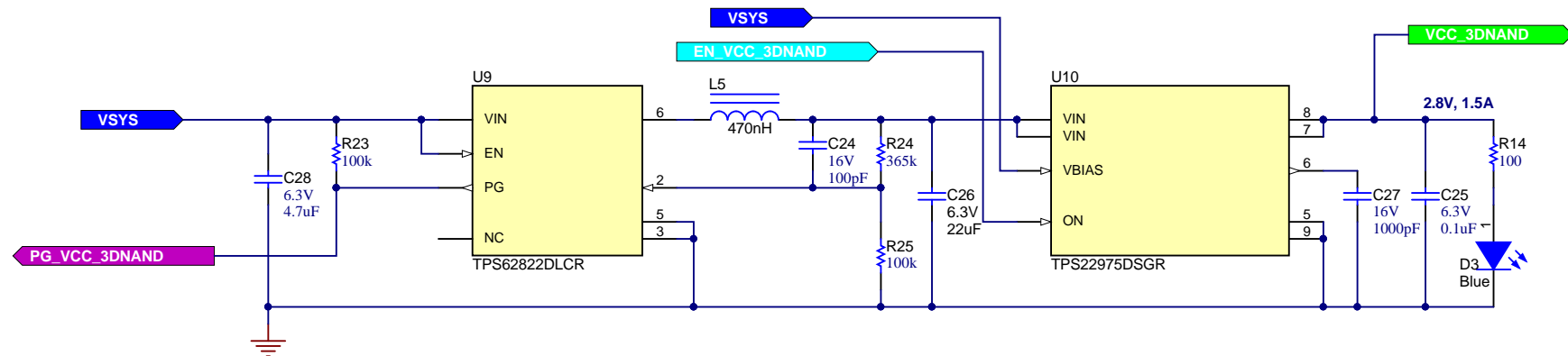
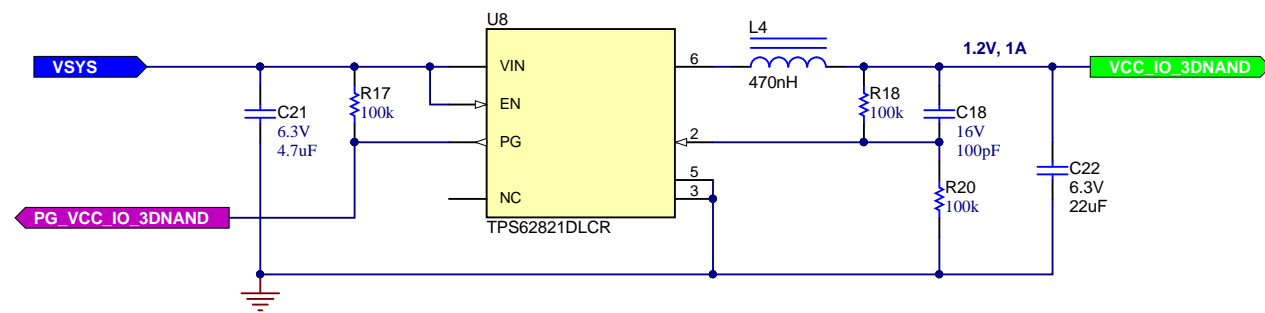
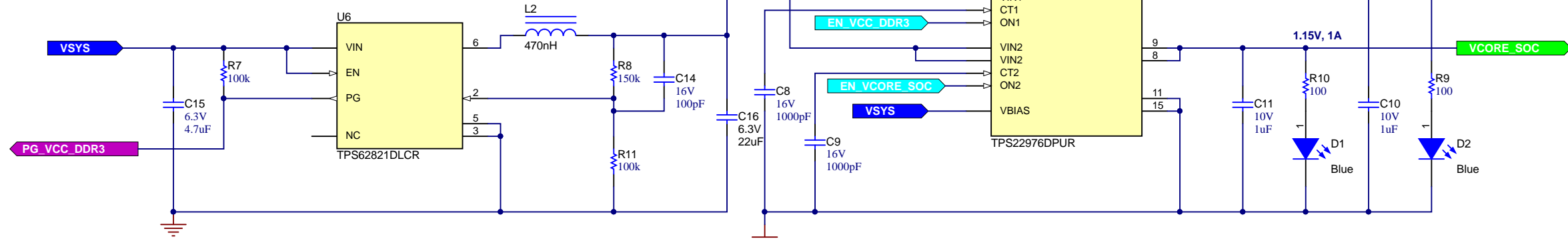
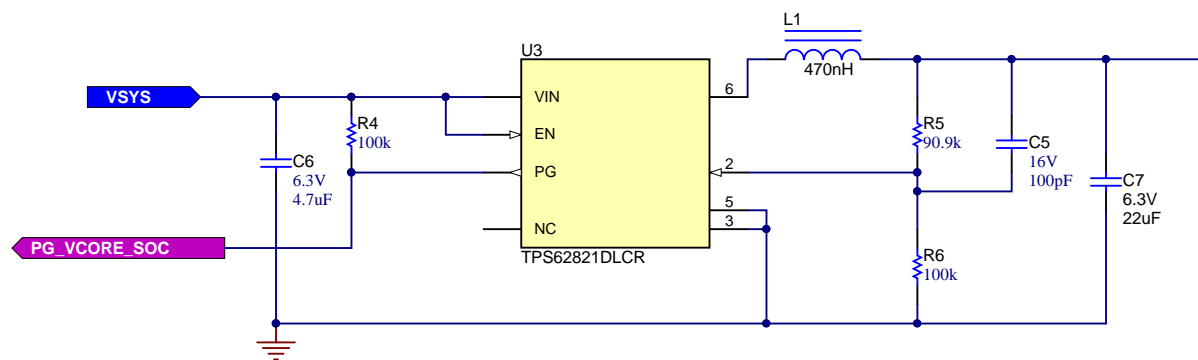
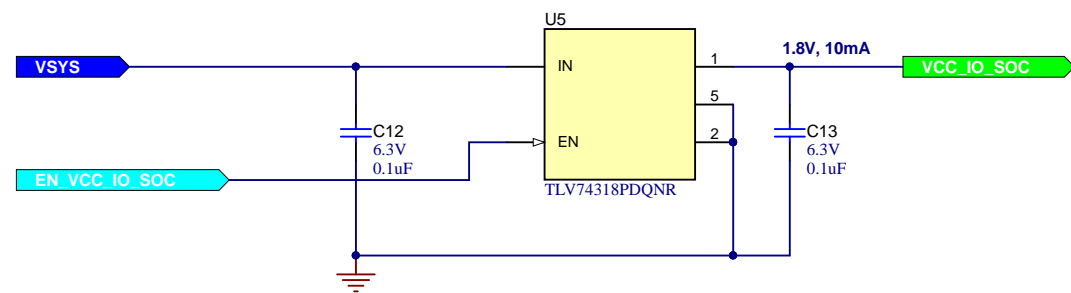
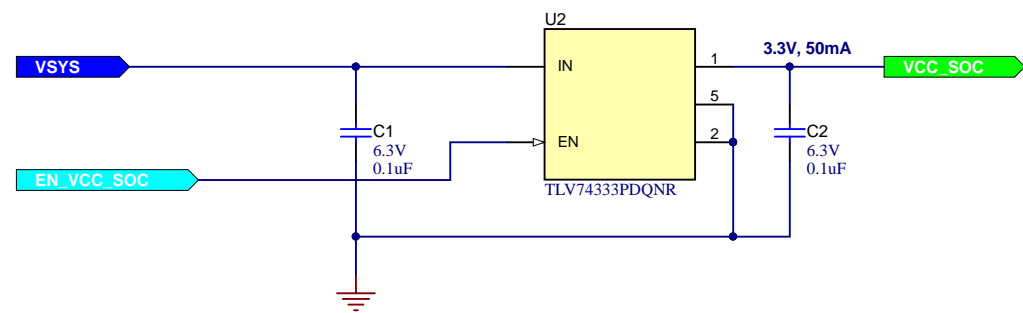
TIDA Block Diagram





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Orderable: EVM_orderable	Designed for: Public Release	Mod. Date: 4/24/2019	 TEXAS INSTRUMENTS http://www.ti.com © Texas Instruments 2017
TID #: TIDA-050028	Project Title: VBus Protection Reference Design with eFuse for S		
Number: TIDA-050028	Rev: E1	Sheet Title:	
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 2 of 4	
Drawn By:	File: Daughter Card_Sheet1.SchDoc	Size: B	
Engineer: Rakesh Panguloori	Contact: http://www.ti.com/support		





PCB Number: TIDA-050028
PCB Rev: E2

ZZ1
Label Assembly Note
This Assembly Note is for PCB labels only

ZZ2
Assembly Note
These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3
Assembly Note
These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4
Assembly Note
These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.