

Linux Board Port Overview for Sitara AM-Class Devices: AM33x, AM43x, and AM57x

Linux Board Port Process Resources



Section overview: Linux board port process resources

- What kernel documentation is available
- PinMux Tool
- TI Linux Kernel User Guide
- TI Linux Software Developer's Guide



¹Linux board port process resources: Kernel documentation

- What kernel documentation is available?
 - https://www.kernel.org/doc/html/latest/

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The Linux Kernel documentation	
This is the top level of the kernel's documentation tree. Kernel documentation, like the kernel itself, is very much a work in progress; we work to integrate our many scattered documents into a coherent whole. Please note that improvements to the documentation a doc list at vger.kernel.org if you want to help out.	; that is especially true as re welcome; join the linux
Licensing documentation	
The following describes the license of the Linux kernel source code (GPLv2), how to properly mark the license of individual files in the links to the full license text.	ne source tree, as well as
Linux kernel licensing rules	
 License identifier syntax License identifiers 	
User-oriented documentation	
The following manuals are written for $users$ of the kernel – those who are trying to get it to work optimally on a given system.	
The Linux kernel user's and administrator's guide	

- Location of DTS bindings in the kernel source tree: documentation/devicetree/bindings



Linux board port process resources: PinMux Tool

PinMux Tool

 Download the tool <u>http://www.ti.com/tool/pinmuxtool</u> or run it from TI Cloud Tools

TI Home > Semiconductors > Processors > Pin Mux Tool							* Worldwide (In English)
Pin Mux Tool							
(ACTIVE) PINMUXTOOL							
Description & Features	Technical Do	uments		💶 Suppo	ort & Trair	ning	ेू Order Now
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Part Number	Buy from Texas Instruments or Third Party	Status	Current Version	Version Date	Host	OS	Description
PINMUXTOOL_DESKTOP_PREVIOUS: Archived version of desktop Pin Mux tool supporting AM35x, AM/DM37x, DM816x. V4 is recommended for new Sitara designs	Free View	ACTIVE	v2 and v3	previous		Windows, Linux	Standalone desktop versions of the tool. Device and OS support vary by version
PINMUXTOOL-V4-CLOUD: PinMux tool for Simplelink, Sitara, C28, Tiva, and IWR/AWR mmWave sensors	Free	ACTIVE	v4	JAN-15- 2018	Windows, Linux, OS X		Browser-based tool access via TI Cloud Tools portal. Automatic solving, high-level requirements entry for configuring device mux settings

 PinMux Tool Wiki page <u>http://processors.wiki.ti.com/index.php/TI_PinMux_Tool</u>

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Linux board port process resources: User guides

• Linux Software Developer's Guide: Linux Kernel

http://processors.wiki.ti.com/index.php/Processor_SDK_Linux_Kernel



• TI Processors Linux SDK Kernel User's Guide

http://processors.wiki.ti.com/index.php/Linux_Kernel_Users_Guide



Conclusions: Linux board port overview

- Abstractions speed up and simplify ports.
- The elements of the Linux Board Port show the recommended steps.
- There are a variety of reference resources available.
- Most importantly, be prepared that the Linux board port is an iterative process.



For more information

- Processor SDK Training Series: <u>https://training.ti.com/processor-sdk-training-series</u>
- For questions about this training, refer to the E2E Community Forums for Sitara Processors at http://e2e.ti.com/support/arm/sitara_arm/f/791/t/277411







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