

TAPE AND REEL INFORMATION



*All dimensions are nominal

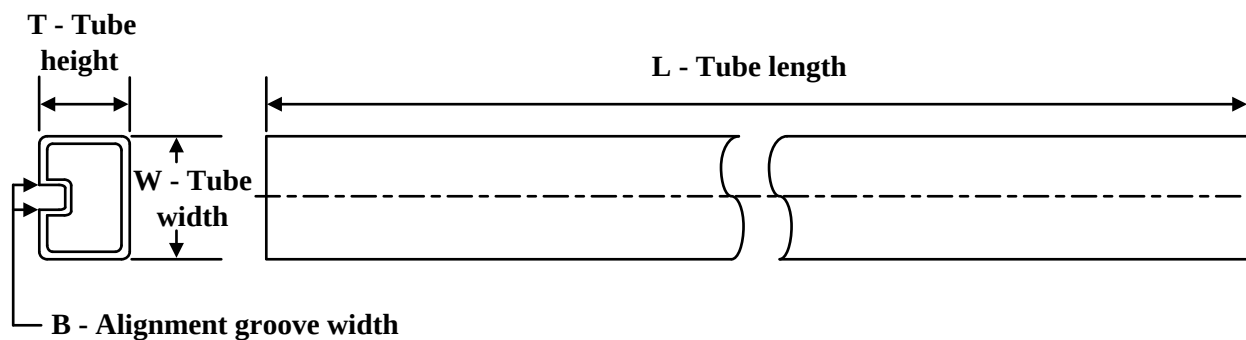
Device	Package Type	Package Drawing	Pins	SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
TPS76701QDR	SOIC	D	8	2500	330.0	12.4	6.4	5.2	2.1	8.0	12.0	Q1
TPS76701QPWPR	HTSSOP	PWP	20	2000	330.0	16.4	6.95	7.1	1.6	8.0	16.0	Q1
TPS76715QDR	SOIC	D	8	2500	330.0	12.4	6.4	5.2	2.1	8.0	12.0	Q1
TPS76718QDR	SOIC	D	8	2500	330.0	12.4	6.4	5.2	2.1	8.0	12.0	Q1
TPS76718QPWPR	HTSSOP	PWP	20	2000	330.0	16.4	6.95	7.1	1.6	8.0	16.0	Q1
TPS76725QPWPR	HTSSOP	PWP	20	2000	330.0	16.4	6.95	7.1	1.6	8.0	16.0	Q1
TPS76730QPWPR	HTSSOP	PWP	20	2000	330.0	16.4	6.95	7.1	1.6	8.0	16.0	Q1
TPS76733QDR	SOIC	D	8	2500	330.0	12.4	6.4	5.2	2.1	8.0	12.0	Q1
TPS76733QPWPR	HTSSOP	PWP	20	2000	330.0	16.4	6.95	7.1	1.6	8.0	16.0	Q1
TPS76750QDR	SOIC	D	8	2500	330.0	12.4	6.4	5.2	2.1	8.0	12.0	Q1
TPS76750QPWPR	HTSSOP	PWP	20	2000	330.0	16.4	6.95	7.1	1.6	8.0	16.0	Q1

TAPE AND REEL BOX DIMENSIONS



*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
TPS76701QDR	SOIC	D	8	2500	350.0	350.0	43.0
TPS76701QPWPR	HTSSOP	PWP	20	2000	350.0	350.0	43.0
TPS76715QDR	SOIC	D	8	2500	350.0	350.0	43.0
TPS76718QDR	SOIC	D	8	2500	350.0	350.0	43.0
TPS76718QPWPR	HTSSOP	PWP	20	2000	350.0	350.0	43.0
TPS76725QPWPR	HTSSOP	PWP	20	2000	350.0	350.0	43.0
TPS76730QPWPR	HTSSOP	PWP	20	2000	350.0	350.0	43.0
TPS76733QDR	SOIC	D	8	2500	350.0	350.0	43.0
TPS76733QPWPR	HTSSOP	PWP	20	2000	350.0	350.0	43.0
TPS76750QDR	SOIC	D	8	2500	350.0	350.0	43.0
TPS76750QPWPR	HTSSOP	PWP	20	2000	350.0	350.0	43.0

TUBE


*All dimensions are nominal

Device	Package Name	Package Type	Pins	SPQ	L (mm)	W (mm)	T (μm)	B (mm)
TPS76701QD	D	SOIC	8	75	505.46	6.76	3810	4
TPS76701QD.A	D	SOIC	8	75	505.46	6.76	3810	4
TPS76701QPWP	PWP	HTSSOP	20	70	530	10.2	3600	3.5
TPS76701QPWP.A	PWP	HTSSOP	20	70	530	10.2	3600	3.5
TPS76701QPWPG4	PWP	HTSSOP	20	70	530	10.2	3600	3.5
TPS76715QD	D	SOIC	8	75	505.46	6.76	3810	4
TPS76715QD.A	D	SOIC	8	75	505.46	6.76	3810	4
TPS76715QPWP	PWP	HTSSOP	20	70	530	10.2	3600	3.5
TPS76715QPWP.A	PWP	HTSSOP	20	70	530	10.2	3600	3.5
TPS76718QD	D	SOIC	8	75	505.46	6.76	3810	4
TPS76718QD.A	D	SOIC	8	75	505.46	6.76	3810	4
TPS76718QPWP	PWP	HTSSOP	20	70	530	10.2	3600	3.5
TPS76718QPWP.A	PWP	HTSSOP	20	70	530	10.2	3600	3.5
TPS76725QD	D	SOIC	8	75	505.46	6.76	3810	4
TPS76725QD.A	D	SOIC	8	75	505.46	6.76	3810	4
TPS76725QPWP	PWP	HTSSOP	20	70	530	10.2	3600	3.5
TPS76725QPWP.A	PWP	HTSSOP	20	70	530	10.2	3600	3.5
TPS76725QPWPG4	PWP	HTSSOP	20	70	530	10.2	3600	3.5
TPS76727QD	D	SOIC	8	75	505.46	6.76	3810	4
TPS76727QD.A	D	SOIC	8	75	505.46	6.76	3810	4
TPS76727QPWP	PWP	HTSSOP	20	70	530	10.2	3600	3.5
TPS76727QPWP.A	PWP	HTSSOP	20	70	530	10.2	3600	3.5
TPS76728QD	D	SOIC	8	75	505.46	6.76	3810	4
TPS76728QD.A	D	SOIC	8	75	505.46	6.76	3810	4
TPS76730QD	D	SOIC	8	75	505.46	6.76	3810	4
TPS76730QD.A	D	SOIC	8	75	505.46	6.76	3810	4
TPS76730QPWP	PWP	HTSSOP	20	70	530	10.2	3600	3.5
TPS76730QPWP.A	PWP	HTSSOP	20	70	530	10.2	3600	3.5
TPS76730QPWPG4	PWP	HTSSOP	20	70	530	10.2	3600	3.5

Device	Package Name	Package Type	Pins	SPQ	L (mm)	W (mm)	T (μm)	B (mm)
TPS76733QD	D	SOIC	8	75	505.46	6.76	3810	4
TPS76733QD.A	D	SOIC	8	75	505.46	6.76	3810	4
TPS76733QPWP	PWP	HTSSOP	20	70	530	10.2	3600	3.5
TPS76733QPWP.A	PWP	HTSSOP	20	70	530	10.2	3600	3.5
TPS76733QPWPG4	PWP	HTSSOP	20	70	530	10.2	3600	3.5
TPS76750QD	D	SOIC	8	75	505.46	6.76	3810	4
TPS76750QD.A	D	SOIC	8	75	505.46	6.76	3810	4
TPS76750QPWP	PWP	HTSSOP	20	70	530	10.2	3600	3.5
TPS76750QPWP.A	PWP	HTSSOP	20	70	530	10.2	3600	3.5