

TAPE AND REEL INFORMATION

QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE

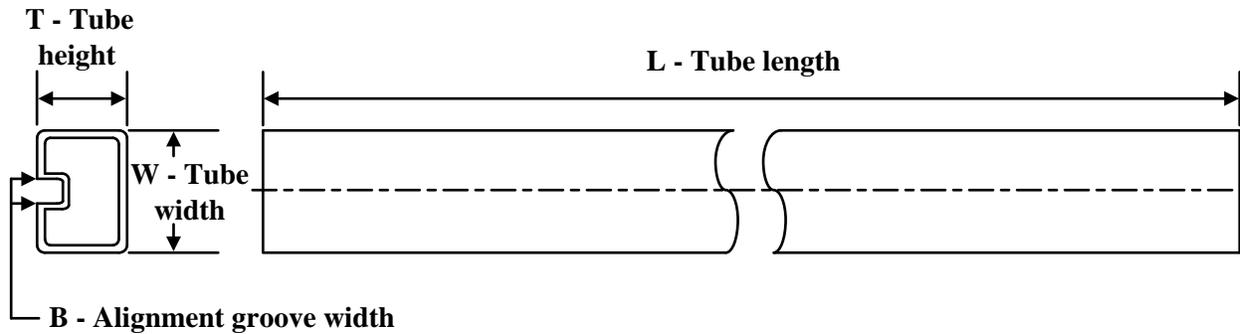

*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
SN65MLVD200ADR	SOIC	D	8	2500	330.0	12.4	6.4	5.2	2.1	8.0	12.0	Q1
SN65MLVD202ADR	SOIC	D	14	2500	330.0	16.4	6.5	9.0	2.1	8.0	16.0	Q1
SN65MLVD204ADR	SOIC	D	8	2500	330.0	12.4	6.4	5.2	2.1	8.0	12.0	Q1
SN65MLVD204ADR1G4	SOIC	D	8	2500	330.0	12.4	6.4	5.2	2.1	8.0	12.0	Q1
SN65MLVD205ADR	SOIC	D	14	2500	330.0	16.4	6.5	9.0	2.1	8.0	16.0	Q1

TAPE AND REEL BOX DIMENSIONS


*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
SN65MLVD200ADR	SOIC	D	8	2500	353.0	353.0	32.0
SN65MLVD202ADR	SOIC	D	14	2500	353.0	353.0	32.0
SN65MLVD204ADR	SOIC	D	8	2500	353.0	353.0	32.0
SN65MLVD204ADR1G4	SOIC	D	8	2500	353.0	353.0	32.0
SN65MLVD205ADR	SOIC	D	14	2500	353.0	353.0	32.0

TUBE


*All dimensions are nominal

Device	Package Name	Package Type	Pins	SPQ	L (mm)	W (mm)	T (μm)	B (mm)
SN65MLVD200AD	D	SOIC	8	75	507	8	3940	4.32
SN65MLVD200AD.B	D	SOIC	8	75	507	8	3940	4.32
SN65MLVD200ADG4	D	SOIC	8	75	507	8	3940	4.32
SN65MLVD202AD	D	SOIC	14	50	507	8	3940	4.32
SN65MLVD202AD.B	D	SOIC	14	50	507	8	3940	4.32
SN65MLVD202ADG4	D	SOIC	14	50	507	8	3940	4.32
SN65MLVD202ADG4.B	D	SOIC	14	50	507	8	3940	4.32
SN65MLVD204AD	D	SOIC	8	75	507	8	3940	4.32
SN65MLVD204AD.B	D	SOIC	8	75	507	8	3940	4.32
SN65MLVD204ADG4	D	SOIC	8	75	507	8	3940	4.32
SN65MLVD205AD	D	SOIC	14	50	507	8	3940	4.32
SN65MLVD205AD.B	D	SOIC	14	50	507	8	3940	4.32
SN65MLVD205ADG4	D	SOIC	14	50	507	8	3940	4.32