

TAPE AND REEL INFORMATION

QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE


*All dimensions are nominal

| Device | Package Type | Package Drawing | Pins | SPQ | Reel Diameter (mm) | Reel Width W1 (mm) | A0 (mm) | B0 (mm) | K0 (mm) | P1 (mm) | W (mm) | Pin1 Quadrant |
|----------------|--------------|-----------------|------|------|--------------------|--------------------|---------|---------|---------|---------|--------|---------------|
| ISO6740FQDWRQ1 | SOIC | DW | 16 | 2000 | 330.0 | 16.4 | 10.75 | 10.7 | 2.7 | 12.0 | 16.0 | Q1 |
| ISO6740FQDWRQ1 | SOIC | DW | 16 | 2000 | 330.0 | 16.4 | 10.75 | 10.7 | 2.7 | 12.0 | 16.0 | Q1 |
| ISO6740QDWRQ1 | SOIC | DW | 16 | 2000 | 330.0 | 16.4 | 10.75 | 10.7 | 2.7 | 12.0 | 16.0 | Q1 |
| ISO6740QDWRQ1 | SOIC | DW | 16 | 2000 | 330.0 | 16.4 | 10.75 | 10.7 | 2.7 | 12.0 | 16.0 | Q1 |
| ISO6740QDWRQ1 | SOIC | DW | 16 | 2000 | 330.0 | 16.4 | 10.75 | 10.7 | 2.7 | 12.0 | 16.0 | Q1 |
| ISO6741FQDWRQ1 | SOIC | DW | 16 | 2000 | 330.0 | 16.4 | 10.75 | 10.7 | 2.7 | 12.0 | 16.0 | Q1 |
| ISO6741FQDWRQ1 | SOIC | DW | 16 | 2000 | 330.0 | 16.4 | 10.75 | 10.7 | 2.7 | 12.0 | 16.0 | Q1 |
| ISO6741QDWRQ1 | SOIC | DW | 16 | 2000 | 330.0 | 16.4 | 10.75 | 10.7 | 2.7 | 12.0 | 16.0 | Q1 |
| ISO6741QDWRQ1 | SOIC | DW | 16 | 2000 | 330.0 | 16.4 | 10.75 | 10.7 | 2.7 | 12.0 | 16.0 | Q1 |
| ISO6742FQDWRQ1 | SOIC | DW | 16 | 2000 | 330.0 | 16.4 | 10.75 | 10.7 | 2.7 | 12.0 | 16.0 | Q1 |
| ISO6742FQDWRQ1 | SOIC | DW | 16 | 2000 | 330.0 | 16.4 | 10.75 | 10.7 | 2.7 | 12.0 | 16.0 | Q1 |
| ISO6742QDWRQ1 | SOIC | DW | 16 | 2000 | 330.0 | 16.4 | 10.75 | 10.7 | 2.7 | 12.0 | 16.0 | Q1 |
| ISO6742QDWRQ1 | SOIC | DW | 16 | 2000 | 330.0 | 16.4 | 10.75 | 10.7 | 2.7 | 12.0 | 16.0 | Q1 |

TAPE AND REEL BOX DIMENSIONS


*All dimensions are nominal

| Device | Package Type | Package Drawing | Pins | SPQ | Length (mm) | Width (mm) | Height (mm) |
|----------------|--------------|-----------------|------|------|-------------|------------|-------------|
| ISO6740FQDWRQ1 | SOIC | DW | 16 | 2000 | 356.0 | 356.0 | 35.0 |
| ISO6740FQDWRQ1 | SOIC | DW | 16 | 2000 | 356.0 | 356.0 | 35.0 |
| ISO6740QDWRQ1 | SOIC | DW | 16 | 2000 | 356.0 | 356.0 | 35.0 |
| ISO6740QDWRQ1 | SOIC | DW | 16 | 2000 | 353.0 | 353.0 | 32.0 |
| ISO6740QDWRQ1 | SOIC | DW | 16 | 2000 | 356.0 | 356.0 | 35.0 |
| ISO6741FQDWRQ1 | SOIC | DW | 16 | 2000 | 353.0 | 353.0 | 32.0 |
| ISO6741FQDWRQ1 | SOIC | DW | 16 | 2000 | 353.0 | 353.0 | 32.0 |
| ISO6741QDWRQ1 | SOIC | DW | 16 | 2000 | 353.0 | 353.0 | 32.0 |
| ISO6741QDWRQ1 | SOIC | DW | 16 | 2000 | 353.0 | 353.0 | 32.0 |
| ISO6742FQDWRQ1 | SOIC | DW | 16 | 2000 | 353.0 | 353.0 | 32.0 |
| ISO6742FQDWRQ1 | SOIC | DW | 16 | 2000 | 353.0 | 353.0 | 32.0 |
| ISO6742QDWRQ1 | SOIC | DW | 16 | 2000 | 353.0 | 353.0 | 32.0 |
| ISO6742QDWRQ1 | SOIC | DW | 16 | 2000 | 353.0 | 353.0 | 32.0 |