

Revision History

Rev	ECN #	Approved Date	Approved by	Notes
N/A	N/A	N/A	N/A	N/A

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BLOCK DIAGRAM

Page 3

MICROCONTROLLER


Page 4

BINARY INPUT

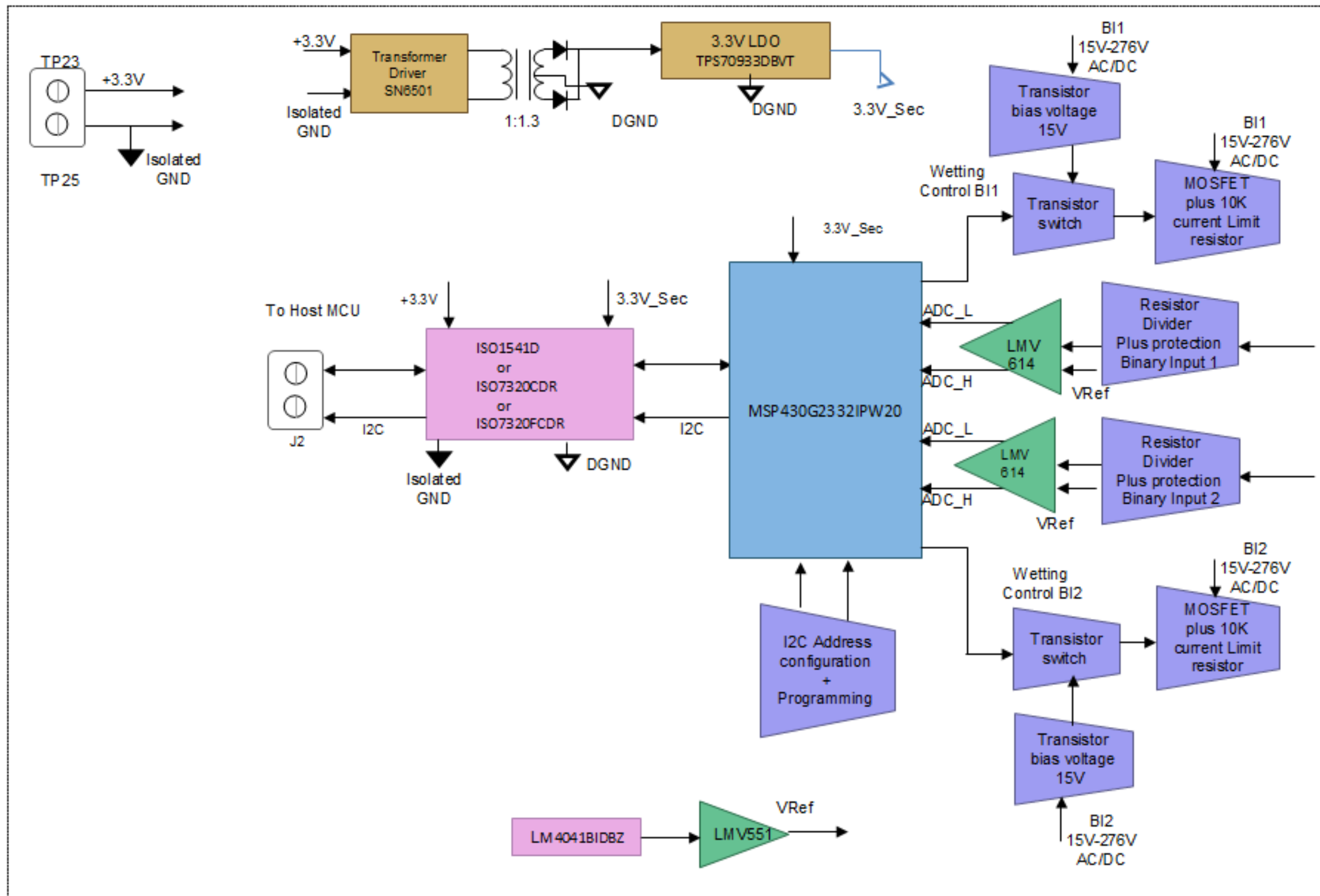
Page 5

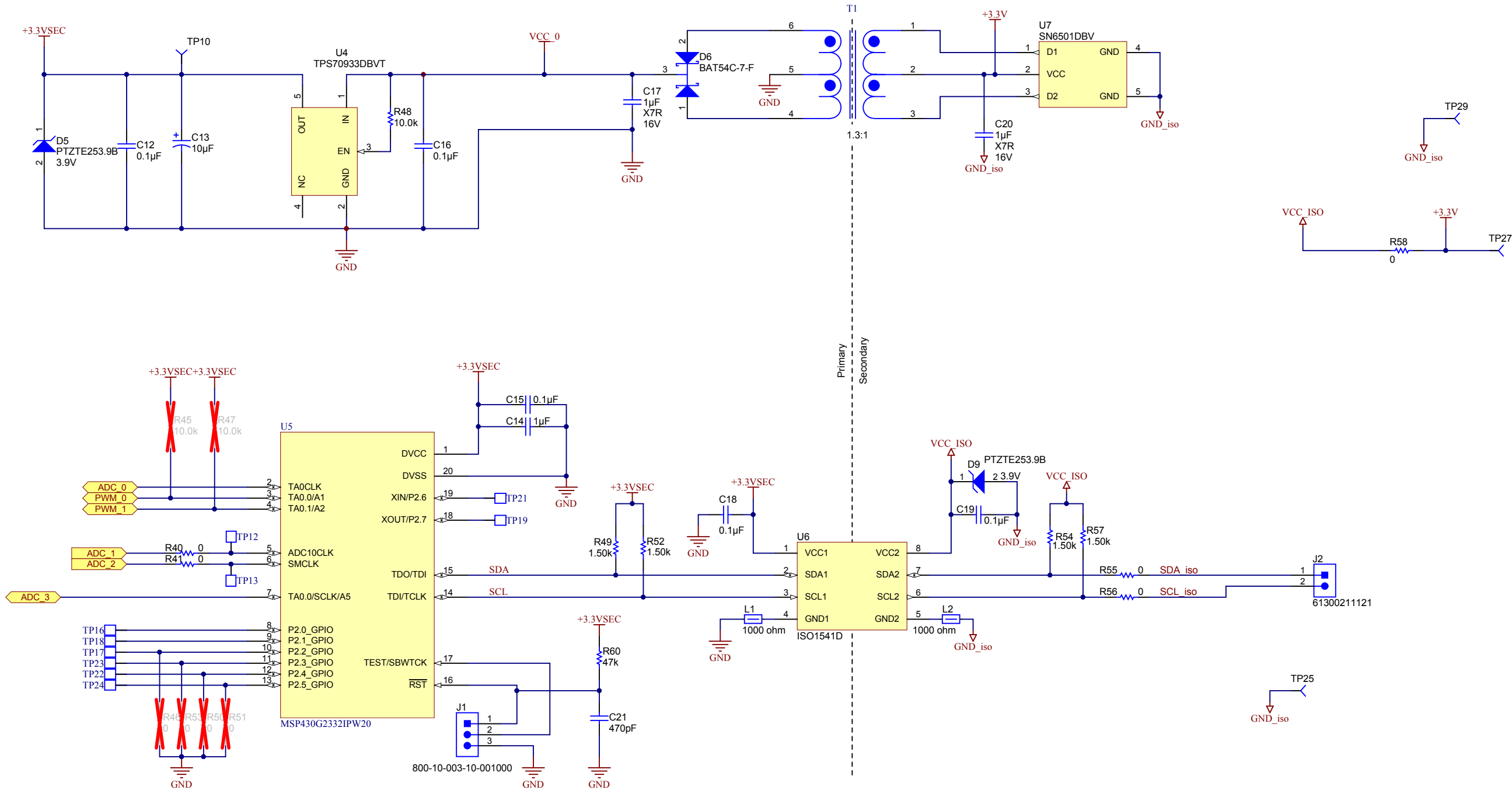
HARDWARE D25-IO_ANSI-B

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Orderable: N/A	Designed for: Public Release	Mod. Date: 5/30/2016	 TEXAS INSTRUMENTS http://www.ti.com © Texas Instruments 2016
TID #: 00809-BI	Project Title: AC/DC BINARY INPUT		
Number: TIDA-00809-BI Rev: E1	Sheet Title: Block Diagram		
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 1 of 5	
Drawn By: Sreenivas	File: Pg 1 Cover Sheet ANSI-B.SchDoc	Size: B	
Engineer: Sreenivas	Contact: http://www.ti.com/support		

AC/DC binary input – TIDA-00809_BI_E1

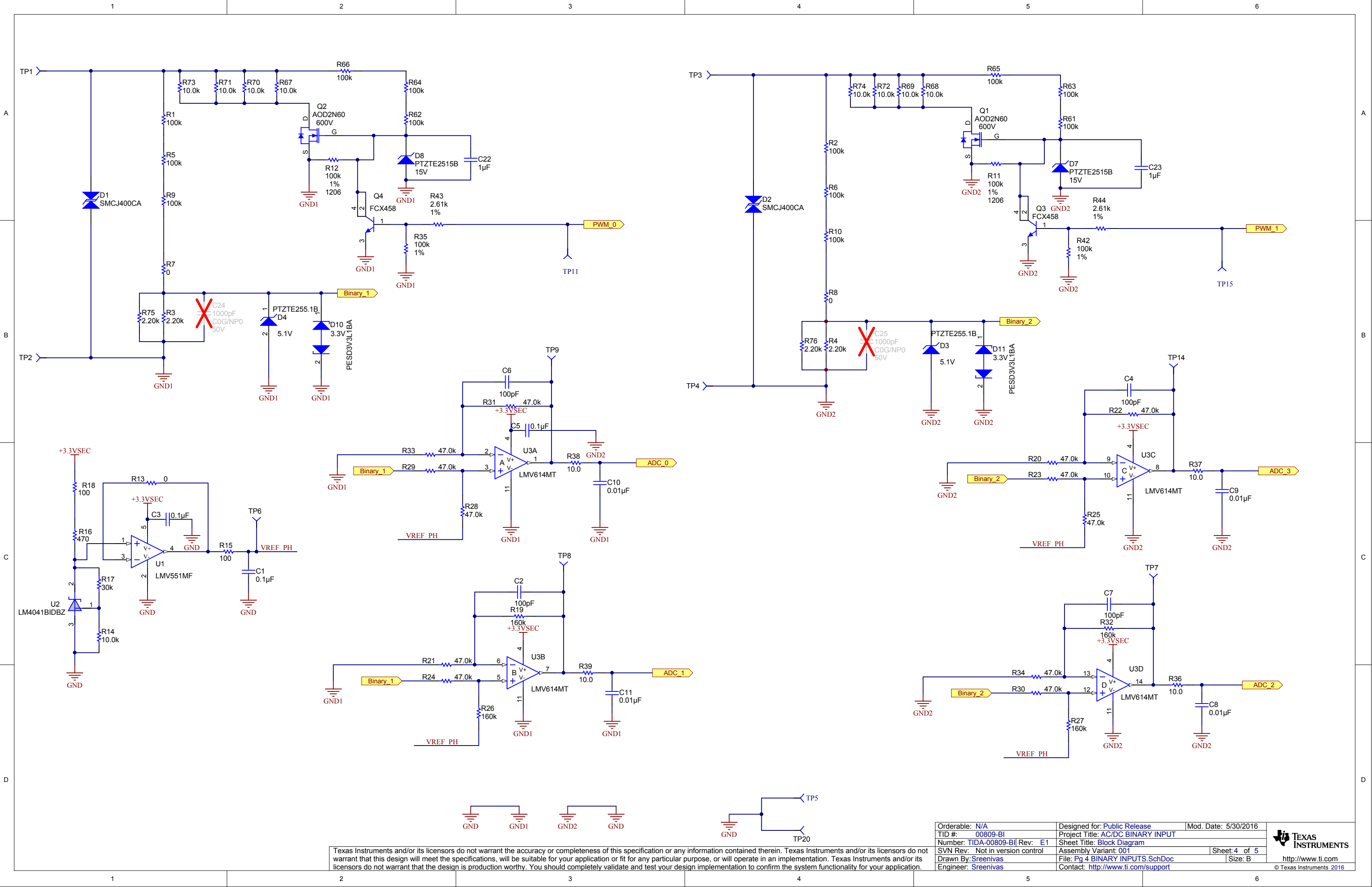




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Number: TIDA-00809-BI Rev: E1	Sheet Title: Block Diagram	
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 3 of 5
Drawn By: Sreenivas	File: Pg3 MICROCONTROLLER.SchDoc	Size: B
Engineer: Sreenivas	Contact: http://www.ti.com/support	© Texas Instruments 2016





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Drawn By: Sreenivas	File: Pg 4 BINARY INPUTS SchDoc	Size: B
Engineer: Sreenivas	Contact: http://www.ti.com/support	



Label Table	
Variant	Label Text
001	ChangeMe!
002	ChangeMe!

PCB Number: TIDA-00809-BI
PCB Rev: E1

PCB
LOGO
Pb_Free

PCB
LOGO
Pb-Free Symbol

LBL1
PCB Label
Size: 0.65" x 0.20 "

ZZ1
Label Assembly Note
This Assembly Note is for PCB labels only

ZZ2
Assembly Note
These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3
Assembly Note
These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4
Assembly Note
These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

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