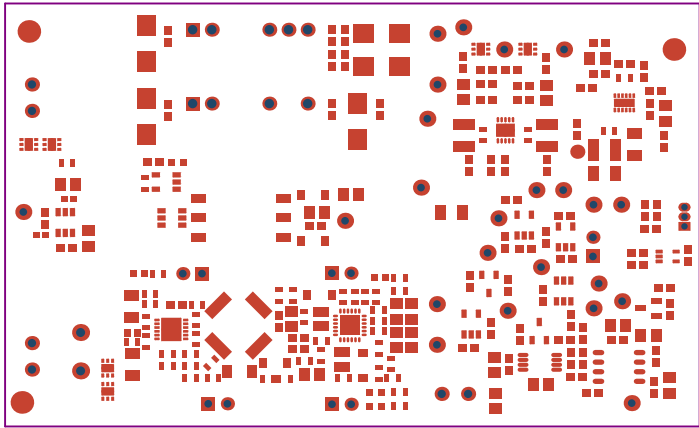
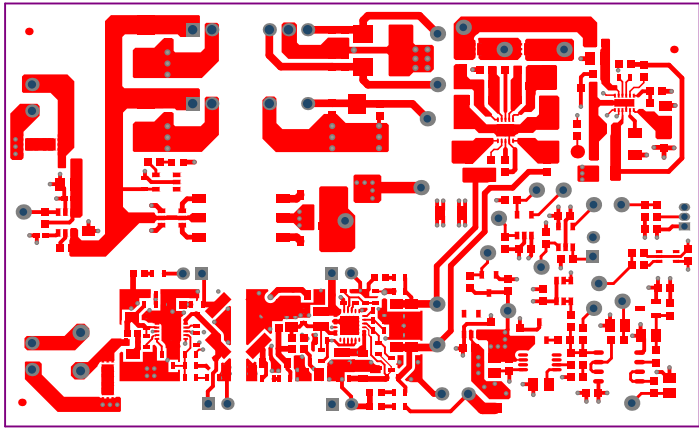


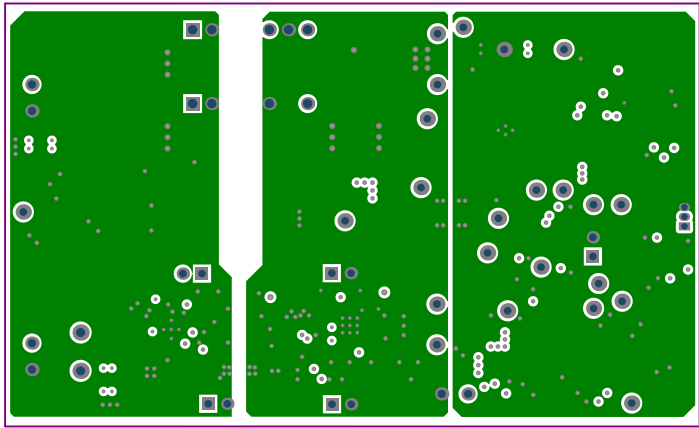
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: TIDA-00300_150AFE-Power Protection	REV: E1	SUN REV: Not In VersionControl
LAYER NAME = Top Overlay	TID #: 00300		
PLOT NAME = Top Overlay	GENERATED : 4/29/2019 4:38:17 PM	TEXAS INSTRUMENTS	



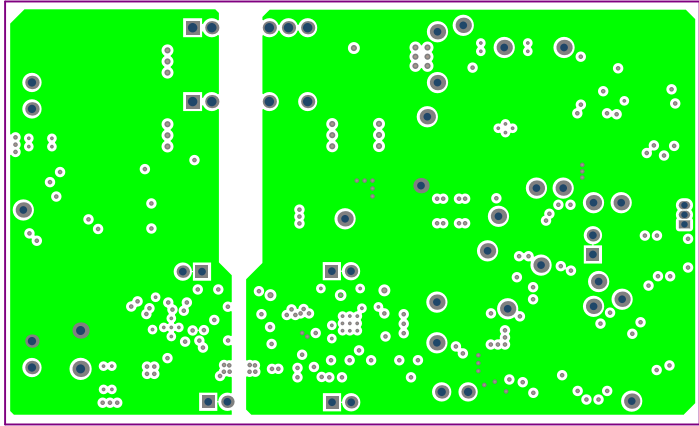
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	TIDA-00300_150_nFE-Power Protection	REV: E1	SUN REV: Not In VersionControl
LAYER NAME = Top Solder	TID #:	00300		
PLOT NAME = Top Solder Mask	GENERATED	: 4/29/2019	4:38:17 PM	TEXAS INSTRUMENTS



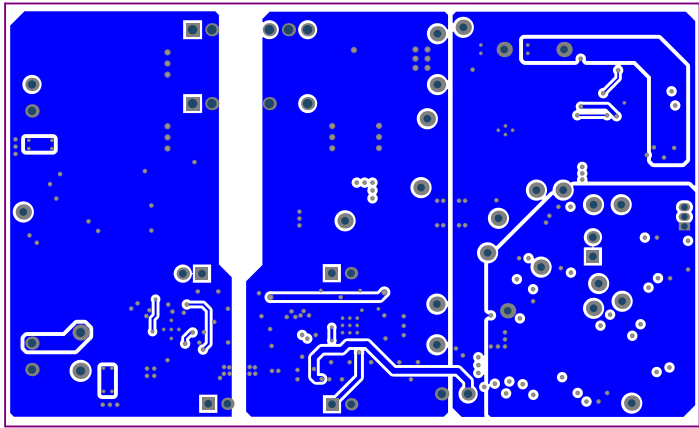
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	TIDA-00300_150_nFE-Power Protection	REV: E1	SUN REV: Not In VersionControl
LAYER NAME = Top Layer	TID #:	00300		
PLOT NAME = Top Layer	GENERATED	: 4/29/2019	4:38:17 PM	TEXAS INSTRUMENTS



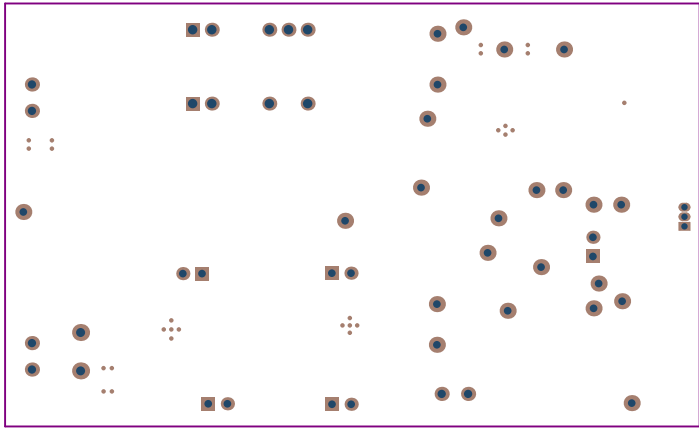
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	TIDA-00300_150_nFE-Power Protection	REV: E1	SUN REV: Not In VersionControl
LAYER NAME =	GND Layer	TID #:	00300	
PLOT NAME =	Gnd Layer	GENERATED	: 4/29/2019 4:38:17 PM	TEXAS INSTRUMENTS



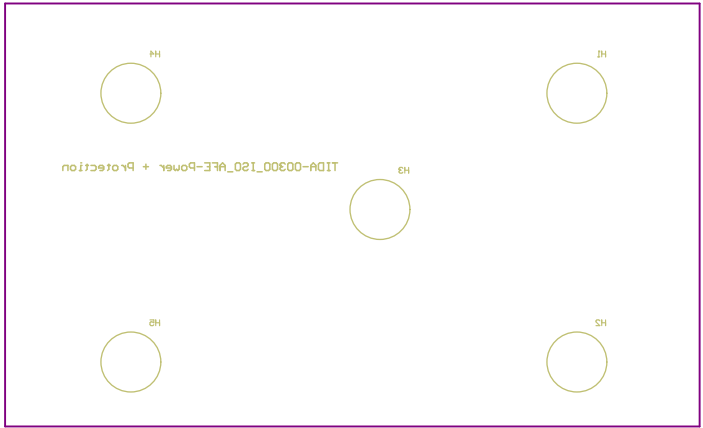
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	TIDA-00300_150_nFE-Power Protection	REV: E1	SUN REV: Not In VersionControl
LAYER NAME =	PWR Layer			
TID #:	00300			
PLOT NAME =	PWR Layer		GENERATED : 4/29/2019 4:38:18 PM	TEXAS INSTRUMENTS



ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	TIDA-00300_150_nFE-Power Protection	REV: E1	SUN REV: Not In VersionControl
LAYER NAME = Bottom Layer	TID #:	00300		
PLOT NAME = Bottom Layer	GENERATED	: 4/29/2019	4:38:18 PM	TEXAS INSTRUMENTS



ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	TIDA-00300_150_AFE-Power Protection	REV: E1	SUN REV: Not In VersionControl
LAYER NAME = Bottom Solder	TID #:	00300		
PLOT NAME = Bottom Solder Mask	GENERATED	: 4/29/2019	4:38:18 PM	TEXAS INSTRUMENTS



ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	TIDA-00300_150_AFE-Power + Protection	REV: E1	SUN REV: Not In VersionControl
LAYER NAME = Bottom Overlay	TID #:	00300		
PLOT NAME = Bottom Overlay	GENERATED	: 4/29/2019	4:38:18 PM	TEXAS INSTRUMENTS

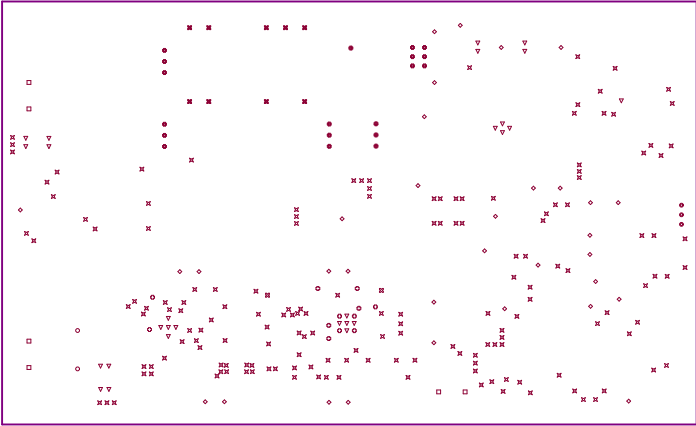


123456

A

Layer	Name	Material	Thickness	Constant	Board Layer Stack
1	Top Overlay				
2	Top Solder	Solder Resist	0.30mil	3.5	
3	Top Layer	Copper	1.40mil		
4	Dielectric1	FR-4 High Tg	15.00mil	4.5	
5	GND Layer	Copper	1.40mil		
6	Dielectric 2	FR-4 High Tg	28.00mil	4.5	
7	PWR Layer	Copper	1.40mil		
8	Dielectric 3	FR-4 High Tg	15.00mil	4.5	
9	Bottom Layer	Copper	1.40mil		
10	Bottom Solder	Solder Resist	0.30mil	3.5	
11	Bottom Overlay				

B



C

Symbol	Count	Hole Size	Plated	Hole Type	Hole Length
▽	27	7.87mil (0.200mm)	PTH	Round	-
⊕	12	8.00mil (0.203mm)	PTH	Round	-
⊗	165	12.00mil (0.305mm)	PTH	Round	-
⊛	19	16.00mil (0.406mm)	PTH	Round	-
⊠	2	20.00mil (0.508mm)	PTH	Round	-
⊙	3	33.47mil (0.850mm)	PTH	Round	-
◇	33	40.00mil (1.016mm)	PTH	Round	-
□	6	43.31mil (1.100mm)	PTH	Round	-
○	2	47.00mil (1.194mm)	PTH	Round	-
⊞	9	49.21mil (1.250mm)	PTH	Round	-
	278 Total				

Drill Table

FOR 7.874MIL DRILL +0/-7.874MIL  
FOR 8MIL DRILL +0/-8MIL  
FOR 10MIL DRILL +0/-10MIL  
FOR 12MIL DRILL +0/-12MIL  
FOR 16MIL DRILL +0/-16MIL  
FOR PTH DRILL +/-3MIL  
FOR NPTH DRILL +/-2MIL

D

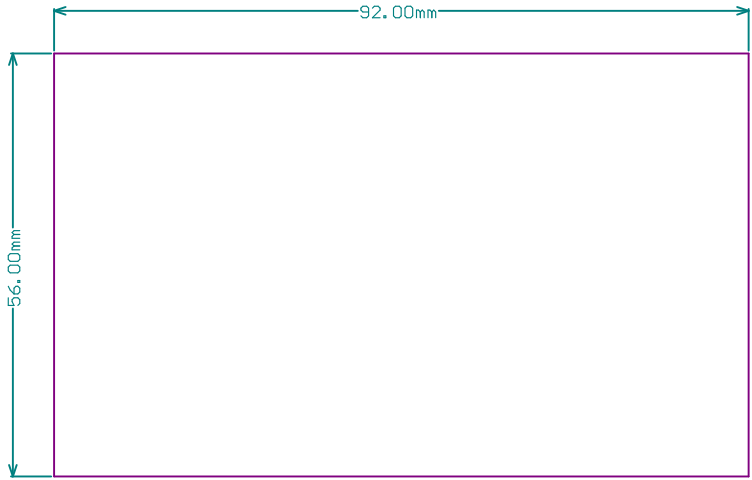
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:TIDA-00300_ISO_AFE-Power+Protection REV: E1	SUN REV: Not In VersionControl	Texas Instruments (TI) and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. TI and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. TI and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.
LAYER NAME = Drill Drawing	TID #: 00300		
PLOT NAME = Drill Drawing	GENERATED : 4/29/2019 4:38:18 PM	TEXAS INSTRUMENTS	

DESIGN INFORMATION

BOARD SIZE (REFER ALSO ARRAY/PANEL PROFILING INFORMATION)  
92MM X 56MM  
Number of Layers : 4  
MIN. TRACK WIDTH: 8 MIL  
MIN. CLEARANCE: 8 MIL  
MIN. VIA DRILL SIZE: 7.874 MIL  
MINIMUM ANNULAR RING 6 MIL ( 0.1524 mm) EXTERNAL  
PER IPC-D-275 CLASS 2 LEVEL C  
REGISTRATION TOLERANCES: METAL +/- 5 MIL, HOLES +/- 3 MIL  
MATERIAL:  
☐ FR-408 ☒ FR-4 High Tg ☐ OTHER  
THICKNESS: ☒ 63 MIL (1.6mm) +/-10% ☐ OTHER  
TOLERANCE: ☒ ANSI IPC-6012 TYPE 3 CLASS 2  
☐ OTHER +/-  
BOW & TWIST: ☒ ANSI IPC-6012 TYPE 3 CLASS 2  
☐ OTHER +/-  
COPPER THICKNESS (FINISHED):  
OUTER: ☒ 1.4MIL (1oz) ☐ 2MIL (1.4oz) ☐ 2.8MIL (2oz)  
INNER SIGNAL: ☒ 1.4MIL (1oz) ☐ 2.8MIL (2oz) ☐ N/A  
DRILLING:  
REFERENCE: ☒ AS SHOWN ☒ NC\_DRILL FILES  
PTH MIN COPPER THICKNESS: ☒ 1MIL ☐ OTHER  
BOARD FINISH:  
SILKSCREEN: ☒ TOP ☒ BOTTOM  
SILKSCREEN COLOR: ☒ WHITE ☐ OTHER  
SOLDER RESIST COLOR:  
☒ GREEN ☐ BLUE ☐ OTHER  
SURFACE FINISH: ☒ IMMERSION GOLD (ENIG) ☐ ENEPIG  
☐ IMM. TIN/SILVER OR EQUIV ☐ OTHER  
ARRAY/PANEL: ☐ CUT AND TRIM PER MECH LAYER 1  
☐ N.C. ROUTE ☒ V. SCORE  
CERTIFICATION: MATERIALS AND WORKMANSHIP FOR ALL PCBs TO MEET OR EXCEED THE REQUIREMENTS OF:  
☒ ANSI IPC-A-600F CLASS -> ☐ 1 ☒ 2 ☐ 3  
☒ UL 94V-0 ☒ RoHS ☐ OTHER PER ORDER  
ADDITIONAL REQUIREMENTS: VIA TENTING: YES ☒ NO ☐  
MICROSECTION: ☐ YES IMPEDANCE CONTROL: YES ☐ NO ☒  
BARE BOARD ELEC. TEST: ☐ NONE ☒ REQUIRED ☐ PER ORDER  
MANUFACTURER'S UL: ☐ RAIL ☐ METAL ☒ SILK

TEXAS INSTRUMENTS

PROJECT TITLE:  
TIDA-00300\_ISO\_AFE-Power+Protection  
DESIGNED FOR:  
Public Release  
FILE NAME:  
TIDA-00300\_ISO\_AFE-Power+Protection.PcbDoc  
ENGINEER:  
Greenivasa  
SCALE: 1.00  
LAYOUT BY:  
Avinash N  
ALTUM DESIGNER VERSION:  
18.1.9.240



ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	TIDA-00300_150_nFE-Power Protection	REV: E1	SUN REV: Not In VersionControl
LAYER NAME =	TID #:	00300		
PLOT NAME = Board Dimensions	GENERATED	: 4/29/2019	4:38:21 PM	TEXAS INSTRUMENTS