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Orderable: LMR66430-2EVM	Designed for: Public Release	Mod. Date: 3/20/2023	 TEXAS INSTRUMENTS
TID #: N/A	Project Title:		
Number: SR019	Rev: A	Sheet Title:	
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 2 of 3	
Drawn By:	File: SR019A.SchDoc	Size: B	
Engineer: Marshall Beck	Contact: http://www.ti.com/support		http://www.ti.com © Texas Instruments 2023

H5

1902C

H6

1902C

H7

1902C

H8

1902C

SH-J1

SNT-100-BK-G

SH-J2

SNT-100-BK-G

SH-J3

SNT-100-BK-G

FID1

FID2

FID3

PCB Number: SR019

PCB Rev: A

PCB
LOGO

Texas Instruments

CE Mark

PCB
LOGO

FCC disclaimer

PCB
LOGO

WEEE logo

CAUTION HOT SURFACE

ZZ1

Assembly Note

These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ2

Assembly Note

These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ3

Assembly Note

These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

ZZ4

Assembly Note

Place SH-J1 across pins 1 and 2 of J1

ZZ5

Assembly Note

Place SH-J2 across pins 1 and 2 of J2.

ZZ6

Assembly Note

Place SH-J3 across pins 1 and 2 of J3.

1

2

3

4

5

6

1

2

3

4

5

6

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