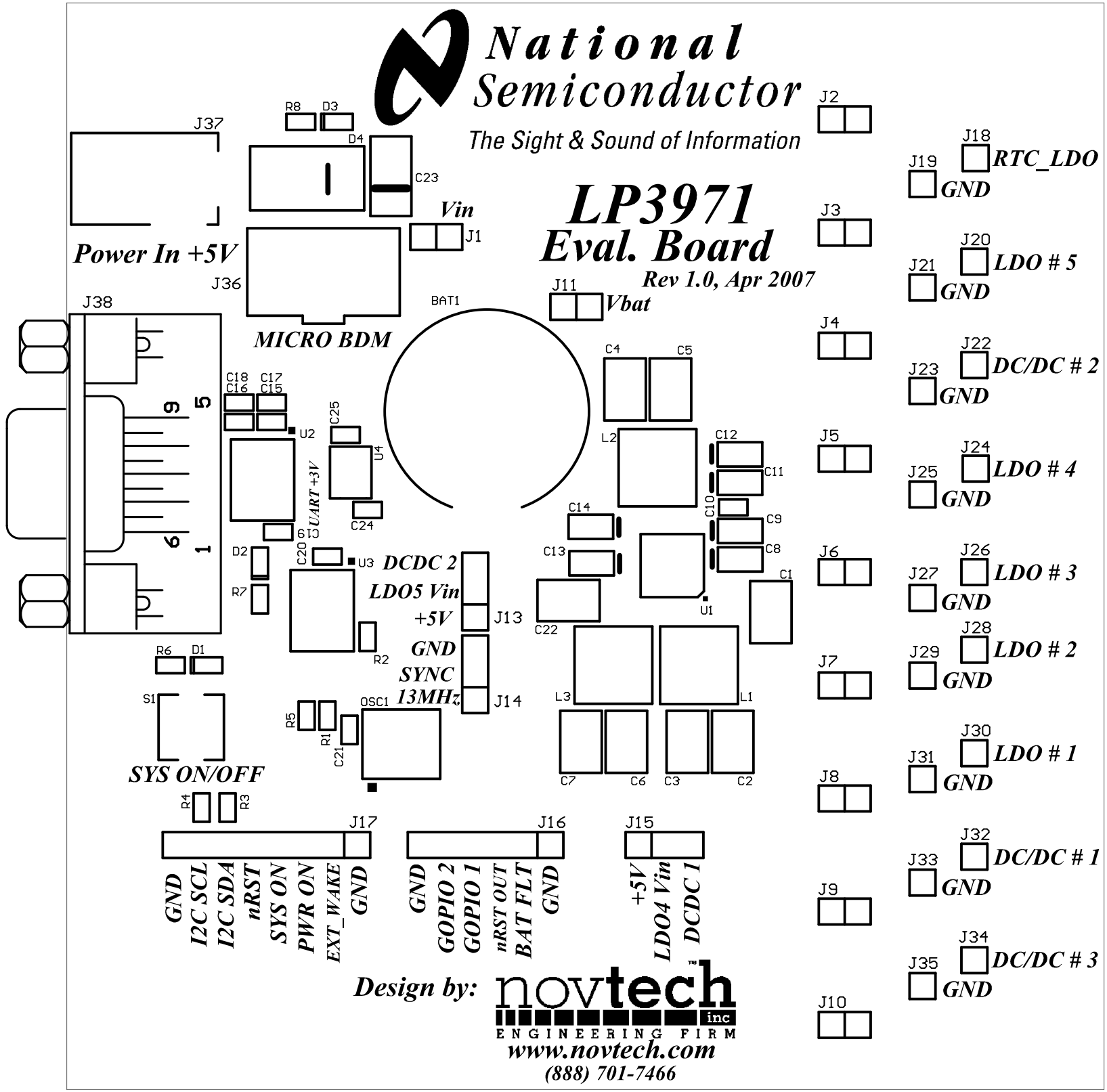
National Semiconductor
The Sight & Sound of Information
LP3971
Eval. Board
Rev 1.0, Apr 2007
Power In +5V
J37
J36
MICRO BDM
J38
Vin
J1
J11
Vbat
BAT1
UART +3V
DCDC 2
LDO5 Vin
+5V
GND
SYNC
13MHz
J13
J14
SYS ON/OFF
S1
OSC1
J17
GND
I2C SCL
I2C SDA
nRST
SYS ON
PWR ON
EXT_WAKE
GND
J16
GND
GOPIO 2
GOPIO 1
nRST OUT
BAT FLT
GND
J15
+5V
LDO4 Vin
DCDC 1
J2
J3
J4
J5
J6
J7
J8
J9
J10
J18
RTC_LDO
J19
GND
J20
LDO # 5
J21
GND
J22
DC/DC # 2
J23
GND
J24
LDO # 4
J25
GND
J26
LDO # 3
J27
GND
J28
LDO # 2
J29
GND
J30
LDO # 1
J31
GND
J32
DC/DC # 1
J33
GND
J34
DC/DC # 3
J35
GND
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