

# TPSM82903 PSPICE Transient Model Features and Limitations

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* Model Usage Notes:
* A. Features have been modelled
*   1. Output Voltage Setting
*   2. Internal Fixed Soft-Start and External Adjustable Soft-start
*   3. Frequency and Operation Mode Selection
*   4. High-side FET Current Limit
*   5. Low-side FET Current Limit
*   6. Low-side FET Sink Current Limit
*   7. Power Good
*   8. Output Voltage Discharge
*   9. UVLO Protection
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* B. Features have not been modelled
*   1. Operating Quiescent Current
*   2. Shutdown Current
*   3. Temperature dependent characteristics
*   4. Ground pins have been tied to 0V internally. Therefore, this model cannot
*       be used for inverting topologies.
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