

Model Usage Notes:

A. Features have been modelled

1. Programmable Soft-Start
2. Programmable VOUT
3. RAMP Selection
4. Oscillator and frequency programmability
5. SYNC IN and SYNC OUT for Master and Slave Configuration
6. Synchronisation and SYNC Point selection
7. MODE pin functionality
8. POWER GOOD and Enable
9. OCP,OVP and UVP Protection
10. High side/Low side Mosfet current Limit
11. HICCUP and Remote sense Functionality
12. Multiphase operation

B. Features have not been modelled

1. Operating Quiescent Current
2. Shutdown Current
3. Temperature dependent characteristics
4. Prebias feature is not modeled
5. Negative current limit is not validated as device hits other faults before reaching negative current limit.
6. Lose of Synchronisation is not modeled.
7. Current sharing is not validated when device SYNC at sync points other than 1/2 of RAMP.
8. Ground Pins have been tied to 0V internally and hence model does not support Inverting topologies.

C. Application Notes

1. The parameter STEADY_STATE has been used to reach the steady state faster.
Keep STEADY_STATE = 0 to observe startup behaviour.
Keep STEADY_STATE = 1 and appropriate IC on Inductor and capacitor to observe for faster Steady state.
2. The parameter IOUT has been used to reach the steady state faster.
Please provide Output load current value for this parameter.
Use this parameter along with STEADY_STATE = 1 for faster Steady state.
3. Please use STEADY_STATE=1 only to observe steady state behaviour for typical conditions.
When STEADY_STATE=1, the internal VREF will be fixed to selected value and remains unchanged.