

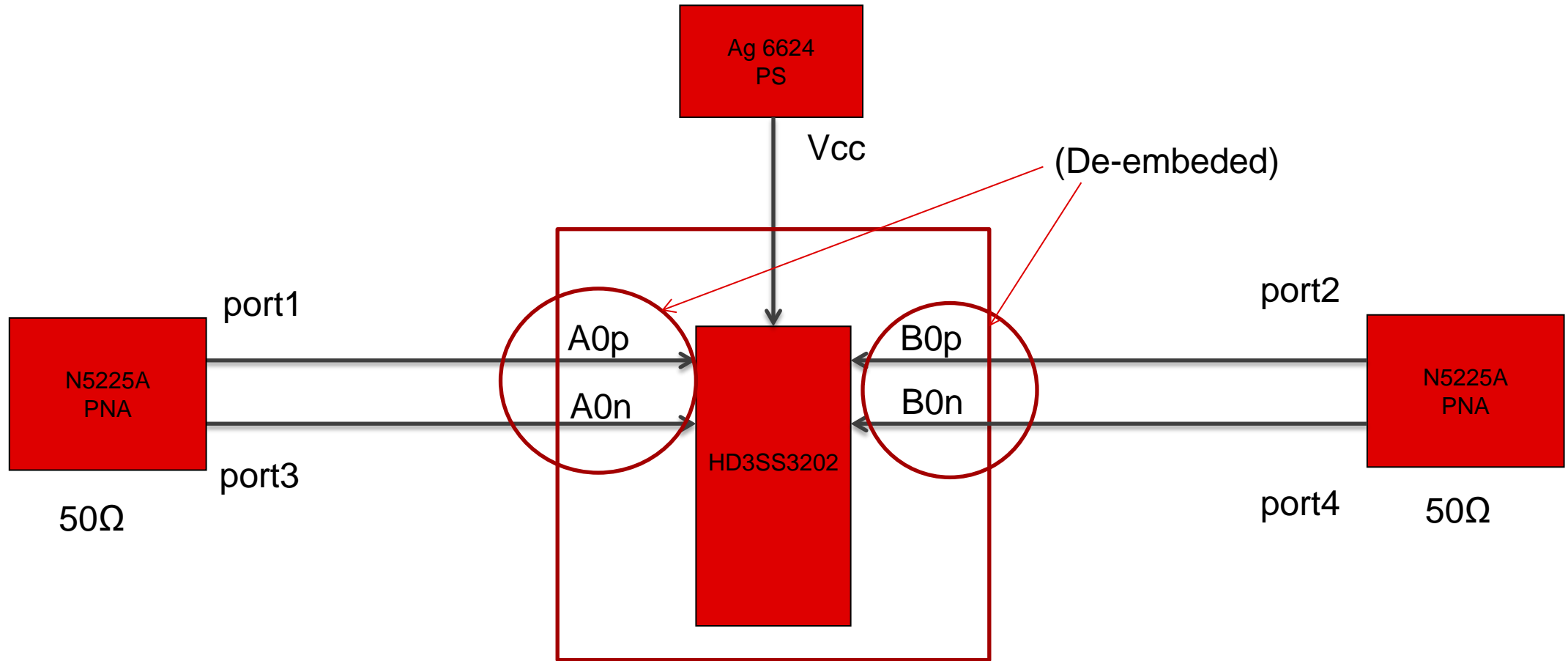
HD3SS3202RSV

High Speed Signal Conditioners

Setup Notes:

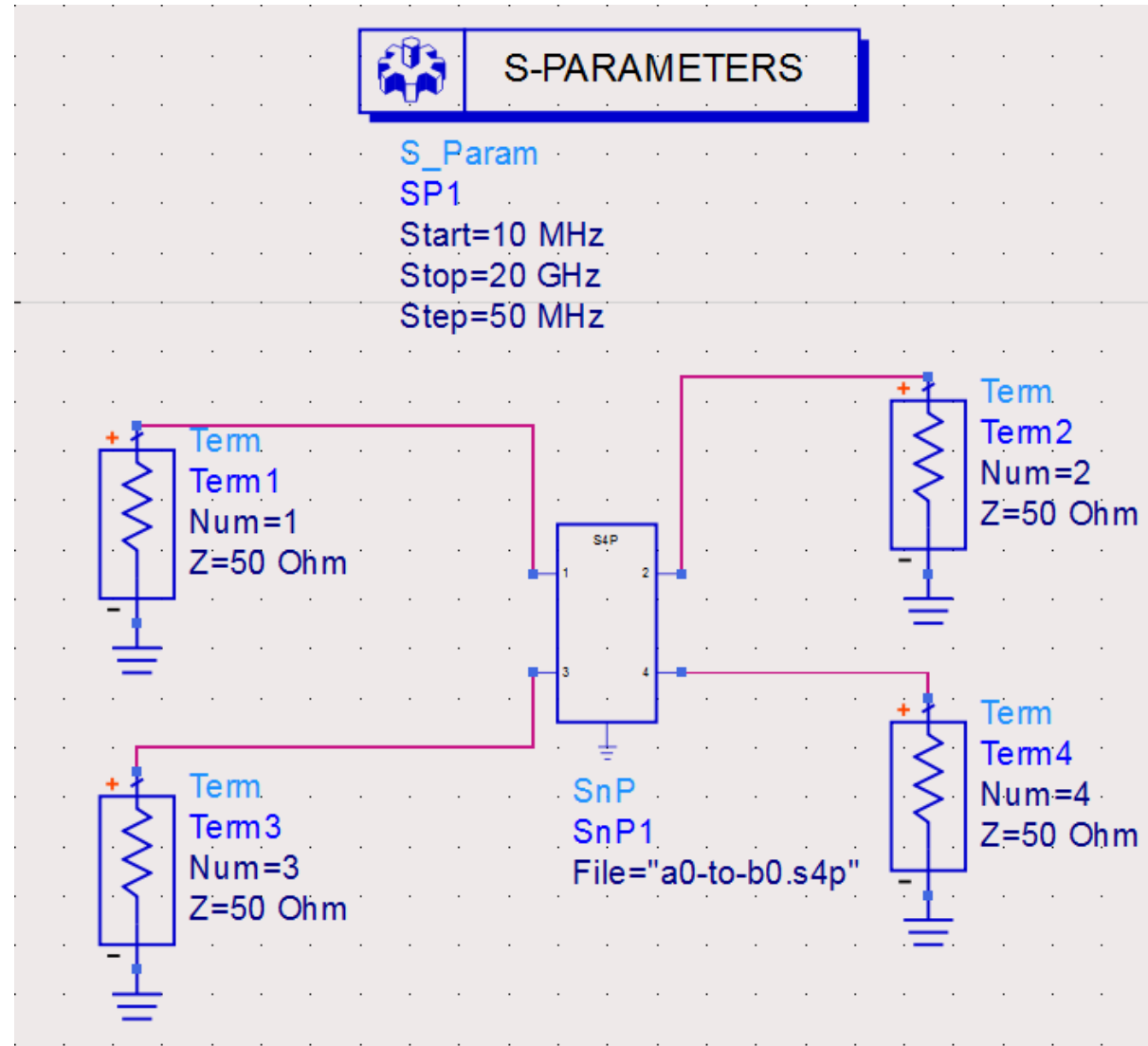
- Bench board traces have been de-embedded at the VNA (Ag N5225A)
- Using RF power of -10dBm
- VNA start freq: 10 MHz, stop freq: 20 GHz, IF BW:1kHz
- Port order: (1,3,2,4)

HD3SS3202RSV test setup: A0 to B0 path

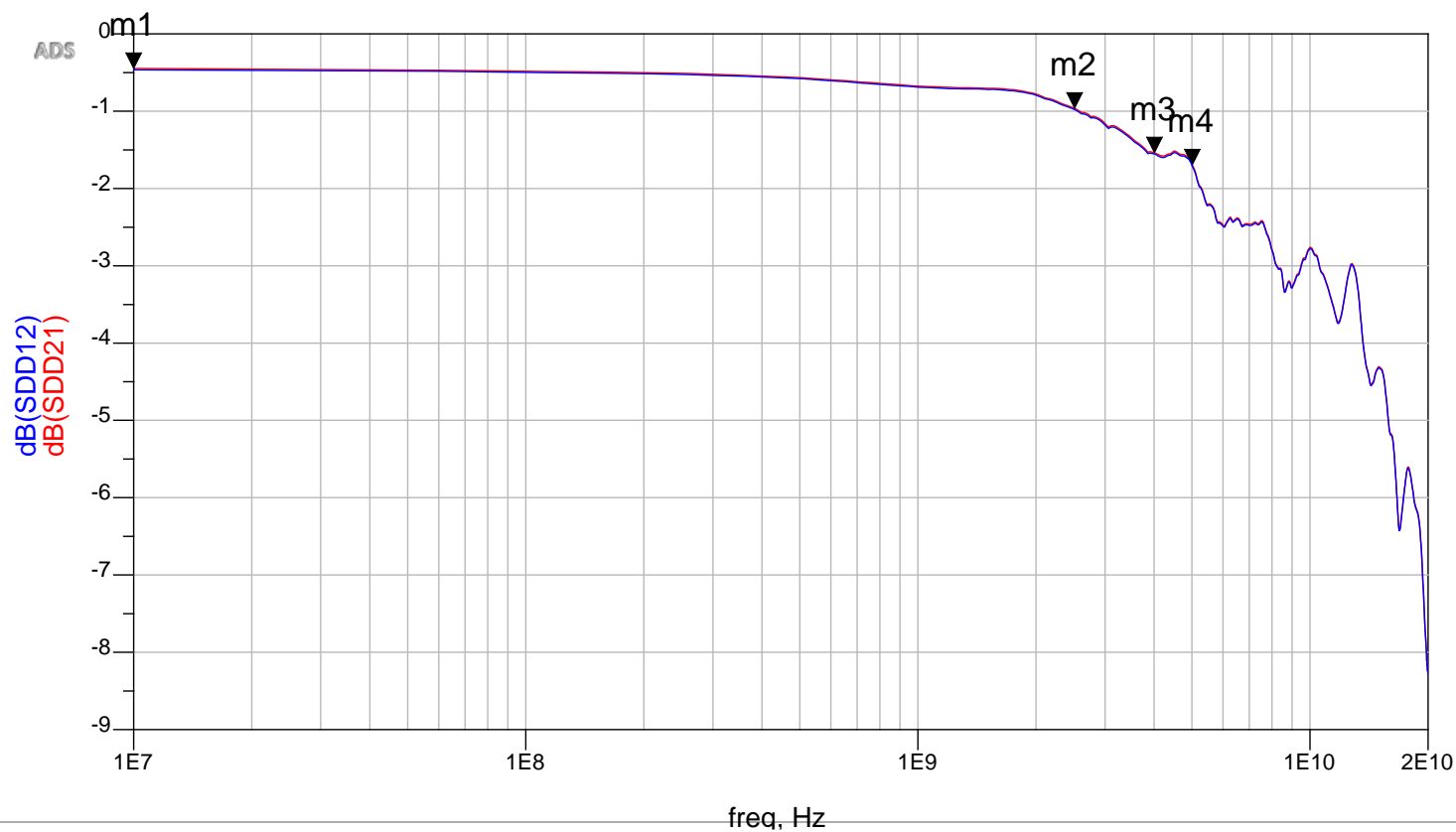


- Unused Ax, Bx, and Cx Ports terminated 50-ohms to GND.

ADS Test Bench



Insertion Loss (SDD21/SDD12))



Return Loss (SDD11/SDD22))

