

Manual Update Sheet

DATE: May 29, 2001

Document Being Updated: *TMS320C20x User's Guide*

Literature Number Being Updated: SPRU127

This Manual Update Sheet (SPRZxxx) ships with the *TMS320C20x User's Guide* (SPRU127).

Page: **Change or Add:**

9-34 In Table 9-7, replace descriptions for bits 2 through 7 as shown in the new table rows.

Table 9-7. SSPST Register — I/O address FFF2h Bit Descriptions

Bit No.	Name	Function
7-5	Transmit FIFO Status	<p>Status of the receive and transmit FIFOs. Define the status of the receive and transmit FIFOs. Each set of 3 bits is capable of indicating five different states that reflect upon the contents of the FIFOs.</p> <p>The bits are as follows:</p> <p>000 FIFO empty 001 FIFO has 1 word to transmit 010 FIFO has 2 words to transmit 011 FIFO has 3 words to transmit 100 FIFO has 4 words to transmit</p>
4-2	Receive FIFO Status	<p>Status of the receive and transmit FIFOs. Define the status of the receive and transmit FIFOs. Each set of 3 bits is capable of indicating five different states that reflect upon the contents of the FIFOs.</p> <p>The bits are as follows:</p> <p>000 FIFO empty 001 FIFO has 1 word 010 FIFO has 2 words 011 FIFO has 3 words 100 FIFO has 4 words</p>

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