PMP10736 Test Report

TEXAS INSTRUMENTS



Table of Contents

I.	Overview	.3
II.	Power Specification	.3
III.	Reference Board	.3
IV.	Efficiency	.5
	Regulation	
VI.	Thermal	.7
VII.	Power Up	.8
VIII.	Switching Waveforms	.9
IX.	Load Transients	10
X.	Output Voltage Ripples	11



I. Overview

The PMP10736 reference design is a 2.5W isolated Fly-Buck power module. It provides an isolated 5V, 500mA output with 5V to 15V input voltage range. The reference board is designed in a compact SIP-7 package, and the board size is $18 \times 13 \text{ mm} (0.7 \times 0.5 \text{ inch})$. The isolation voltage rating is 2500VAC for 1sec. The module design uses the Fly-Buck converter featuring the LM5160 synchronous buck regulator. The Fly-Buck has the advantages of primary side regulation (with no need of opto-coupler feedback). The total output variation over line and load condition is within 9%, and the peak efficiency is about 83%.

II. Power Specification

Input Voltage:	5V – 15V
Output:	Isolated 5V @ 500mA
Total output power:	2.5W
Switching frequency:	200 kHz

III. Reference Board

The reference board is designed as a power module in standard SIP 7-pin package (100mil pin pitch). The footprint of the board is shown in Figure 2. The reference board uses 1oz copper 2- layer PCB, and its dimensions are as follows:

Board size: 18 x 13 mm (0.7 x 0.5 inch).

Component height: top side 2.5mm, bottom side 10.5mm

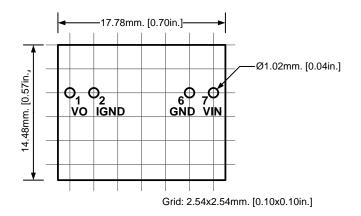


Figure 1 Reference board footprint



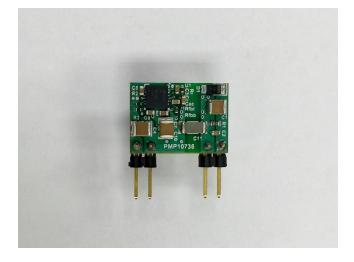


Figure 2 Reference board top view

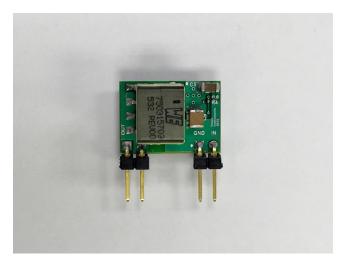


Figure 3 Reference board bottom view

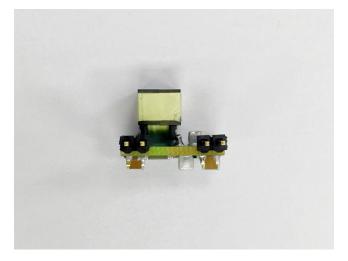


Figure 4 Reference board side view



IV. Efficiency

The reference board has a peak efficiency of 83% at 15V input and full load condition.

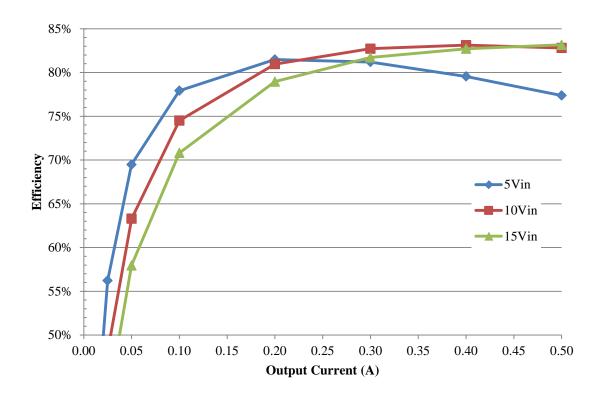


Figure 5 Power efficiency



V. Regulation

The output regulation of the reference design was examined at full input and load range. The test results show that the total output variation is within +2% to -7%.

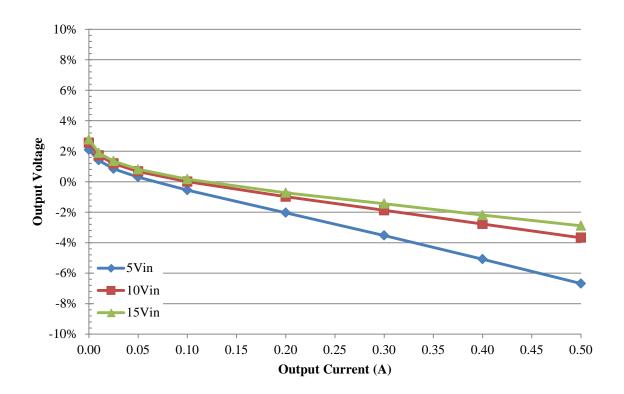


Figure 6 Output regulation



VI. Thermal

The thermal image was taken at 23°C room temperature, no air flow. The board was operating at 10V input with 0.5A load on the output.

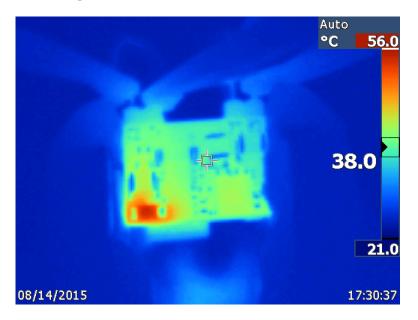


Figure 7 Thermal image from top view at 10V input, full load

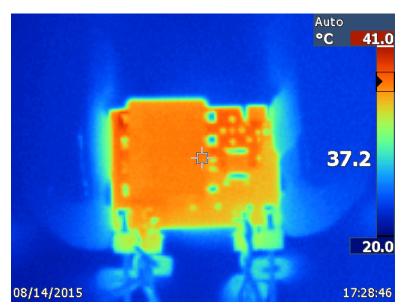


Figure 8 Thermal image from bottom view at 10V input, full load



VII. Power Up

The reference board was tested under no load and full load at 10V input. Ch1 (yellow) is the input voltage, Ch2 (green) is the output voltage.



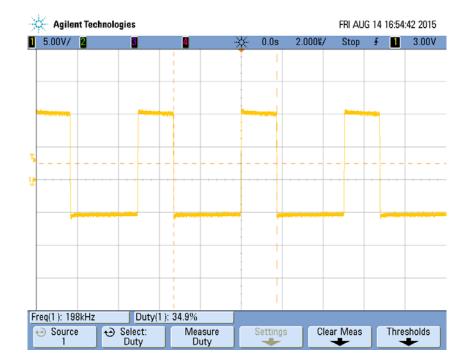
Figure 9 Power up into no load at 10V input



Figure 10 Power up into full load at 10V input



VIII. Switching Waveforms



The primary side switch node voltage was measured at no load and full load condition at 10V input. Ch1 (yellow) is the switch node voltage.

Figure 11 Switch node voltage at 10V input, no load

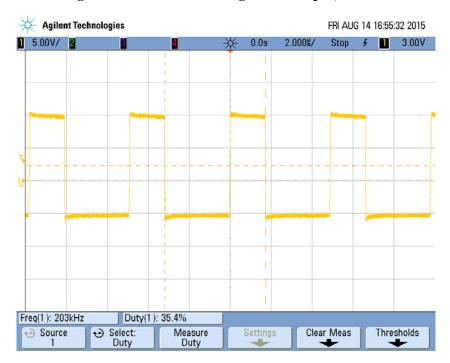
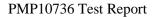


Figure 12 Switch node voltage at 10V input, full load





The output rectifier diode voltage was examined at 15V input and full load condition, which gave the highest voltage spike on the diode. Ch1 (yellow) shows the voltage across the diode.



Figure 13 Output diode anode (+) to cathode (-) voltage at 15V input, full load

IX. Load Transients

The load transient response was tested by applying 0.25A to 0.5A load steps at 10V input. Ch1 (yellow) is the output voltage in AC mode, and Ch4 (green) is the output load current.



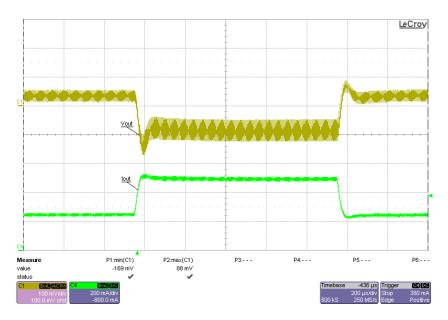


Figure 14 Output load transient at 10V input

X. Output Voltage Ripples

The output ripples were measured at 10V input, full load. Ch1 (yellow) is the output voltage ripple in AC mode.

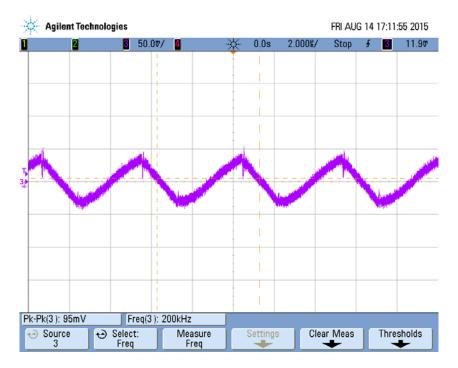


Figure 15 +15V output ripple at 10V input, full load

IMPORTANT NOTICE AND DISCLAIMER

TI PROVIDES TECHNICAL AND RELIABILITY DATA (INCLUDING DATASHEETS), DESIGN RESOURCES (INCLUDING REFERENCE DESIGNS), APPLICATION OR OTHER DESIGN ADVICE, WEB TOOLS, SAFETY INFORMATION, AND OTHER RESOURCES "AS IS" AND WITH ALL FAULTS, AND DISCLAIMS ALL WARRANTIES, EXPRESS AND IMPLIED, INCLUDING WITHOUT LIMITATION ANY IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE OR NON-INFRINGEMENT OF THIRD PARTY INTELLECTUAL PROPERTY RIGHTS.

These resources are intended for skilled developers designing with TI products. You are solely responsible for (1) selecting the appropriate TI products for your application, (2) designing, validating and testing your application, and (3) ensuring your application meets applicable standards, and any other safety, security, or other requirements. These resources are subject to change without notice. TI grants you permission to use these resources only for development of an application that uses the TI products described in the resource. Other reproduction and display of these resources is prohibited. No license is granted to any other TI intellectual property right or to any third party intellectual property right. TI disclaims responsibility for, and you will fully indemnify TI and its representatives against, any claims, damages, costs, losses, and liabilities arising out of your use of these resources.

TI's products are provided subject to TI's Terms of Sale (https://www.ti.com/legal/termsofsale.html) or other applicable terms available either on ti.com or provided in conjunction with such TI products. TI's provision of these resources does not expand or otherwise alter TI's applicable warranties or warranty disclaimers for TI products.

Mailing Address: Texas Instruments, Post Office Box 655303, Dallas, Texas 75265 Copyright © 2021, Texas Instruments Incorporated