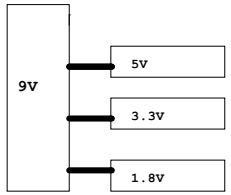
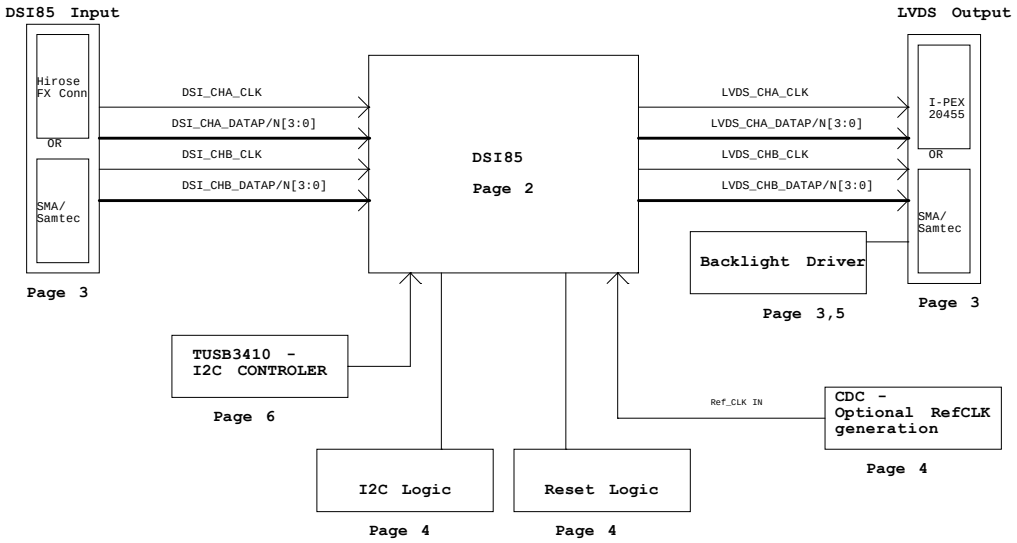
SN65DSI85Q1-EVM INT062-001



Page 5



****NOTE LEGEND****

SilkScreen: SILK SCREEN

DESIGN NOTES
NOTE:

FUNCTIONAL BLOCKS

POWER BLOCKS

DNI: Do Not Install

Title: SN65DSI85Q1-EVM INT062-001: Schematics Block Diagram		
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NOTES:

1. Reset(EN) can be implemented with passive components as shown or active circuitry. In case of using passive components, the values of the RC circuitry need to be adjusted to make sure the low to high transition occurs after the Vcc supply has reached the minimum recommended operating voltage. For this reason, it is recommended to USE ACTIVE CIRCUITRY for better control of the RESET/EN timing.

2. The number of capacitors and their values may vary depending on the system implementation

3. Optional ref CLK for LVDS Pixel CLK. 25MHz-154MHz

4. C11 1uF is min value.

5. R5: Terminate to GND with a pull-down resistor if unused

6. LEAVE UNCONNECTED FOR NORMAL OPERATION

7. This connector is to interface DSIA Ch using FX64-40P-0.8SV Type plug.

8. - Remove R6-R15 for DSI source connected to J4
- Populate R6-R15 when a source connected through J1
****R6 - R15 to be placed as near as J4 to avoid stub when J1 is not in use****

9. This header is for supplying backlight power to flat panels with LED backlight. The pinout mathces the connector definition of the back light driver TPS61181 EVM.

10. 20455-040E-12 has no pin1 datum mark. 20455-040E-0X series is same connector with pin1 datum mark, however the datum mark is on the opposite end of where pin 1 is.

11. This connector is to interface DSI interface with any source with SMA connectors via Samtec to SMA cable

12. This connector is to interface Flatlink LVDS interfaceto LCD Panels via breakout boards with Flatlink LCD panel connector

13. SHUNT on CDC_SDA and CDC_SCL pin1 and 2 if external I2C host via J10 for I2C programming of CDC device

14. WHEN INSTALLED DO NOT INSTALL J15 Power selection header

15. Remove FB6 for external power option. Vin = '5V-24V'

16. Tie to 'LOW' if external backlight driver used.

17. DimmingCTRL set to 'LOW' for default config.

18. DNI if external backlight driver used.

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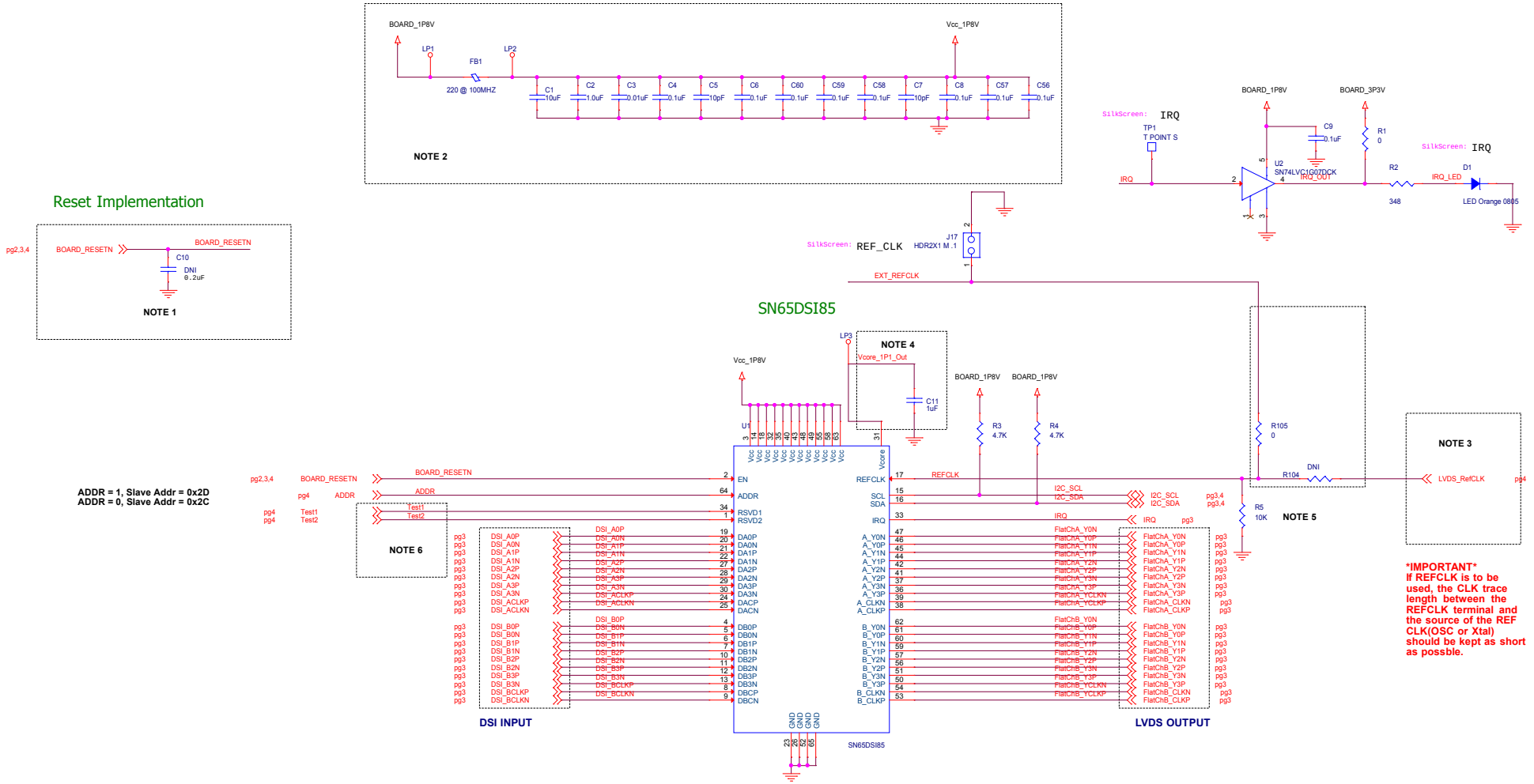

Title	SN65DSI85Q1-EVM INT062-001: PAGE 2 SN65DSI83	
Size C	Document Number SN65DSI85Q1-EVM INT062-001 REV A	Rev
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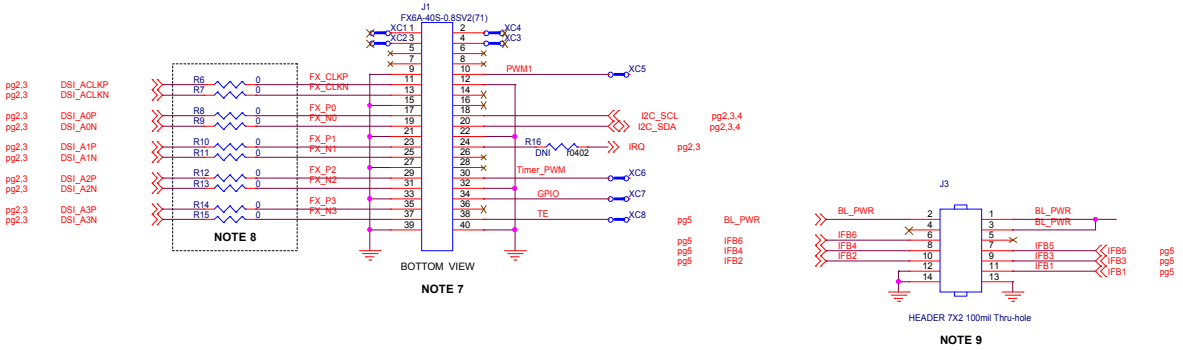
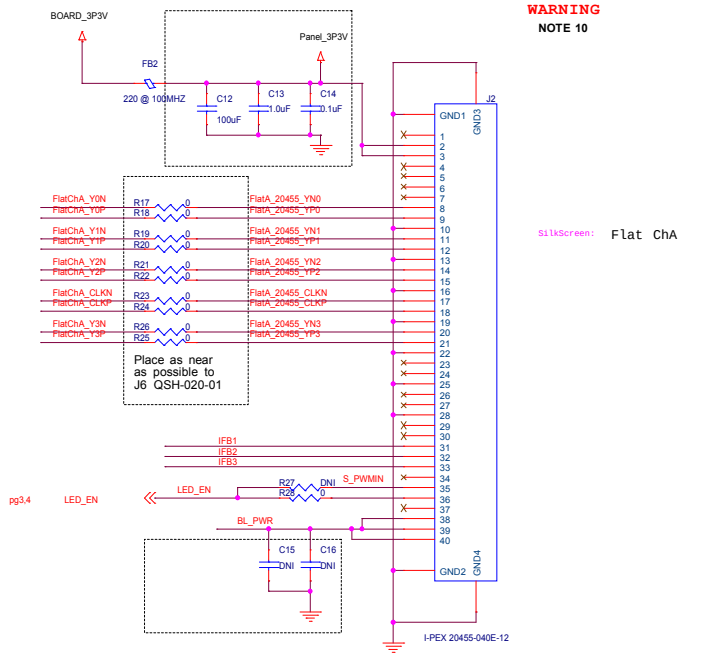
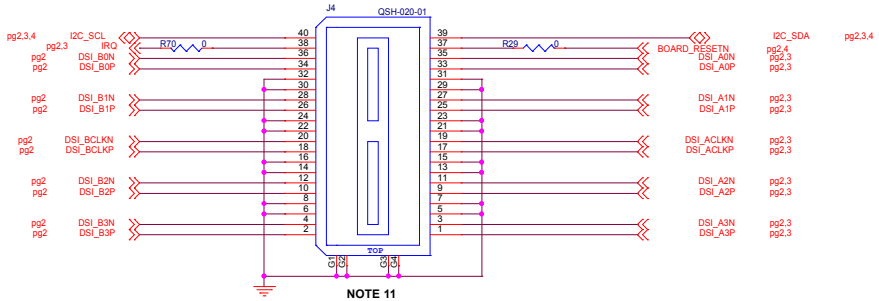
DSI ChA Display Expansion Connector



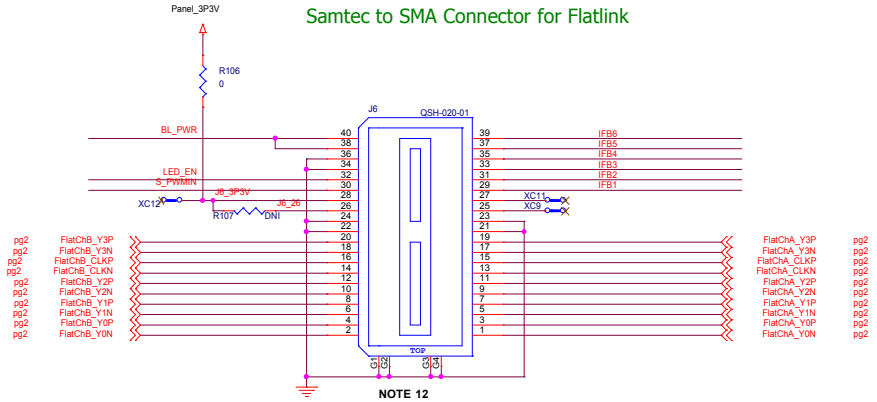
I-PEX 20455 Flatpanel Connectors

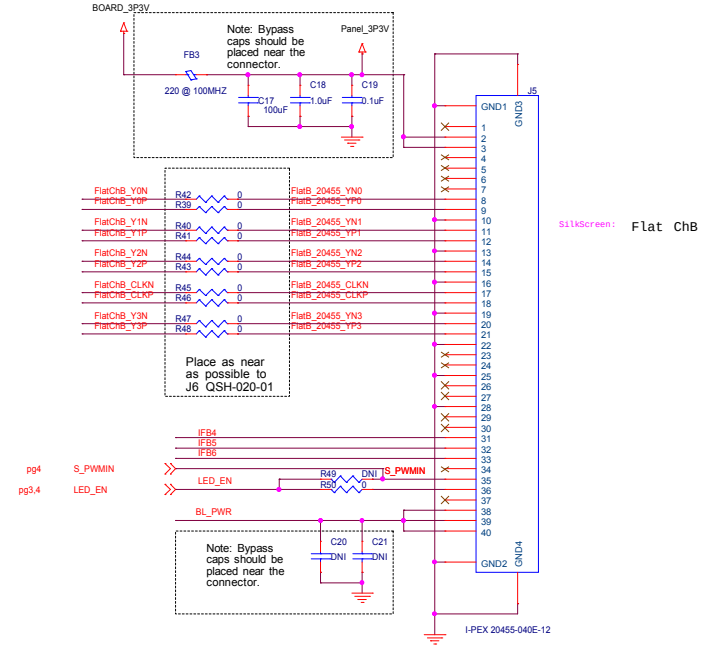


Samtec to SMA Connector for DSIA and DSIB



Samtec to SMA Connector for Flatlink





Title	SN65DSI85Q1-EVM INT062-001:PAGE 03 Connectors	
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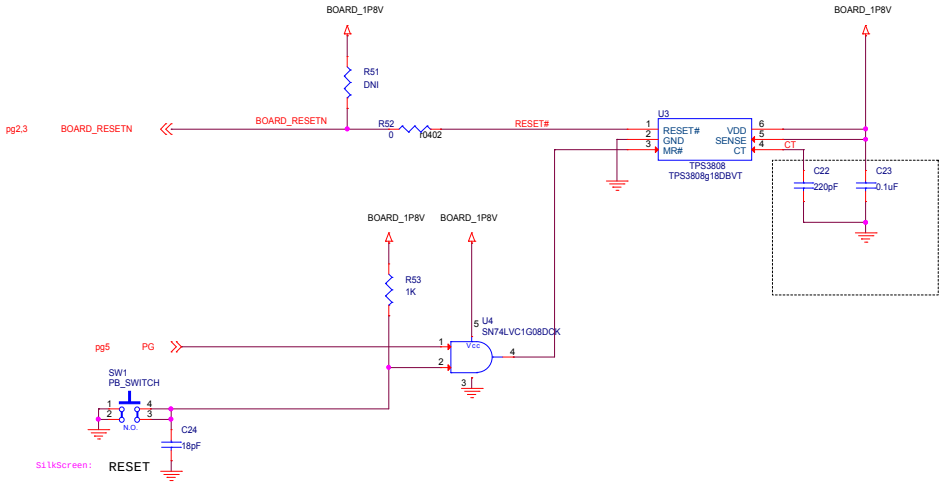
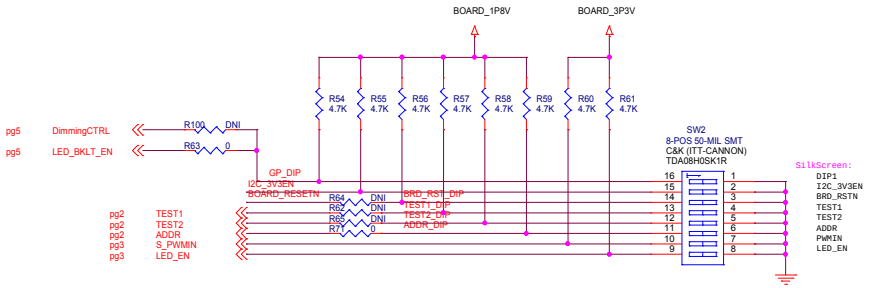
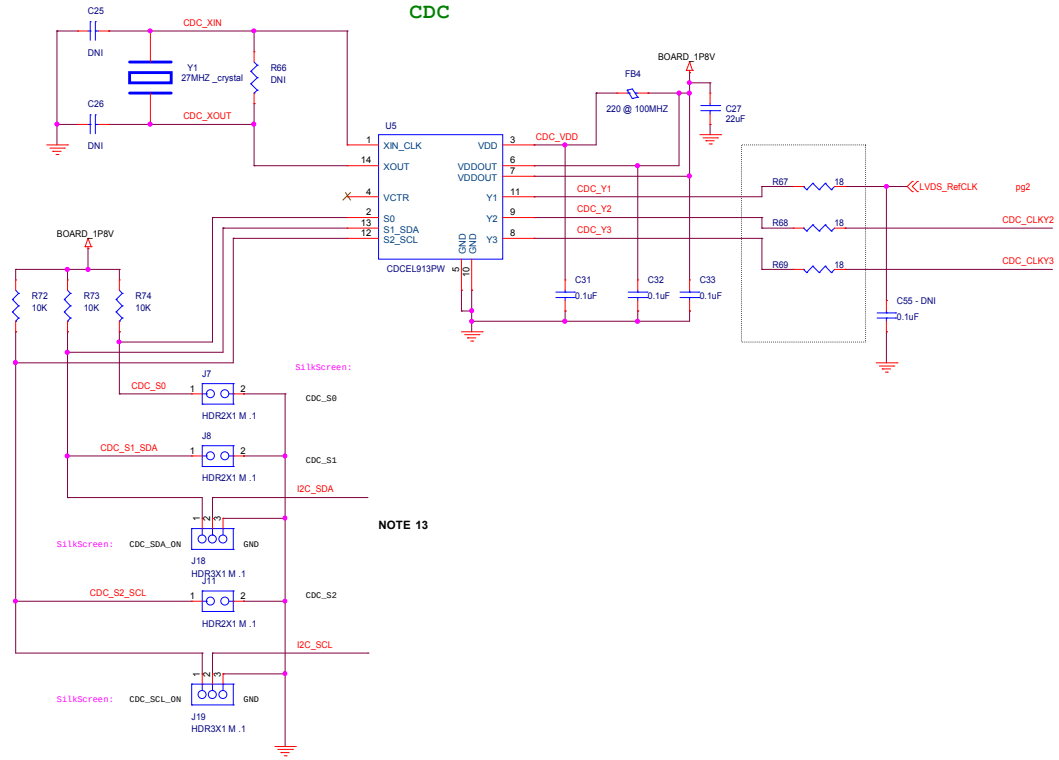
RESET



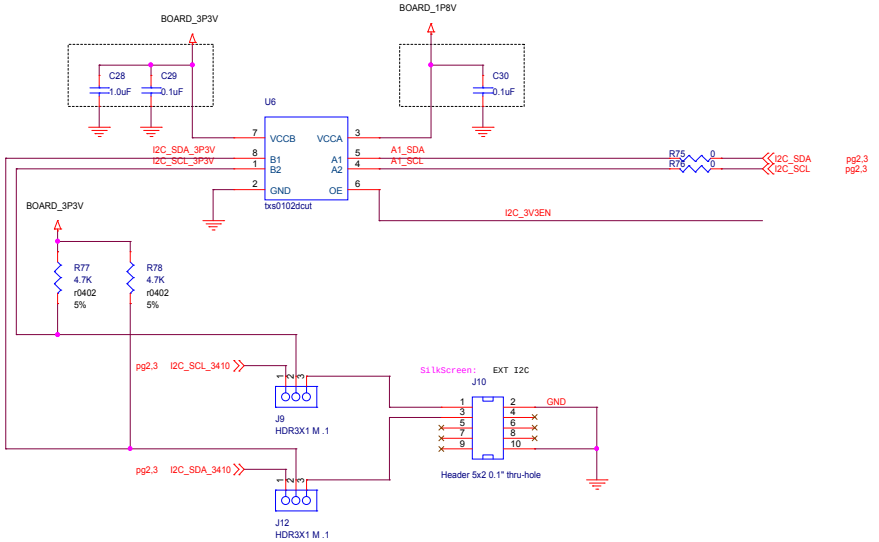
DIPSW



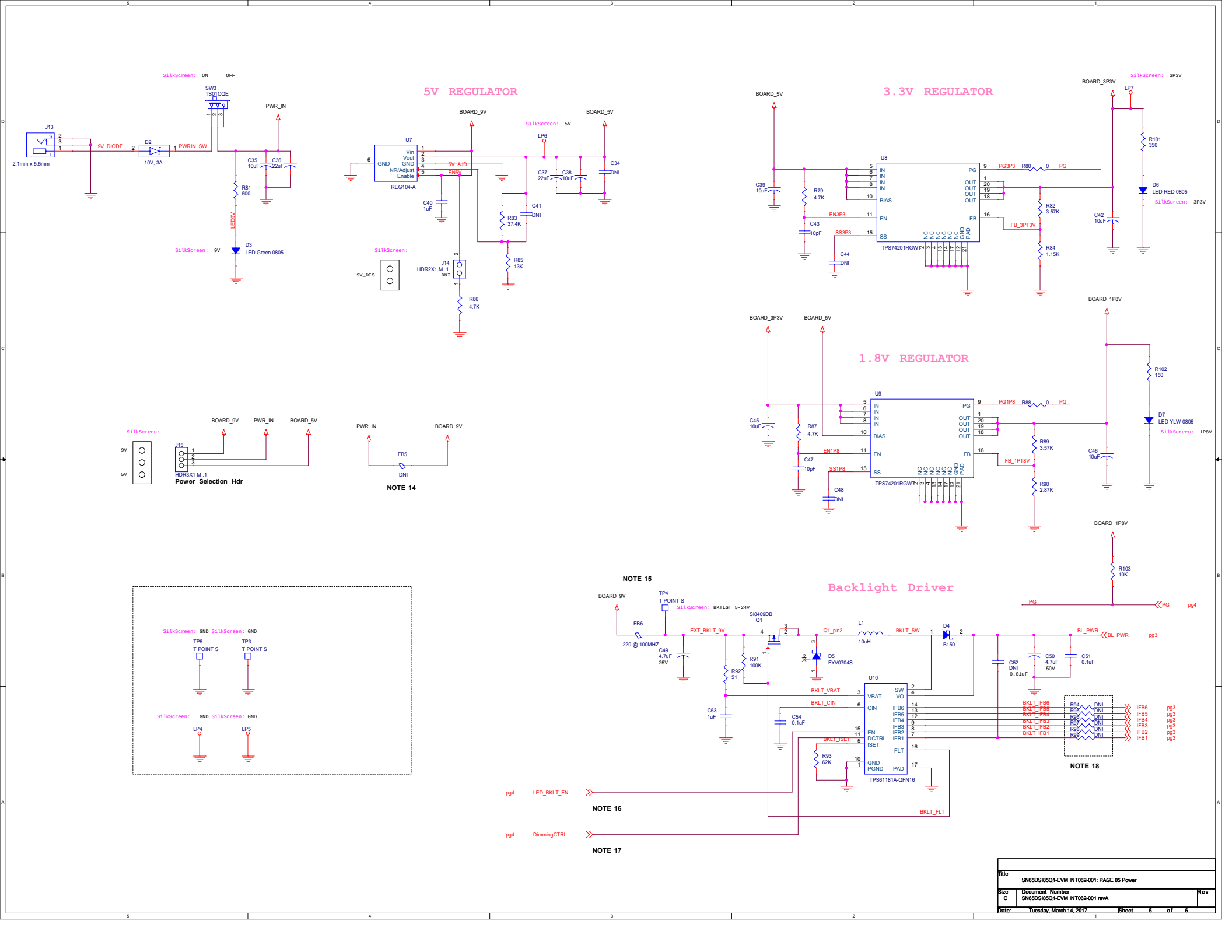
CDC



I2C



Title	SN65DSI85Q1-EVM INT062-001: PAGE 04 RESET_CDC	
Size C	Document Number SN65DSI85Q1-EVM INT062-001 revA	Rev
Date: Wednesday, March 15, 2017	Sheet 4 of 6	


5V REGULATOR
3.3V REGULATOR
1.8V REGULATOR
Backlight Driver
Power Selection Hdr
NOTE 14
NOTE 15
NOTE 16
NOTE 17
NOTE 18
TPS74201RGWT
TPS61181A-QFN16
REG104-A
Title
SN65DSI85Q1-EVM INT062-001: PAGE 05 Power
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Rev
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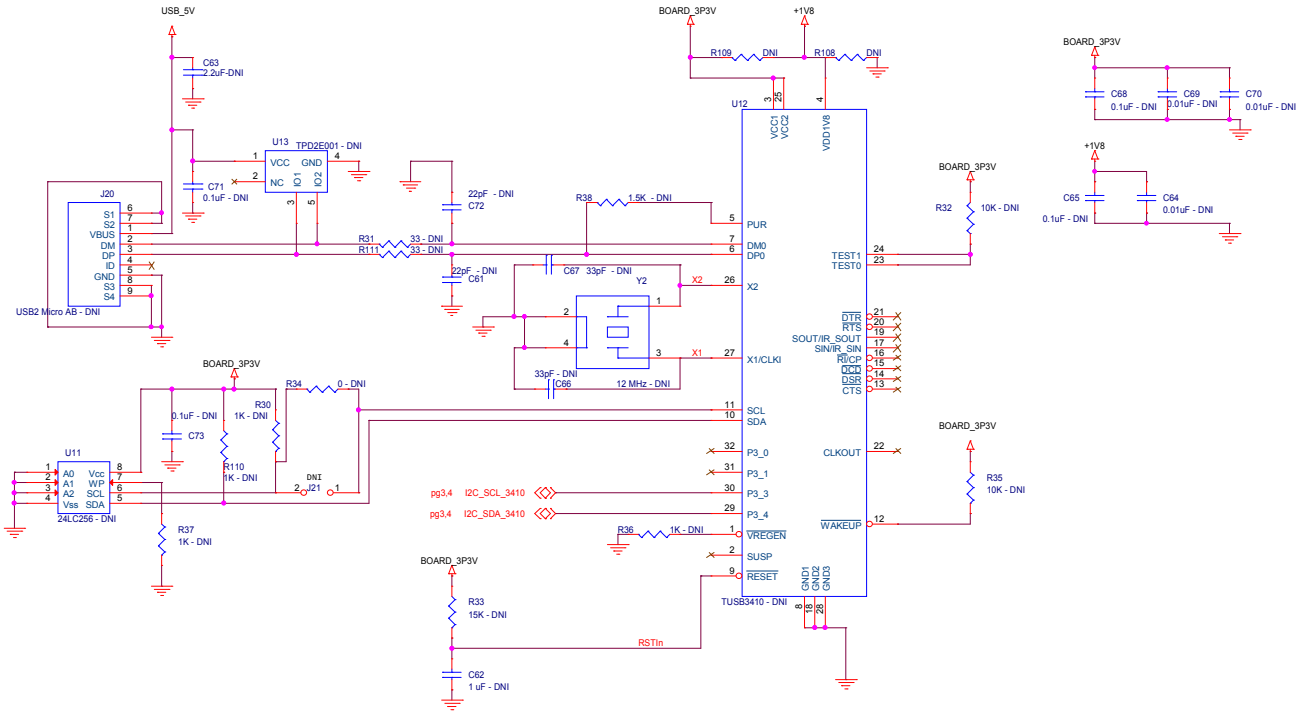

Title	SN65DSI83Q1-EVM INT062-002	
Size C	Document Number SN65DSI83Q1-EVM INT062-002 rev A	Rev
Date: Friday, April 21, 2017	Sheet 6 of 6	

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