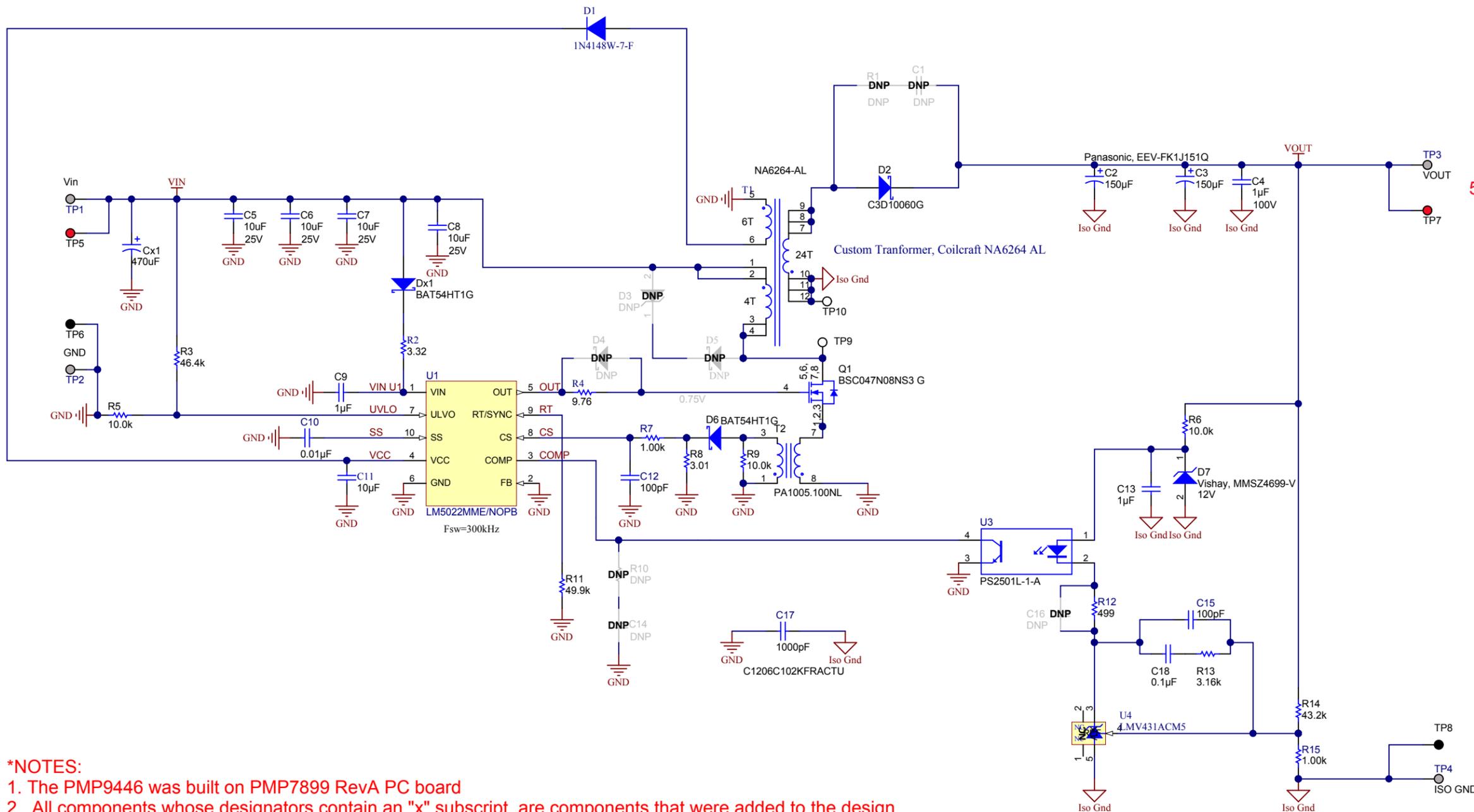


Input Voltage = 10Vin to 15Vin
(12Vin Nominal)



54Vout @ 0.75A

- *NOTES:**
1. The PMP9446 was built on PMP7899 RevA PC board
 2. All components whose designators contain an "x" subscript, are components that were added to the design. Hence, these components do not have footprints on the PCB.
 3. Cx1 is an input bulk capacitor, used to dampen the input supply, which has long inductive leads.

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Number: PMP9446	Rev: 1	Designed for: Public Release	Mod. Date: 4/14/2015
SVN Rev: * Version control disabled	Sheet Title: *	Project Title: LM5022 Flyback	
Drawn By: *	File: PMP9446 Schematics.SchDoc	Assembly Variant: 001	Sheet 1 of 2
Engineer: Hrag Kasparian	Contact: http://www.ti.com/support	Size: B	



DNP FID1 DNP FID2 DNP FID3

PCB: PMP7899
PMP7899

PCB
LOGO
Texas Instruments

LBL1
PCB Label
Size: 0.65" x 0.20 "

Label Table	
Variant	Label Text
001	

ZZ2
Assembly Note
These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3
Assembly Note
These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4
Assembly Note
These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

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