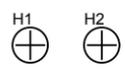
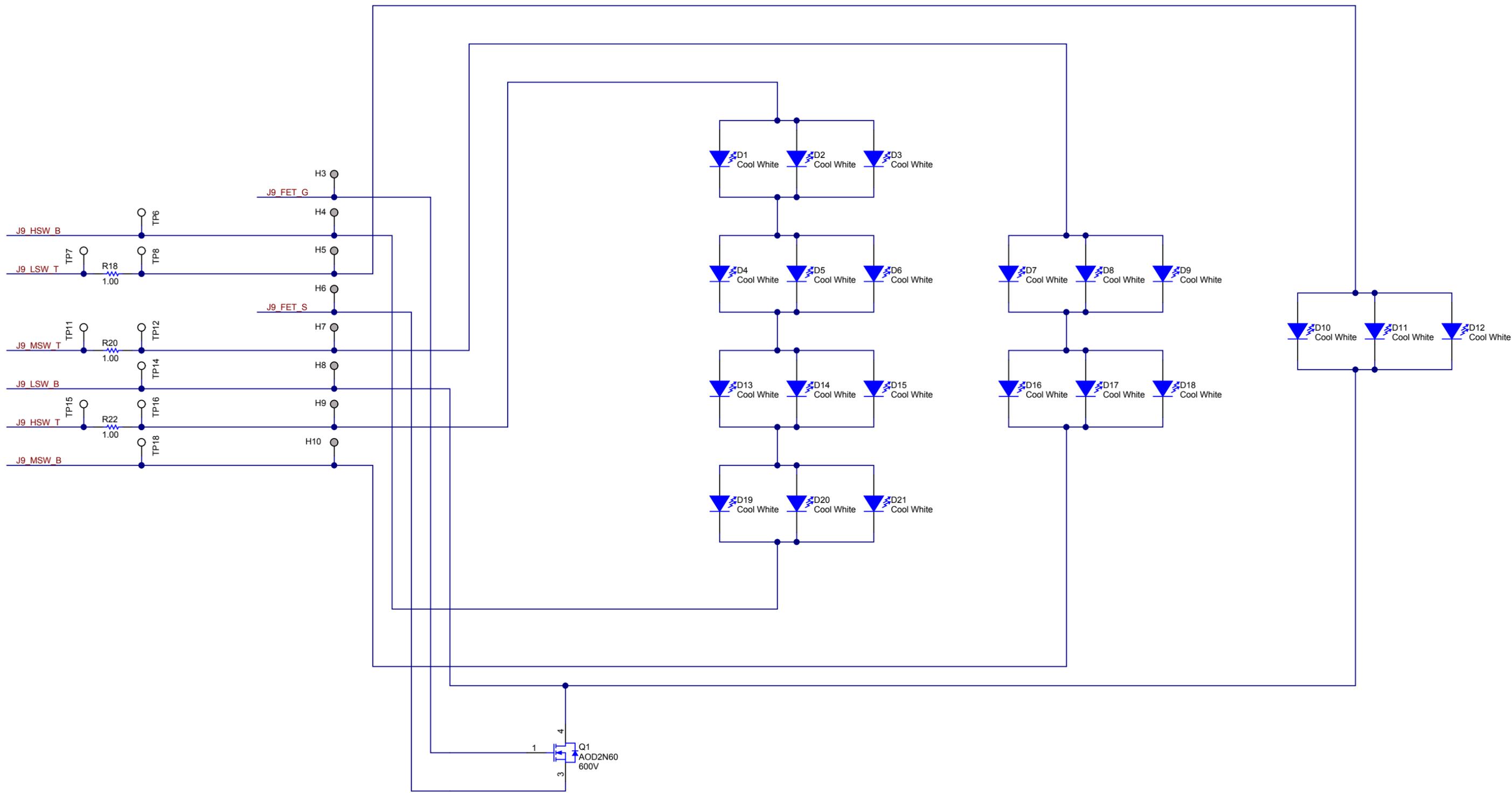


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| Designed for: Public Release | | Mod. Date: 7/28/2014 | |
| Project Title: PMP6022 | | Sheet Title: Control_Driver_Ckt | |
| Number: PMP6022 | Rev: E1 | Assembly Variant: [No Variations] | Sheet: 1 of 2 |
| SVN Rev: Not in version control | | File: PMP6022E1_pg2.SchDoc | Size: B |
| Drawn By: Kerry Murphy | | Contact: http://www.ti.com/support | |
| Engineer: Irwin Nederbragt | | © Texas Instruments 2014 | |



120V FR4 w/LEDs & FET [to simulate TClad design]



PCB Number: PMP6022
PCB Rev: E1

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| Project Title: PMP6022 | Rev: E1 | |
| Number: PMP6022 | Sheet Title: LEDs_FET | http://www.ti.com © Texas Instruments 2014 |
| SVN Rev: Not in version control | Assembly Variant: [No Variations] | |
| Drawn By: Kerry Murphy | File: PMP6022E1_pg1.SchDoc | |
| Engineer: Irwin Nederbragt | Contact: http://www.ti.com/support | |

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