	1	2	3	4		5	6
[		_		I	I		Up Detail for: HVLO84A.PcbDoc
				Symbol Hit Count Tool Size	Plated Hole Type	Layer <u>Kame</u> Top Solder Mask Top Layer	Gerber Copper Dielectric Document Thickness Paterial (.GTS) Solder Resist (.GTL) I.4mil rp.4
A				○ 55 28mil (0.711mm)   × 10 39.37mil (1mm)   ∞ 52 40mil (1.016mm)   ∨ 36 40.157mil (1.02mm)   ∨ 4 43.307mil (1.1mm)   ∞ 3 45mil (1.143mm)   × 2 46mil (1.143mm)   × 2 46mil (1.6mm)	PTH Round   PTH Round		C.GD 1.4ml FR-4 C.G20 1.4ml FR-4 C.GBLO 1.4ml FR-4
				B 3 65mil (1.651mm)   c 8 67mil (1.702mm)   o 2 86.614mil (2.2mm)   a 125.984mil (3.2mm)   348 Total   Drill Table	NPTH Round PTH Round PTH Round DPTH Round	MIN. MIN. MIN.	ber of Loyers : <u>4</u> TRACK WDTH: <u>6</u> ML CLEARANCE: <u>3</u> ML VA PAD SIZE: <u>16</u> ML WA PAD SIZE: <u>16</u> ML
				ntji jadie		PER PC	ULAR RNG 0.101mm (4NIL] EXTERNAL
		инни				FR-	408 X FR-4 High Tg OTHER X 62 MlL (1.6mm) +/-10% OTHER X ANSI IPC-6012 TYPE 3 CLASS 2
в							OTHER +/ NESS (FNISHED):
	0 28 O					INNER SIGNAL DRILLING: REFERENCE:	k: X 1.4ML (1oz) 2ML (1.4oz) 2.8ML (2oz)   :: X 1.4ML (1oz) 2.8ML (2oz) N/A   X: AS SHOWN X NC_PRIL FLES
	~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~	н о ородина инини о ородина ородо о о о о				BOARD FINISH: SILKSCREET	РРЕР ТНІСКИЕSS: X 1ML
	× * × * × ° 0 ° 0 ◊ 0 °					SURFACE FINIS	ISIST COLOR: GREEN BLUE X OTHER <u>RED</u> H: X IMMERSION GOLD (ENIG) ENEPIG N/SILVER OR EQUIV OTHER
с						_	MATERIALS AND WORKMANSHIP FOR ALL PCBS TO MEET OR EXCEED THE REQUIREMENTS OF:
	0 0 8 0 0 0 0 0 0 0 0 0 0 0 0 0					X UL ADDITIONAL RE MICROSECTIO	N: YES
	ν τ τ τ τ τ τ τ τ τ τ τ τ τ τ τ τ τ τ τ	α α α α α σ σ α μ μ σ σ α α α α σ σ α α α α σ σ φ ο σ σ σ σ σ					DELEC. TEST: NONE X REQUIRED PER ORDER RER'S UL: RAL METAL X SUK
	о о о т о о о т т	*** <sup>*</sup> *				PROJECT TITLE: TCA5013E	
D	4000 (mil)	(mil)				DESIGNED FOR: Public R FLE NAME: HVL084A.	
	ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: HVL084 REV: A		exas Instruments (TI) and/or its licensors do not uarr r any information contained therein. TI and/or its lice me specifications, will be suitable for your application	ensors do not warrant that this design will me n or fit for any particular purpose, or will e	operate in Brian Ber	
	PLOT NAME = Fabrication Drawing	GENERATED : 5/16/2014 1:14:02 PM 2		n implementation. TI and/or its licensors do not uarrar empletely validate and test your design implementation 4.	to confirm the system functionality for your .		.70 ALTUM DESGNER VERSON: 10.0.0.27009 6
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