

# bq27532EVM with bq27532 Battery Management Unit Impedance Track<sup>™</sup> Fuel Gauge and bq24250 2-A, Switch-Mode Battery Charger for Single-Cell Applications

This evaluation module (EVM) is a complete evaluation system for the Battery Management Unit (BMU) chipset consisting of the bq27532-G1 fuel gauge and bq24250 battery charger. The EVM includes one bq27532 circuit, including a current sense resistor and one thermistor. In addition, the fuel gauge controls the settings of the bq24250 battery charger and monitors its status via I<sup>2</sup>C communication lines. Together, the chipset provides all necessary components to monitor and predict capacity for a system-side fuel gauge solution as well as to charge the battery from either an adapter or USB input with up to 2-A of charge current. The circuit module connects directly across the battery pack. With the EV2300 interface board and software, the user can read the bq27532-G1 data registers, program the chipset for different pack configurations, log cycling data for further evaluation, and evaluate the overall functionality of the battery management unit solution under different charge and discharge conditions. The latest Windows<sup>TM</sup>-based PC software can be downloaded from the product folder on the Texas Instruments Web site.

#### Contents

| 1 | Features  | 2  |
|---|---|----|
| 2 | bq27532-Based Circuit Module                                      | 2  |
| 3 | Circuit Module Physical Layouts, Bill of Materials, and Schematic | 5  |
| 4 | EVM Hardware and Software Setup                                   | 12 |
| 5 | Troubleshooting Unexpected Dialog Boxes                           | 12 |
| 6 | Operation   | 13 |
| 7 | Calibrate Screen  | 14 |
| 8 | Advanced Communication I2C  | 17 |
| 9 | Related Documentation From Texas Instruments                      | 19 |

#### List of Figures

| 1  | Schematic                           |    |
|----|-------------------------------------|----|
| 2  | bq27532EVM-001 Layout – Silk Screen | 5  |
| 3  | Layer 1                             | 6  |
| 4  | Layer 2                             | 7  |
| 5  | Layer 3                             | 8  |
| 6  | Layer 4                             | 9  |
| 7  | DataRAM Screen                      | 13 |
| 8  | Data Memory Screen                  | 14 |
| 9  | Calibration Screen                  | 16 |
| 10 | I <sup>2</sup> C Pro Screen         | 17 |
| 11 | Parameter Q&A Screen                | 18 |
| 12 | Golden Image Output Screen          | 18 |
| 13 | Perform Programming Screen          | 18 |

#### List of Tables

#### 1 Ordering Information ...... 2

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| Features |
|----------|
|----------|

2

#### 1 Features

- Complete evaluation system for the Battery Management Unit chipset consisting of the bq27532-G1 Impedance Track<sup>™</sup> fuel gauge and bq24250 2-A Battery Charger.
- Populated circuit module for quick setup
- Personal computer (PC) software and interface board (orderable on TI website) for easy evaluation
- Software that allows data logging for system analysis (available in product folder on TI website)
- · Ability to upgrade to the latest firmware version by flash reprogramming

#### 1.1 Kit Contents

- bq27532-G1 and bq24250 chipset circuit module (PWR656)
- NTC103AT thermistor

This EVM is used for the evaluation of the bq27532-G1 and bq24250 BMU chipset. Ensure that you visit the product Web folder at <u>www.ti.com</u> to download the latest firmware version, evaluation software, and documentation for the associated product to be evaluated.

#### 1.2 Ordering Information

#### **Table 1. Ordering Information**

| EVM PART NUMBER | CHEMISTRY | CONFIGURATION | CAPACITY |
|-----------------|-----------|---------------|----------|
| bq27532EVM-656  | Li-ion    | 1 cell        | Any      |

### 2 bq27532-Based Circuit Module

The bq27532-based circuit module is a complete and compact example solution of a bq27532 circuit for battery management. The circuit module incorporates a bq27532 battery gas gauge integrated circuit (IC) and all other components necessary to accurately predict the capacity of 1-series Li-ion cell.

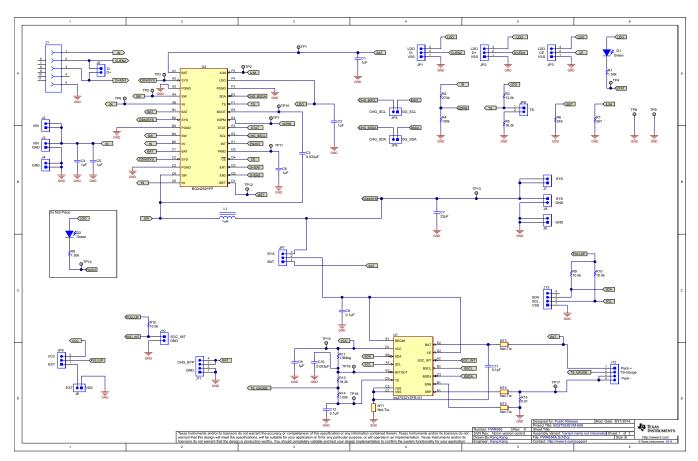


Figure 1. Schematic

### 2.1 Circuit Module Connections

Contacts on the circuit module provide the following connections:

- Direct connection to the battery pack (J12): Pack+, Pack-, and TS-Gauge
- To the serial communications port (J13): SDA, SCL, and  $\mathrm{V}_{\mathrm{SS}}$
- To the system load (J8)
- To charger (J3)
- Access to charger bypass (J11): CHG\_BYP, GND
- External pull-up for gauge I<sup>2</sup>C (J6): EXT,  $V_{ss}$
- USB charger input (J1)

### 2.2 I/O Description

| Header/Terminal Block | Description                                     |
|-----------------------|---|
| J1                    | Micro-USB                                       |
| J2-VIN                | Adapter positive terminal                       |
| J3-VIN                | Adapter positive terminal                       |
| J3-GND                | Adapter negative terminal                       |
| J4-GND                | Adapter negative terminal                       |
| J6-EXT                | External gauge I <sup>2</sup> C pull-up voltage |
| J7-SYS                | System positive terminal                        |



#### bq27532-Based Circuit Module

| Header/Terminal Block | Description                                  |
|-----------------------|--|
| J8-SYS                | System positive terminal                     |
| J8-GND                | System negative terminal                     |
| J9-GND                | System negative terminal                     |
| J11-CHG_BYP           | Charger bypass access                        |
| J12-Pack+             | Battery positive terminal                    |
| J12-TS-Gauge          | Pack thermistor input to gauge TS pin        |
| J12-Pack-             | Battery negative terminal                    |
| J13                   | I <sup>2</sup> C access for gauge evaluation |

#### 2.3 Test Points

| Test Point | Description                 |
|------------|-----------------------------|
| TP1        | bq24250 BAT pin             |
| TP2        | bq24250 ILIM pin            |
| TP3        | bq24250 SYS pin             |
| TP4        | bq24250 STAT pin            |
| TP5        | bq24250 SW pin              |
| TP6        | bq24250 IN pin              |
| TP7        | bq24250 VDPM pin            |
| TP8        | GND                         |
| TP9        | GND                         |
| TP10       | bq24250 BOOT pin            |
| TP11       | bq24250 PMID pin            |
| TP12       | bq24250 ISET pin            |
| TP13       | System positive terminal    |
| TP14       | bq24250 INT pin             |
| TP15       | bq27532 V <sub>cc</sub> pin |
| TP16       | bq27532 BI/TOUT pin         |
| TP17       | Pack-                       |
| TP18       | bq27532 TS pin              |

#### Control and Key Parameters Setting 2.4

| Jumper | Description   | Default Factory Setting   |
|--------|---|---------------------------|
| J5     | Connects D+/EN1 and D-/EN2  | NOT INSTALLED             |
| J10    | Connects SOC_INT to external pullup resistor  | NOT INSTALLED             |
| JP1    | 2-3 (D–/EN2 = HI): D–/EN2 to logic high<br>2-1 (D–/EN2 = LO): D–/EN2 to logic low   | 2-1 (D–/EN2 = LO)         |
| JP2    | 2-3 (D+/EN1 = HI): D+/EN1 to logic high<br>2-1 (D+/EN1 = LO): D+/EN1 to logic low   | 2-1 (D+/EN1 = LO)         |
| JP3    | 2-3 (CE = HI): To place the battery charge in standby mode<br>2-1 (CE = LO): Charge enable  | 2-1 (CE = LO)             |
| JP4    | Connects gauge BSCL to charger SCL  | INSTALLED                 |
| JP5    | Connects gauge BSDA to charger SDA  | INSTALLED                 |
| JP6    | 2-3 (PULL_UP = $V_{CC}$ ): Ties I <sup>2</sup> C pull-up resistor to gauge $V_{CC}$<br>2-1 (PULL_UP = EXT): Ties I <sup>2</sup> C pull-up resistor to external source | 2-3 (PULL_UP = $V_{CC}$ ) |
| JP7    | 2-3 (REGIN = SYS): Powers gauge from system<br>2-1 (REGIN = BAT): Powers gauge from battery   | 2-3 (REGIN = SYS)         |
| JP8    | Connects TS to pull-down resistor to force charger into cold temperature JEITA range  | NOT INSTALLED             |

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### 3 Circuit Module Physical Layouts, Bill of Materials, and Schematic

This section contains the board layout, bill of materials, assembly drawings, and schematic for the bq27532 circuit module.

### 3.1 Board Layout

This section shows the printed-circuit board (PCB) layers (Figure 2 through Figure 5), assembly drawing, and schematic for the bq27532 module.

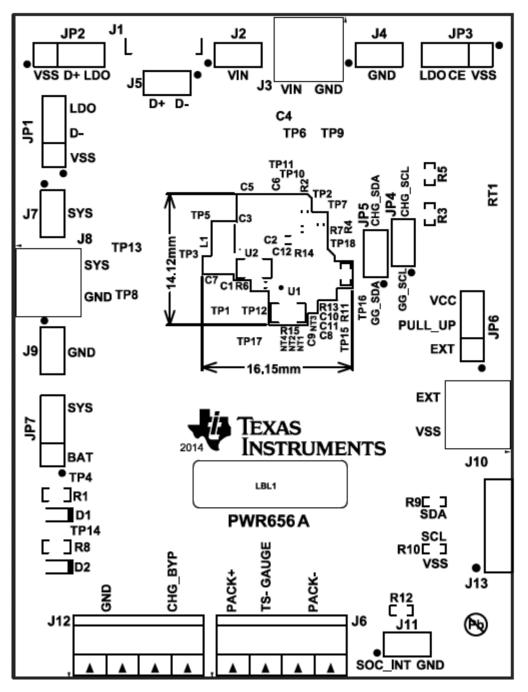


Figure 2. bq27532EVM-001 Layout – Silk Screen



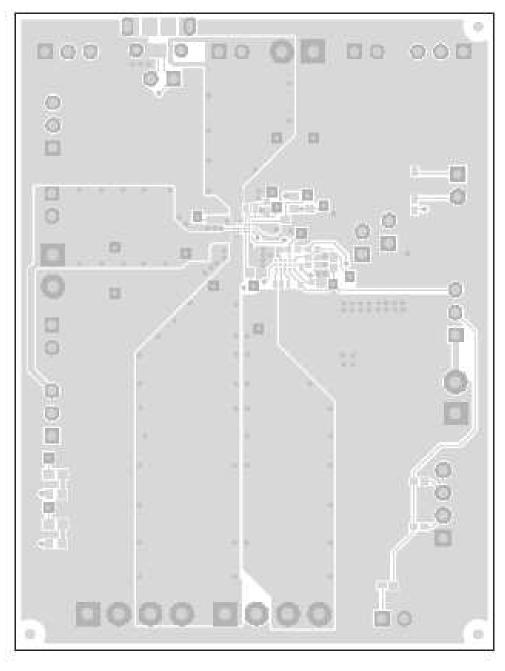


Figure 3. Layer 1

6



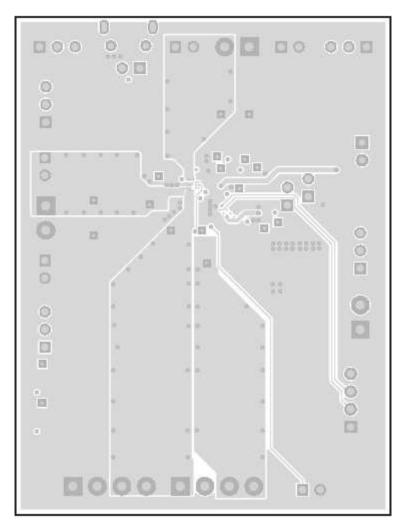


Figure 4. Layer 2



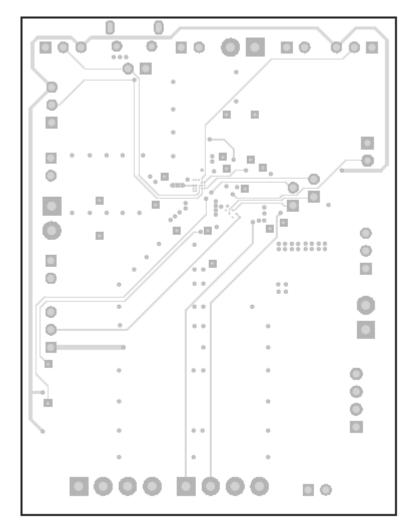


Figure 5. Layer 3

8



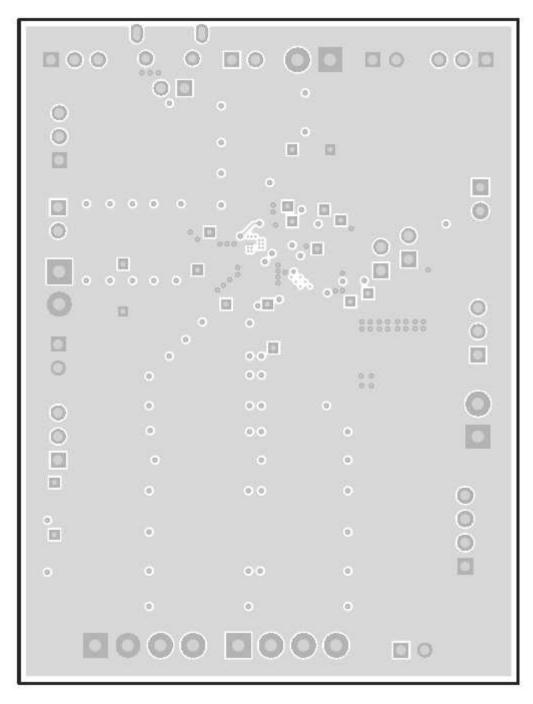


Figure 6. Layer 4



### 3.2 Bill of Materials and Schematic

| Designator                                | Quantity | Value   | Description  | Package Reference                  | Part Number         | Manufacturer                   |
|---|----------|---------|--|------------------------------------|---------------------|--------------------------------|
| C1  | 1        | 1µF     | CAP, CERM, 1µF, 10V, ±10%, X5R, 0805   | 0805                               | GRM216R61A105KA01D  | MuRata                         |
| C2  | 1        | 1µF     | CAP, CERM, 1µF, 16V, ±10%, X7R, 0603   | 0603                               | GRM188R71C105KA12D  | MuRata                         |
| C3, C10                                   | 2        | 0.033µF | CAP, CERM, 0.033µF, 25V, ±10%, X7R, 0402   | 0402                               | GRM155R71E333KA88D  | MuRata                         |
| C4, C5                                    | 2        | 1µF     | CAP, CERM, 1µF, 25V, ±10%, X5R, 0805   | 0805                               | GRM216R61E105KA12D  | MuRata                         |
| C6  | 1        | 1µF     | CAP, CERM, 1µF, 16V, ±10%, X5R, 0402   | 0402                               | C1005X5R1C105K050BC | TDK                            |
| C7  | 1        | 22µF    | CAP, CERM, 22µF, 10V, ±20%, X5R, 0805  | 0805                               | CL21A226MPCLRNC     | Samsung                        |
| C8, C11, C12                              | 3        | 0.1µF   | CAP, CERM, 0.1µF, 6.3V, ±10%, X5R, 0402  | 10%, X5R, 0402 0402 C1005X5R0J104K |                     | TDK                            |
| C9  | 1        | 1µF     | CAP, CERM, 1µF, 10V, ±10%, X5R, 0402   | 0402 GRM155R61A105KE15D            |                     | MuRata                         |
| D1  | 1        | Green   | LED, Green, SMD  | 1.6 x 0.8 x 0.8 mm LTST-C190GKT    |                     | Lite-On                        |
| J1  | 1        |         | Connector, Receptacle, Micro-USB Type B, R/A,<br>Bottom Mount SMT Micro-USB Type B, R/A, |                                    | Molex               |                                |
| J2, J4, J5, J7, J9, J10,<br>JP4, JP5, JP8 | 9        |         | Header, 100mil, 2x1, Tin plated, TH  | Header, 2 Pin, 100 mil,<br>Tin     | PEC02SAAN           | Sullins Connector<br>Solutions |
| J3, J6, J8                                | 3        |         | Terminal Block, 6A, 3.5mm Pitch, 2-Pos, TH   | 7.0 x 8.2 x 6.5 mm                 | ED555/2DS           | On-Shore<br>Technology         |
| J11, J12                                  | 2        |         | Terminal Block, 6A, 3.5mm Pitch, 4-Pos, TH   | 14 x 8.2 x 6.5 mm                  | ED555/4DS           | On-Shore<br>Technology         |
| J13                                       | 1        |         | Header, 100 mil, 4x1, R/A, TH  | 4x1 R/A Header                     | 22-05-3041          | Molex                          |
| JP1, JP2, JP3, JP6,<br>JP7                | 5        |         | Header, 100 mil, 3x1, Tin plated, TH   | Header, 3 Pin, 100 mil,<br>Tin     | PEC03SAAN           | Sullins Connector<br>Solutions |
| L1  | 1        | 1µH     | Inductor, Shielded, Ferrite, 1μH, 3A, 0.045 Ω, SMD                                       | 2.5 x 1.2 x 2 mm                   | 1239AS-H-1R0M       | Toko                           |
| R1  | 1        | 1.50k   | RES, 1.50kΩ, 1%, 0.1W, 0603  | 0603                               | CRCW06031K50FKEA    | Vishay-Dale                    |
| R2  | 1        | 274k    | RES, 274kΩ, 1%, 0.1W, 0603   | 0603                               | CRCW0603274KFKEA    | Vishay-Dale                    |
| R3  | 1        | 13.0k   | RES, 13.0kΩ, 1%, 0.063W, 0402  | 0402                               | CRCW040213K0FKED    | Vishay-Dale                    |
| R4  | 1        | 100k    | RES, 100kΩ, 1%, 0.1W, 0603   | 0603                               | RC0603FR-07100KL    | Yageo America                  |
| R5, R13                                   | 2        | 18.2k   | RES, 18.2kΩ, 1%, 0.063W, 0402  | 0402                               | CRCW040218K2FKED    | Vishay-Dale                    |
| R6  | 1        | 249     | RES, 249 Ω, 1%, 0.1W, 0603   | 0603                               | CRCW0603249RFKEA    | Vishay-Dale                    |
| R7  | 1        | 267     | RES, 267 Ω, 1%, 0.1W, 0603   | 0603                               | CRCW0603267RFKEA    | Vishay-Dale                    |
| R9, R10, R12                              | 3        | 10.0k   | RES, 10.0kΩ, 1%, 0.063W, 0402  | 0402                               | CRCW040210K0FKED    | Vishay-Dale                    |
| R11                                       | 1        | 1.8Meg  | RES, 1.8MΩ, 5%, 0.063W, 0402   | 0402                               | CRCW04021M80JNED    | Vishay-Dale                    |
| R14                                       | 1        | 1.00k   | RES, 1.00kΩ, 1%, 0.063W, 0402  | 0402                               | CRCW04021K00FKED    | Vishay-Dale                    |
| R15                                       | 1        | 0.01    | RES, 0.01 Ω, 1%, 0.25W, 0805   | 0805                               | WSL0805R0100FEA18   | Vishay-Dale                    |
| U1  | 1        |         | Battery Management Unit Impedance Track™ Fuel<br>Gauge for bq2425x Charger, YZF0015BFAN  | YZF0015BFAN                        | bq27532YZFR-G1      | Texas Instruments              |

#### Table 2. Bill of Materials

10 bq27532EVM with bq27532 Battery Management Unit Impedance Track™ Fuel Gauge and bq24250 2-A, Switch-Mode Battery Charger for Single-Cell Applications



### Table 2. Bill of Materials (continued)

| Designator   | Quantity | Value | Description  | Package Reference | Part Number      | Manufacturer      |
|--|----------|-------|--|-------------------|------------------|-------------------|
| U2   | 1        |       | 2.0A Single Input I <sup>2</sup> C/Standalone Switch-Mode Li-Ion<br>Battery Charger with Power-Path Management,<br>YFF0030ABAB | YFF0030ABAB       | BQ24250YFF       | Texas Instruments |
| D2   | 0        | Green | LED, Green, SMD  | 1.6x0.8x0.8mm     | LTST-C190GKT     | Lite-On           |
| R8   | 0        | 1.50k | RES, 1.50kΩ, 1%, 0.1W, 0603  | 0603              | CRCW06031K50FKEA | Vishay-Dale       |
| TP1, TP2, TP3, TP4,<br>TP5, TP6, TP7, TP8,<br>TP9, TP10, TP11,<br>TP12, TP13, TP14,<br>TP15, TP16, TP17,<br>TP18 | 0        |       | TEST POINT pad Nothing to buy or mount.  |                   |                  |                   |

### 4 EVM Hardware and Software Setup

This section describes how to connect the different components of the EVM how to install the bq27532EVM PC software and.

### 4.1 Software System Requirements

The bq27532EVSW software requires 32-bit versions of Windows 2000 or Windows XP. Drivers for Windows 98SE are provided, but Microsoft<sup>™</sup> no longer supports Windows 98; therefore, Windows 98 can have issues with USB driver support. The EV2300 USB drivers have been tested for Windows 98SE, but no assurance is made for problem-free operation with specific system configurations.

# 4.2 Software Installation

Find the latest software version in the bq27532 tool folder on <u>power.ti.com</u>. Make a search by part number for bq27532 to access the tool folder.

# 4.2.1 Using EV2300

Use the following steps to install the bq27532EVSW software when using EV2300:

- 1. Ensure that the EV2300 is not connected to the PC through a USB cable before starting this procedure.
- 2. Browse for the supported software link within the bq27532 TI web site product folder to find the downloadable evaluation software (EVSW) installation files.
- 3. Open the software file that was downloaded from the TI web site.
- 4. Follow the instructions on screen until the software installation is completed.
- 5. Before starting the EVSW, connect the EV2300 to the computer using the USB cable.
- 6. Wait until the system prompt "new hardware found" appears. Choose "select location manually", and use the "browse" button to point to subdirectory TIUSBWin2K-XP-1.
- 7. Answer "continue" to the warning that drivers are not certified with Microsoft<sup>™</sup>.
- After installation finishes, another system prompt "new hardware found" appears. Repeat steps 6 and 7, but specify the directory as TIUSBWin2K-XP-2.
- 9. Answer "continue" to the warning that drivers are not certified with Microsoft. Driver installation is now finished.

# 4.2.2 Using EV2400:

12

- 1. Ensure that the EV2400 is not connected to the PC through a USB cable before starting this procedure.
- 2. Browse for the supported software link within the bq27532 TI web site product folder to find the downloadable EVSW installation files.
- 3. Open the software file that was downloaded from the TI web site.
- 4. Follow the instructions on screen until the software installation is completed.

# 5 Troubleshooting Unexpected Dialog Boxes

The user that is downloading the files must be logged in as the administrator.

The driver is not signed, so the administrator must allow installation of unsigned drivers in the operating system policy.



#### 6 Operation

This section details the operation of the bq27532 EVSW software.

#### 6.1 Starting the Program

Run bqStudio from the Start | All Programs | Texas Instruments | Battery Management Studio. The main screen (Figure 7) appears. Data begins to appear once the <Refresh> (single-time scan) button is clicked, or when the Scan button is clicked. To disable the scan feature, simply click the *Scan* button again.

The continuous scanning period can be set by opening Window | Preferences  $\rightarrow$  Registers section. The range for this interval is 0 ms to 65,535 ms. Only items that are selected for scanning are scanned within this period.

Battery Management Studio provides a logging function which logs the values that were last scanned. To enable this function, select the *Start Log* button; this causes the *Scan* button to be pressed. When logging is *Stopped*, the *Scan* button will still be selected and has to be manually clicked again.

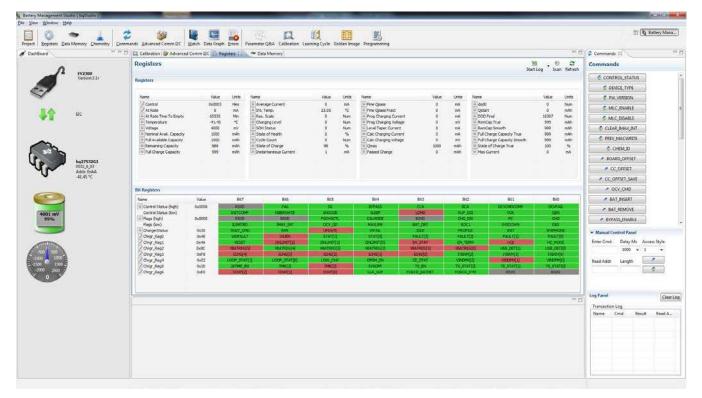


Figure 7. DataRAM Screen

This screen (Figure 7) shows the RAM data in the bq27532 device. Additional Flag and Status data can be viewed at the bottom of the Registers screen.

### 6.2 Setting Programmable bq27532 Options

The bq27532 data memory comes configured per the default settings detailed in the bq27532 technical reference manual (<u>SLUUB04</u>). Ensure that the settings are correctly changed to match the pack and application for the bq27532 solution being evaluated.

**IMPORTANT:** The correct setting of these options is essential to get the best performance.

The settings can be configured using the Data Memory screen (Figure 8).



Calibrate Screen

| Evzao<br>Vesion31:  | gaten 🕋 Data N<br>a Memory   | annuy control                     |               |              |              | Fib          | n/Search     |                | 7010               | Commands 12                                      | 2      |
|---|--|-----------------------------------|---------------|--------------|--------------|--------------|--------------|----------------|--------------------|--|--------|
| Evizion31r Read/  |  |                                   |               |              |              |              |              |                |                    |  |        |
| Version3.1: Read/   | Alleite Data Mare  |                                   |               |              |              |              | Auto Export  | Esport largort | Write All Read All | Commands   |        |
|   | Read/Write Data Me   | timory Contents                   |               |              |              |              |              |                | CONTROL STATUS     |  |        |
|   | Concernant of the  | Name                              | Value         | Unit         | Sobcless ID  | Data Length  | Block Number | Block Offset   | Native Units *     | CEVICE_TYPE                                      |        |
|   | onfiguration   | Charge Termination                | Tarios.       | - unit       |              | - and congen | Stock Humble | and an annual  | THOUTE GIVE        | FW_VERSION                                       |        |
| 3   | stem Data  | Charging Voltage                  | 4200          | mit          | 0,24         | 2            | Ū            | 0              | ntV                | MLC ENABLE                                       |        |
| Le uc Ga  | ias Gauging  | Taper Current                     | 100           | mA<br>mV     | 0x24<br>0x24 |              | 0            | 2              | mA                 |  |        |
|   | Adaption and a second sec | Taper Voltage<br>TCA Set %        | 100           | nv<br>N      | 0x24         |              | 0            | 6              | mV .               | MLC DISABLE                                      |        |
| 105   | OCY Table  | TCA Clear %                       | 95            | - 2          | 0x24         | î            | ő            | 10             | - C                | CLEAR BMAX INT                                   |        |
| Est.  | Ra Table   | FC Clear %                        | 98            |              | 0x24         | 1            | 0            | 11             | 5                  | PREV MACWRITE                                    |        |
| <b>~</b>  | Calibration  | FC Clear Volt                     | 0             | w/v          | 0x24         | 2            | 0            | 12             | enV/               | Name of Street and Date of Street and Street and |        |
|   | Caloranon  | DODatEOC Delta T                  | 5.0           | °C           | Dx24         | 2            | Û            | 14             | °C                 | CHEM_3D  |        |
| bq2753261   | Security   | Data     CC Threshold             | 900           | mAh          | 0x10         | 2            | Ð            | 0              | milh               | BOARD_OFFSET                                     |        |
| 0532.0.03   | Charger.   | Design Capacity                   | 1000          | mah          | 0x30         | 2            | 0            | 1              | mAh                | CC_OFFSET  |        |
| Addr: 0xAA  |  | Des Energy Scale                  | 1             | Nam          | 0x30         | 1            | 0            | 5              | Num =              |  |        |
| -41.45 °C   |  | SOH Load                          | -400          | mA           | 0x30         | 2            | 0            | 6              | mA                 | CC_OFFSET_SAVE                                   |        |
|   |  | Default Temperature               | 25.0          | °C.          | 0x30         | 2            | Q            | 8              | °C                 | OCV_CMD  |        |
|   |  | Device Name                       | 6q27532       | String       | 0,30         | 8            | 0            | 10<br>18       | String             |  |        |
|   |  | Data Rash Version<br># Discharge  | 0000          | Hei          | 0:30         | 2            | 072          | 18             | Hex                | BAT_INSERT                                       |        |
| And the second se |  | SOC1 Set Threshold                | 150           | mAh          | 0.31         | 2            | 0            | 0              | mAh                | # BAT_REMOVE                                     |        |
| 99%   |  | SQC1 Clear Threshold              | 175           | mAb          | 0,01         | 2            | 0            | 2              | mAb                | # BYPASS_ENABLE                                  |        |
|   |  | Final Voltage                     | 3100          | mV           | 0x31         | 1            | 0            | 34             | mV.                |  |        |
|   |  | Final Volt Time                   | 2             | Sec          | 0x31         | 1            | 0            | 6              | Sec                | * Manual Control Panel                           |        |
|   |  | Def Avg I Last Run                | -299<br>-1131 | mà           | 0.31         | 3            | 0            | 9              | mA                 | Enter Crind Delay Ms. Acces                      | . Side |
|   |  | Def Avg P Last Rom<br># Registers | -1194         | mar          | 0x31         | ×            | Ó            | -11            | me                 | 1000 - 1   | 100    |
| Silling in  |  | Op Config                         | 1971          | Flag         | 0x40         | 2            | Ó            |                | Flag               |  | _      |
|   |  | SOC Delta                         | 1             | 1            | 0x40         | 1            | 0            | 2              |                    | Read Adds Length                                 |        |
| A 700 7   |  | ize Timeout                       | 4             | Non          | 0x40         | 1            | 0            | 3              | Num                |  | 6      |
| 0   |  | OpConfig 8                        | 41            | Flag         | 0x40         | T            | 0            | 4              | flag               |  |        |
|   |  | OpConfig C<br>OpConfig D          | 0c<br>d3      | Flag<br>Flag | 0x40<br>0x40 | 1            | 0            | 5              | Flag<br>Flag +     |  |        |
|   |  | circuity o                        |               | 1.04         | 1111         |              | 1            | 1000           | 100                | Log Panel  |        |
|   |  |                                   |               |              |              |              |              |                | 20                 |  | Clea   |
|   |  |                                   |               |              |              |              |              |                |                    | Transaction Log                                  |        |
|   |  |                                   |               |              |              |              |              |                |                    | Nerve Crnd Result                                | Read A |

Figure 8. Data Memory Screen

To read all the data from the bq27532 data flash, click on menu option | Data Memory | Read All |.

To write to a data flash location, click on the desired location, enter the data and press <Enter>, which writes the entire tab of flash data, or select menu option | Data Flash | Write All |. The data flash must be read before any writes are performed to avoid any incorrect data being written to the device.

The data-flash configuration can be saved to a file by clicking | Export | and entering a file name. A dataflash file also can be retrieved in this way, | Import |, and written to the bq27532 using the | Write All| button.

The module calibration data is also held in the bq27532 data flash.

Battery Management Studio allows for an automatic data-flash export function, similar to the Registers logging function. This feature, when selected via | Auto Export | button, exports Data Flash to a sequential series of files named as *FilenameNNNN.gg.csv* where N = a decimal number from 0 to 9.

The AutoExport interval is set under the | Window | Preferences  $\rightarrow$  Data Memory | window with a default value of 1800 seconds. There is also the ability to select Classic or Default style. Classic is the standard .gg file that can be opened with typical notepad editors. Default style is the gg.csv which is a comma delimited file.

When AutoExport is pressed, the Auto Export is in progress. The same button is used to turn on/off AutoExport. AutoExport is typically used for troubleshooting experiments, but is highly recommended for any experiment. The standard AutoExport interval is typically 30 minutes.

### 7 Calibrate Screen

To ensure proper calibration, perform the following steps. These steps may or may not be required, depending on the type of calibration being performed. Only one calibration item can be selected and calibrated at a time.

14



### 7.1 To Calibrate the bq27532 EVM

Calibrate each item one at a time in the order presented in this document. Select the types of calibration to be performed by selecting the corresponding checkbox (see Figure 9).

Enter the measured values for the types selected, if necessary.

Then press the *Calibrate Part as indicated below* button. After all calibration is complete, close the Calibrate subwindow. While the Calibrate subwindow is open, even in the background, the calibration routines are running in firmware. Close the subwindow to ensure that they are stopped before proceeding with configuration or testing.

### 7.2 CC Offset Calibration

This performs the internal calibration of the coulomb counter input offset. Press the *Calibrate Coulomb Counter* button.

### 7.3 Voltage Calibration

- Measure the voltage across Pack+ and Pack- with a calibrated meter.
- Type the voltage value in mV into Enter Actual Voltage .
- Measure the temperature for PACK.
- Type the temperature value into Enter Actual Temperature.
- Press the Calibrate Voltage and Temperature as indicated below button.

### 7.4 Board Offset Calibration

This performs the offset calibration for the current offset of the board. It is expected that no current is flowing through the sense resistor while performing this calibration step.

- Remove load and short PACK- to GND.
- Press the Calibration Board Offset button.

### 7.5 Pack Current Calibration

- Connect a load to GND and SYS that draws approximately 1 A, or connect a current source to GND and Pack–. Ensure that the Measured Current reported is negative, or else reverse the connections.
- Measure the current with a calibrated meter, and type the value into *Enter Actual Current* using (–) for current in discharge direction.
- Press the Calibrate Pack Current button.



Calibrate Screen

www.ti.com

| 🔹 🗢 🗼                      | Sommands Advanced Commit  | 🗊 🔯 Battery Mena                  |
|----------------------------|---|-----------------------------------|
|                            | <sup>™</sup> □ Q Repited Contract, Data Statement, Contracting Data Statement, Contracting Cycle, Social Statement, Contracting Programming | T D & Commands 18                 |
|                            | Calibration   | Commands                          |
| EV2300<br>Version3.1r      | Perform Calibration   | CONTROL STATUS                    |
| A                          | Select the type of calibration to perform and enter the actual input parameters.  | DEVICE, TYPE                      |
|                            |   | PW VERSION                        |
|                            | CC Offset   | C MLC_ENABLE                      |
| 12C                        |   | © MLC_DISABLE                     |
|                            | Board Offset  | CLEAR, MAX, BIT                   |
|                            | Caldrate Board Offset   | PREV_MACWRITE                     |
|                            | Temperature   | © CHEM JD                         |
| be2753261                  | Applied Temperature 🖉 Internal Temp   | # BOARD_OFFSET                    |
|                            | deg C 🗇 Calibrate Jerrgenature 👘 Estamal Terrge   |                                   |
|                            | Current   | ◆ CC_OFFSET_SAVE                  |
|                            | Applied Current   | ≠ QCV,CMD                         |
|                            | mä 🔟 Calibrate Current  | RAT_BASERT                        |
|                            |   | # BAT_REMOVE                      |
| 4000 mV<br>99%             | Voltage<br>Applied Cell Voltage   |                                   |
|                            | mV 📳 Calibrata Ledtage.   | - Manual Control Panel            |
|                            | Calibrate Gas Gauge   | Enter Crind Delay Ms Access Style |
| 300 300                    |   | 1000 • 1 •                        |
| 2000 - 1000<br>XX - 1500 - |   | Read Addr Length                  |
| 2000 2000                  |   | 8                                 |
|                            |   |                                   |
|                            |   | + Log Panel Clear                 |
|                            |   | Transaction Log                   |
|                            |   | Name Crist Read A.                |
|                            |   |                                   |
|                            |   |                                   |
|                            |   |                                   |
|                            |   |                                   |
|                            |   |                                   |

Figure 9. Calibration Screen



### 8 Advanced Communication I2C

### I<sup>2</sup>C Communication

The read-write operations of the I2C Pro function is not specific to any gas gauge. These operations serve as general-purpose communication tools (Figure 10).

| hllowid                           | 🕫 💷 🖸 👰 Registers 🗢 Data Memory 🛄 Calibration 🎯 Advanced Comm RC 😳  | 17 🗂 😫 Commands 12                 |
|-----------------------------------|---|------------------------------------|
|                                   | Advanced Comm 12C   | Cherlog Sevelog Calculator         |
| evzice<br>Version:3.1r            | DC Matter Control Panel   | CONTROL STATUS                     |
|                                   | Byte Read/Write   | & DUVICE, TYPE                     |
|                                   | 12C Address AA  | E FW WERSON                        |
|                                   | Start Register 34   | MIC ENABLE                         |
| 📲 🛛 🖉                             | China | MLC DISABLE                        |
| Je uc                             | Bytes to Write 41.00 - Write  | CLEAR BAAX INT                     |
|                                   |   | PREV, MACWRITE                     |
| 2                                 | Number of Bytes to Read 32 decimal value Read   | CHEM,ID                            |
|                                   | Transaction Log   | · DOARD_OFFSET                     |
| 632.0,0<br>Addr. 0,0A<br>-4LAS 'C | Timckamp Rd/Wr Adds., Regs., Length Data  | CC_OFFSET                          |
| 4L45 °C                           |   | <ul> <li>CC_OFFSET_SAVE</li> </ul> |
|                                   |   | OCV_OMD                            |
|                                   |   | # BAT, INSERT                      |
|                                   |   | # BAT, REMOVE                      |
| 4001 mV<br>99%                    |   | EVPASS, ENABLE                     |
|                                   |   | · Manual Control Panel             |
|                                   |   | Enter Crind Delay Ms. Access Style |
|                                   |   | 1000 - 1 -                         |
| uo 1000 -                         |   | Read Addy Length                   |
| 00 1900-2<br>7000 2006 2          |   | 2                                  |
| 0 11                              |   |                                    |
|                                   |   |                                    |
|                                   |   | T C Log Panel                      |
|                                   |   | Transaction Log                    |
|                                   |   | Name Cmd Result Read               |
|                                   |   |                                    |
|                                   |   |                                    |
|                                   |   |                                    |

Figure 10. I<sup>2</sup>C Pro Screen



| Do you want to us SOC investing?<br>The ull make of duals the SMOOTHED bit of the Operation Configuration D register is data flanh.   | Versonald  | Parameter (QAA Section<br>This play in will globe you to properly configure the gauge parameters through a series of questions.   |             | CONTROL STATUS   |
|---|--|---|-------------|--|
| Version 3.0*       The place in mill points:       Implicit point of place in particip provide configuration the particip provide configuration the place place in the diagenergy of the mail to the place in the place in the diagenergy of the mail to the diagenergy of the diagener   | Versonald  | This plug-in will guide you to properly configure the gauge parameters through a suries of questions.   |             | C DEVACE_TYPE  |
| Image: Comparison of the Comparison   | ₽<br>₽₽ ac   |   |             | A second se   |
| Image: Comparison of the state between the state in  | ₽<br>₽<br>₽<br>₽   |   |             | An a summary and a start of the Part of the Control |
| If          If          If         If         If          If         If          If          If </td <td><b>U</b>C EC</td> <td></td> <td></td> <td># FW_VERSION</td>   | <b>U</b> C EC  |   |             | # FW_VERSION   |
| Image: Specific S   | ec 🖓   |   |             | & MLC ENABLE   |
| Image: Space of the space  |  |   |             | & MLC OBABLE   |
| Approximation       By own Checkenstration       General Spectration       The second sp  |  |   |             | CLEAR MAX INT  |
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| ALLSY       Dimensional metalence include producting 2011.       Dimensional metalence include producting 2011.       Dimensional metalence include producting 2011.       Image: Constructional metalence inclone metalence inclone metalence include prod   | 0532.0.03  |   |             | CC_OFFSET  |
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| At and a specify should not block like tool. It like tool at the hole consolid the SOCI bit in the Rugs register.<br>At and a specify should the SOCI like model generation of the file of the societ in the Societ bit in the Rugs register.<br>At and a specify should the SOCI like model generation of the file of the societ in the Societ bit in the Rugs register.<br>At and a specify should the SOCI like model generation of the file of the societ in the Societ bit in the Rugs register.<br>At and a specify should the SOCI like model generation of the file of the societ in the Societ bit is the Societ bit in the Rugs register.<br>Do you want to societ the societ of the societ of the Societ bit is SOCI bit in the Rugs register.<br>Do you want to societ the Societ of the Societ bit is SOCI bit in the Rugs register.<br>Do you want to societ the Societ bit is SOCI bit in the Rugs register.<br>Do you want to societ the Societ bit is SOCI bit in the Rugs register.<br>Do you want to societ the Societ bit is SOCI bit in the Rugs register.<br>Do you want to societ the Societ bit is Societ bit is SOCI bit in the Rugs register in data flats.<br>Do you want to socie the Societ bit is Societ bit is SOCI bit is the Rugs register in data flats.<br>Do want the Societ bit is |  | What is the level of current in mA that you would like to be used for the purposes of StateOHealth (SOH) simulations to recentor hattery degradation?                   | -400 mA     | # OCV_CMD  |
| The all style SQL for Department in data flash which control the SQL bit in the Play register.  |  |   |             | · BAT INSERT   |
| Proprovent to see the standard thermatic or the kernel temperature string.     Prove set to a set the standard temperature string.     Prove set to a so the set the standard temperature string.     Prove set to a so the standa                             |  | This will set the SOCI Set Threshold parameter in data flash which controls the SOCI bit in the Plago register.   | 150 mAn     | # BAT, REMOVE  |
| This will satisfy a data of person Configuration register in data flash.     Dependent to SDCOHED bit of the Operation Configuration D register in data flash.     Dependent to SDCOHED bit of the Operation Configuration D register in data flash.     Dependent to SDCOHED bit of the Operation Configuration D register in data flash.     Dependent to SDCOHED bit of the Operation Configuration D register in data flash.     Dependent to SDCOHED bit of the Operation Configuration D register in data flash.     Dependent to SDCOHED bit of the Operation Configuration D register in data flash.     Dependent to SDCOHED bit of the Operation Configuration D register in data flash.     Dependent to SDCOHED bit of the Operation Configuration D register in data flash.     Dependent to SDCOHED bit of the Operation Configuration D register in data flash.     Dependent to SDCOHED bit of the Operation Configuration D register in data flash.     Dependent to SDCOHED bit of the Operation Configuration D register in data flash.     Dependent to SDCOHED bit of the Operation Configuration D register in data flash.     Dependent to SDCOHED bit of the Operation Configuration D register in data flash.     Dependent to SDCOHED bit of the Operation Configuration D register in data flash.     Dependent to SDCOHED bit of the Operation Configuration D register in data flash.     Dependent to SDCOHED bit of the Operation Configuration D register in data flash.     Dependent to SDCOHED bit of the Operation Configuration D register in data flash.     Dependent to SDCOHED bit of the Operation Configuration D register in data flash.     Dependent to SDCOHED bit of the Operation Configuration D register in data flash.     Dependent to SDCOHED bit of the Operation Configuration D register in data flash.     Dependent to SDCOHED bit of the Operation Configuration D register in data flash.     Dependent to SDCOHED bit of the Operation Configuration D register in data flash.     Dependent to SDCOHED bit of the Operation Configuration D register in da                             | 4000 mV<br>99%   | At what capacity should the 5OCI Alarm be cleaned?<br>This will get the 5OCI Clear Threehold parameter in data flash.   | 175 mAh     | # BYPASS, ENABLE   |
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|   | 00 100 - 100 |   |             |  |
|   | 0  |   |             |  |
|   |  |   |             | -  |
| Transaction Log   |  | 6   |             | Log Panel Cie  |
|   |  |   |             | Transaction Log  |
| Name Cirid Fisual   |  |   |             | Name Crind Result Read A   |



| Output Location   | nory contents of the connected gauge and save it to your hard drive in various formats. |              |
|-------------------|---|--------------|
| Output Directory  | C:\ti\BatteryManagementStudio\OutputFiles   | Browse       |
| Base File Name    | 0532_0_03-bq27532G1   | Open Directo |
| Output Formats    |   |              |
| SREC File (.srec) | 0532_0_03-bq27532G1.srec  | Options      |
| ✓ BQFS File (.fs) | 0532_0_03-bq27532G1.bq.fs   | Options      |
| ✓ DFFS File (.fs) | 0532_0_03-bq27532G1.df.fs   | Options      |
|                   | Create Image Files  |              |

### Figure 12. Golden Image Output Screen

| Programming   |          |
|---|----------|
| Perform Programming   |          |
| This plug-in will allow you to program image files to a device.<br>Select Programmable File |          |
|   | ▼ Browse |
|   | Program  |

### Figure 13. Perform Programming Screen



### 9 Related Documentation From Texas Instruments

To obtain a copy of any of the following TI documents, call the Texas Instruments Literature Response Center at (800) 477-8924 or the Support Center at (512) 434-1560. When ordering, identify this document by its title and literature number. Updated documents also can be obtained through the TI Web site at <a href="http://www.ti.com">www.ti.com</a>.

• bq27532, System-Side Impedance Track<sup>™</sup> Fuel Gauge With Integrated LDO data sheet (SLUSBU6)

#### STANDARD TERMS AND CONDITIONS FOR EVALUATION MODULES

- 1. Delivery: TI delivers TI evaluation boards, kits, or modules, including any accompanying demonstration software, components, or documentation (collectively, an "EVM" or "EVMs") to the User ("User") in accordance with the terms and conditions set forth herein. Acceptance of the EVM is expressly subject to the following terms and conditions.
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  - 1.2 EVMs are not intended for consumer or household use. EVMs may not be sold, sublicensed, leased, rented, loaned, assigned, or otherwise distributed for commercial purposes by Users, in whole or in part, or used in any finished product or production system.
- 2 Limited Warranty and Related Remedies/Disclaimers:
  - 2.1 These terms and conditions do not apply to Software. The warranty, if any, for Software is covered in the applicable Software License Agreement.
  - 2.2 TI warrants that the TI EVM will conform to TI's published specifications for ninety (90) days after the date TI delivers such EVM to User. Notwithstanding the foregoing, TI shall not be liable for any defects that are caused by neglect, misuse or mistreatment by an entity other than TI, including improper installation or testing, or for any EVMs that have been altered or modified in any way by an entity other than TI. Moreover, TI shall not be liable for any defects that result from User's design, specifications or instructions for such EVMs. Testing and other quality control techniques are used to the extent TI deems necessary or as mandated by government requirements. TI does not test all parameters of each EVM.
  - 2.3 If any EVM fails to conform to the warranty set forth above, TI's sole liability shall be at its option to repair or replace such EVM, or credit User's account for such EVM. TI's liability under this warranty shall be limited to EVMs that are returned during the warranty period to the address designated by TI and that are determined by TI not to conform to such warranty. If TI elects to repair or replace such EVM, TI shall have a reasonable time to repair such EVM or provide replacements. Repaired EVMs shall be warranted for the remainder of the original warranty period. Replaced EVMs shall be warranted for a new full ninety (90) day warranty period.
- 3 Regulatory Notices:
  - 3.1 United States
    - 3.1.1 Notice applicable to EVMs not FCC-Approved:

This kit is designed to allow product developers to evaluate electronic components, circuitry, or software associated with the kit to determine whether to incorporate such items in a finished product and software developers to write software applications for use with the end product. This kit is not a finished product and when assembled may not be resold or otherwise marketed unless all required FCC equipment authorizations are first obtained. Operation is subject to the condition that this product not cause harmful interference to licensed radio stations and that this product accept harmful interference. Unless the assembled kit is designed to operate under part 15, part 18 or part 95 of this chapter, the operator of the kit must operate under the authority of an FCC license holder or must secure an experimental authorization under part 5 of this chapter.

3.1.2 For EVMs annotated as FCC – FEDERAL COMMUNICATIONS COMMISSION Part 15 Compliant:

#### CAUTION

This device complies with part 15 of the FCC Rules. Operation is subject to the following two conditions: (1) This device may not cause harmful interference, and (2) this device must accept any interference received, including interference that may cause undesired operation.

Changes or modifications not expressly approved by the party responsible for compliance could void the user's authority to operate the equipment.

#### FCC Interference Statement for Class A EVM devices

NOTE: This equipment has been tested and found to comply with the limits for a Class A digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instruction manual, may cause harmful interference to radio communications. Operation of this equipment in a residential area is likely to cause harmful interference in which case the user will be required to correct the interference at his own expense.

#### FCC Interference Statement for Class B EVM devices

NOTE: This equipment has been tested and found to comply with the limits for a Class B digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates, uses and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following measures:

- Reorient or relocate the receiving antenna.
- Increase the separation between the equipment and receiver.
- Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.
- Consult the dealer or an experienced radio/TV technician for help.

#### 3.2 Canada

3.2.1 For EVMs issued with an Industry Canada Certificate of Conformance to RSS-210

#### **Concerning EVMs Including Radio Transmitters:**

This device complies with Industry Canada license-exempt RSS standard(s). Operation is subject to the following two conditions: (1) this device may not cause interference, and (2) this device must accept any interference, including interference that may cause undesired operation of the device.

#### Concernant les EVMs avec appareils radio:

Le présent appareil est conforme aux CNR d'Industrie Canada applicables aux appareils radio exempts de licence. L'exploitation est autorisée aux deux conditions suivantes: (1) l'appareil ne doit pas produire de brouillage, et (2) l'utilisateur de l'appareil doit accepter tout brouillage radioélectrique subi, même si le brouillage est susceptible d'en compromettre le fonctionnement.

#### **Concerning EVMs Including Detachable Antennas:**

Under Industry Canada regulations, this radio transmitter may only operate using an antenna of a type and maximum (or lesser) gain approved for the transmitter by Industry Canada. To reduce potential radio interference to other users, the antenna type and its gain should be so chosen that the equivalent isotropically radiated power (e.i.r.p.) is not more than that necessary for successful communication. This radio transmitter has been approved by Industry Canada to operate with the antenna types listed in the user guide with the maximum permissible gain and required antenna impedance for each antenna type indicated. Antenna types not included in this list, having a gain greater than the maximum gain indicated for that type, are strictly prohibited for use with this device.

#### Concernant les EVMs avec antennes détachables

Conformément à la réglementation d'Industrie Canada, le présent émetteur radio peut fonctionner avec une antenne d'un type et d'un gain maximal (ou inférieur) approuvé pour l'émetteur par Industrie Canada. Dans le but de réduire les risques de brouillage radioélectrique à l'intention des autres utilisateurs, il faut choisir le type d'antenne et son gain de sorte que la puissance isotrope rayonnée équivalente (p.i.r.e.) ne dépasse pas l'intensité nécessaire à l'établissement d'une communication satisfaisante. Le présent émetteur radio a été approuvé par Industrie Canada pour fonctionner avec les types d'antenne énumérés dans le manuel d'usage et ayant un gain admissible maximal et l'impédance requise pour chaque type d'antenne. Les types d'antenne non inclus dans cette liste, ou dont le gain est supérieur au gain maximal indiqué, sont strictement interdits pour l'exploitation de l'émetteur

#### 3.3 Japan

- 3.3.1 Notice for EVMs delivered in Japan: Please see <a href="http://www.tij.co.jp/lsds/ti\_ja/general/eStore/notice\_01.page">http://www.tij.co.jp/lsds/ti\_ja/general/eStore/notice\_01.page</a> 日本国内に 輸入される評価用キット、ボードについては、次のところをご覧ください。 http://www.tij.co.jp/lsds/ti\_ja/general/eStore/notice\_01.page
- 3.3.2 Notice for Users of EVMs Considered "Radio Frequency Products" in Japan: EVMs entering Japan are NOT certified by TI as conforming to Technical Regulations of Radio Law of Japan.

If User uses EVMs in Japan, User is required by Radio Law of Japan to follow the instructions below with respect to EVMs:

- 1. Use EVMs in a shielded room or any other test facility as defined in the notification #173 issued by Ministry of Internal Affairs and Communications on March 28, 2006, based on Sub-section 1.1 of Article 6 of the Ministry's Rule for Enforcement of Radio Law of Japan,
- 2. Use EVMs only after User obtains the license of Test Radio Station as provided in Radio Law of Japan with respect to EVMs, or
- 3. Use of EVMs only after User obtains the Technical Regulations Conformity Certification as provided in Radio Law of Japan with respect to EVMs. Also, do not transfer EVMs, unless User gives the same notice above to the transferee. Please note that if User does not follow the instructions above, User will be subject to penalties of Radio Law of Japan.

【無線電波を送信する製品の開発キットをお使いになる際の注意事項】

本開発キットは技術基準適合証明を受けておりません。

本製品のご使用に際しては、電波法遵守のため、以下のいずれかの措置を取っていただく必要がありますのでご注意ください。

- 1. 電波法施行規則第6条第1項第1号に基づく平成18年3月28日総務省告示第173号で定められた電波暗室等の試験設備でご使用 いただく。
- 2. 実験局の免許を取得後ご使用いただく。
- 3. 技術基準適合証明を取得後ご使用いただく。
- なお、本製品は、上記の「ご使用にあたっての注意」を譲渡先、移転先に通知しない限り、譲渡、移転できないものとします。 上記を遵守頂けない場合は、電波法の罰則が適用される可能性があることをご留意ください。

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- 3.3.3 Notice for EVMs for Power Line Communication: Please see <a href="http://www.tij.co.jp/lsds/ti\_ja/general/eStore/notice\_02.page">http://www.tij.co.jp/lsds/ti\_ja/general/eStore/notice\_02.page</a> 電力線搬送波通信についての開発キットをお使いになる際の注意事項については、次のところをご覧ください。http://www.tij.co.jp/lsds/ti\_ja/general/eStore/notice\_02.page
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