

# Using the TPS2492/3EVM Hot Swap Controller

## User's Guide



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# **Using the TPS2492/3 Hot Swap Controller**

This User's Guide describes the setup and operation of the TPS2492/3 System Test Board.

## **1 Introduction**

This User's Guide describes the features of the TPS2492/3EVM. The TPS2492/3 schematic, layout and List of Materials are provided.

## **2 Description**

The EVM is a 9-V to 80-V module using the TPS2492/3 hot swap controller with external MOSFET. At power on, the output is power limited to control inrush current and protect the MOSFET. On an over-current condition, the controller interrupts power to the load at high speed and signals load status. Operating current, MOSFET power dissipation, and fault timer settings are hardware programmable.

### **2.1 Applications**

- Server
  - Plug-In Circuit Boards
  - RAID / Disk Drive
- Telecom
  - ATCA
  - Micro-ATCA
- General Hot Plug

### **2.2 Features**

- 9-V to 80-V Operation
- Controlled Inrush Current
- Fast Circuit Breaker Control
- Hardware Programmable
  - MOSFET Power Limit
  - Fault Current
  - Fault Timer to Avoid Nuisance Tripping
  - TPS2492 Latch Off or TPS2493 Retry
- LED Status Display
  - FAULT
  - POWER GOOD
- Slide Switch Controlling the ENABLE Signal.
- 25-A Nominal Operation with Thermal Characteristics for Proper MOSFET Operation
- On-Board transorb for Over-Voltage Input Protection
- Common Output Diode Prevents Negative Spiking if the Load is Removed While Powered On.
- IMON Output Signal Voltage Representing Load Current.
- Test Points for Circuit Operation Monitoring

### **2.3 Equipment supplied**

- EVM-491
- The datasheet and this users guide is available on the TPS2492/3 product page at [www.ti.com](http://www.ti.com).

### **2.4 Equipment Required**

- Power Supply is able to output twice the steady state current for pulse duration. For the 12-V factory set EVM, steady state power is 25-A nominal.
- Oscilloscope
- Current Probe

### **2.5 EVM Documentation**

The manufacturing and engineering documentation for the EVM is shown in sections 3 to 6. The EVM is orderable in 12-V or 48-V configurations, latched off or retry on fault.

### **2.6 EVM Configuration**

Components may be changed for operation at voltage or current not configured as supplied. The List of Materials, , shows the parts to change to configure the EVM to 48 V at 5-A operation. Configure the circuit board for other operating current to 50 A maximum. Changing the sense resistor, the power limit programming components, and possibly the number of MOSFETs as required.

Use the List of Material as a guide when changing module configurations. The TPS2492/93 datasheet has design example describing how to select parts based on specifications. A calculator tool is also available from the product page at [www.ti.com](http://www.ti.com).

### **2.7 Other EVM Options**

There are EVM options not described in the other TPS2492/3 documentation. These are described in this section. They involve the not populated components of the EVM.

#### **2.7.1 Trip Point Trim Resistors**

R15 and R16 are added to fine tune the sense current when the required current cannot be achieved with standard sense resistors. These resistors form a divider across the sense main sense resistor to increase the trip point from the main sense setting. These are low power rated components. They should be kept below 100  $\Omega$  to minimize the set point error.

For example, to select 30 A nominal,

- Bracket the setpoint. A 0.001- $\Omega$  sense resistor selects 50 A and a 0.002  $\Omega$  selects 25 A.
- Use the larger value sense resistor. In this case 0.002  $\Omega$ .
- At 30 A, the voltage across the 0.002- $\Omega$  sense resistor is 60 mV.
- R15 and R16 make a standard voltage divider. Use the formula  $(60 \text{ mV} \times R16) / (R15 + R16) = 50 \text{ mV}$ .
- Let  $R16 = 33 \Omega$ , solve for  $R15$ . In this case,  $R15 = 6.6 \Omega$ , use standard value  $R = 6.55 \Omega$ .

### 2.7.2 dv/dt Control

When the VPROG calculates below the 0.4 V minimum, one option is to use dv/dt control to start the uncharged output capacitor. In this way, the power limiting is not used on power up. R11 and C4, (see schematic [Figure 2](#) for component reference), are normally not populated and control the power turn on with a dv/dt soft start. Recommendation for R11 is at 1 k $\Omega$ . C4 can be calculated by knowing the output capacitor, selecting the charge current and calculating the start time. Calculation is done by the capacitor V I relationship equation,  $I = C \frac{dV}{dt}$ .

- Assume a 12-V system with a 1000- $\mu$ F output capacitor. Select the charge at 0.5 A. Use the formula to solve for time.  $T = CV/I$ ,  $T = 24$  ms.
- Since the output FET is a gate follower, the time calculated applies to the gate rise time. Use the same formula to solve for  $C_t$  but this time the current is the gate sourcing current in the datasheet specifications section, 22  $\mu$ A nominal.
- The voltage, again from the datasheet is the output  $V_{GS}$ , 14 V.
- $C_t = IT / V = 22 \mu\text{A} \times 24 \text{ ms} / 14 = 37.7 \text{ nF}$ , use standard value 39 nF.

### 2.7.3 Disable Power Limit

It's not usually done, but sometimes because of high voltage with high current the power limit may be disabled on over load. TPS2492 can be configured to run in current limit only. To disable power limit remove R3 and install 47 k $\Omega$  in R2, (see schematic [Figure 2](#) for component reference). With power limit disabled, dv/dt start outlined in the previous section would be used for power on.

## 3 EVM Block Diagram TPS2492EVM-491

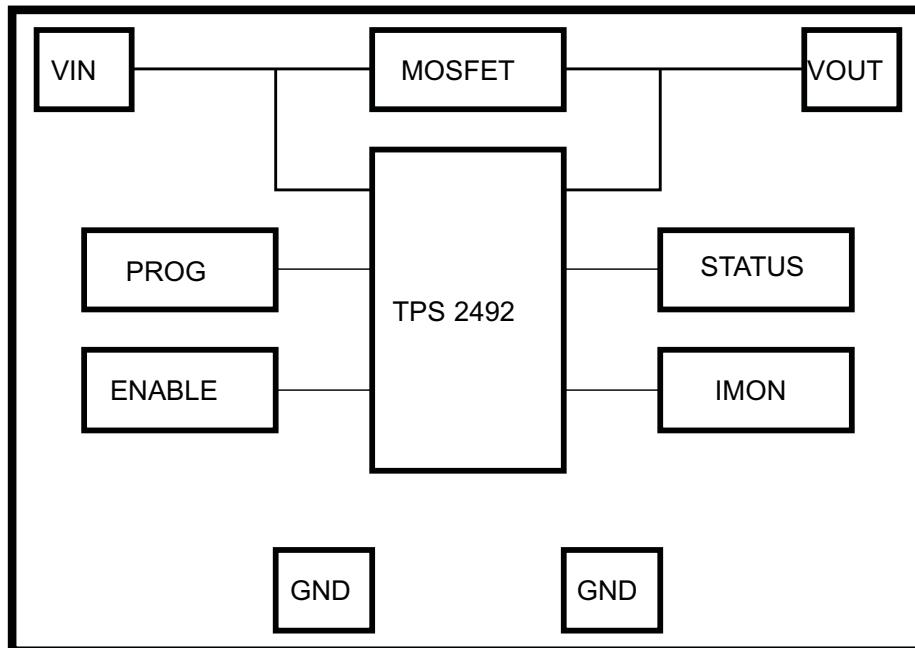
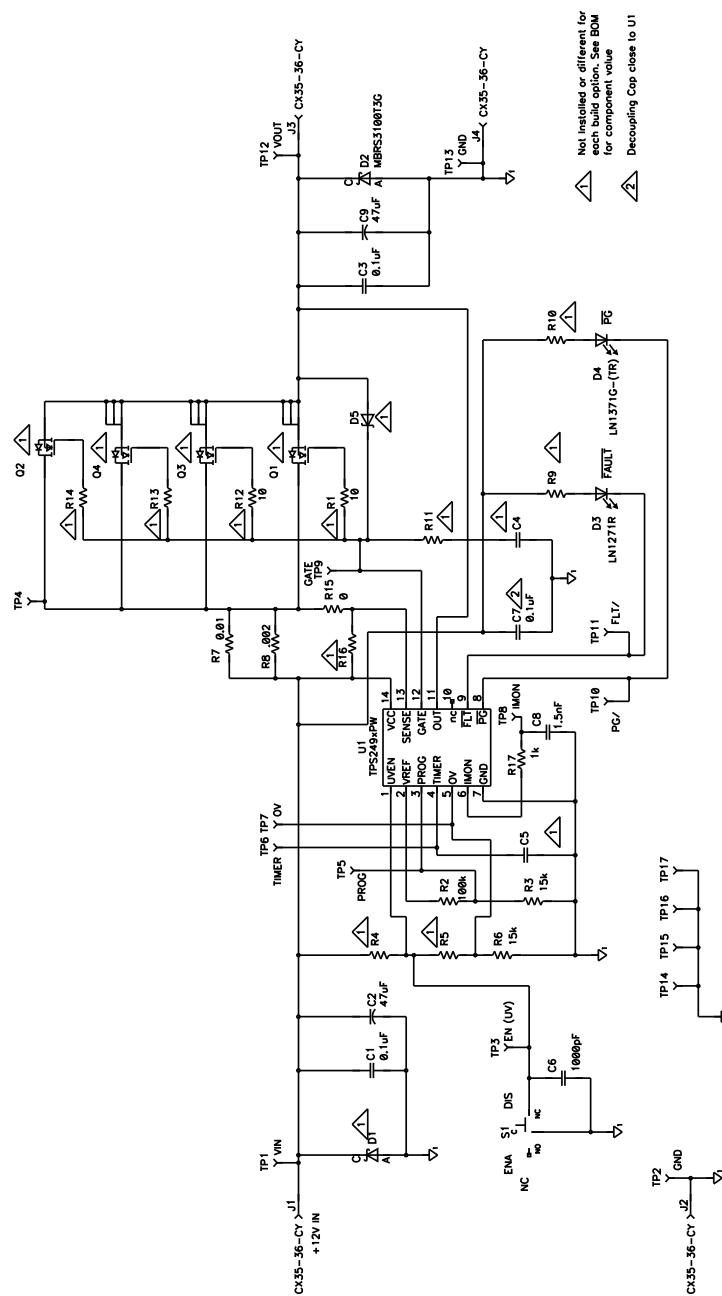


Figure 1. EVM Block Diagram

## 4 Schematic



**Figure 2. HPA270A Schematic, TPS2420EVM**

## 5 Test Points

**Table 1. TPS249xEVM Test Points**

TEST POINT	NAME	DESCRIPTION
TP1	VIN	Input voltage power supply
TP2	GND	Ground
TP3	EN-UV	Enable signal, high true (and under-voltage detection)
TP4	None	
TP5	PROG	Program input
TP6	CT	Fault timer capacitor
TP7	OV	Over-voltage detection
TP8	IMON	Output current monitor
TP9	GATE	Gate output
TP10	PG	Power good signal, low true
TP11	FLT	Fault signal, low true
TP12	VOUT	Output voltage
TP13	GND	Scope ground test point
TP14	GND	Scope ground test point
TP15	GND	Scope ground test point
TP16	GND	Scope ground test point
TP17	GND	Scope ground test point
TP18	SENSE	Low-side sense resistor

## 6 Connectors

**Table 2. Connectors**

CONNECTOR	DESCRIPTION
J1	Main power input, VIN
J3	Main power ground, GND
J2	Load
J4	Load ground, GND

## 7 Jumpers

None.

## 8 Switches

### 8.1 Enable Switch, S1

The Enable slide switch turns on the hot plug controller to ramp the output voltage .When the switch is off, the output is off.

## 9 Indicators

- Green LED indicator for Power Good (PG)
- Red LED indicator for FAULT (FLT)

## 10 Board Outline

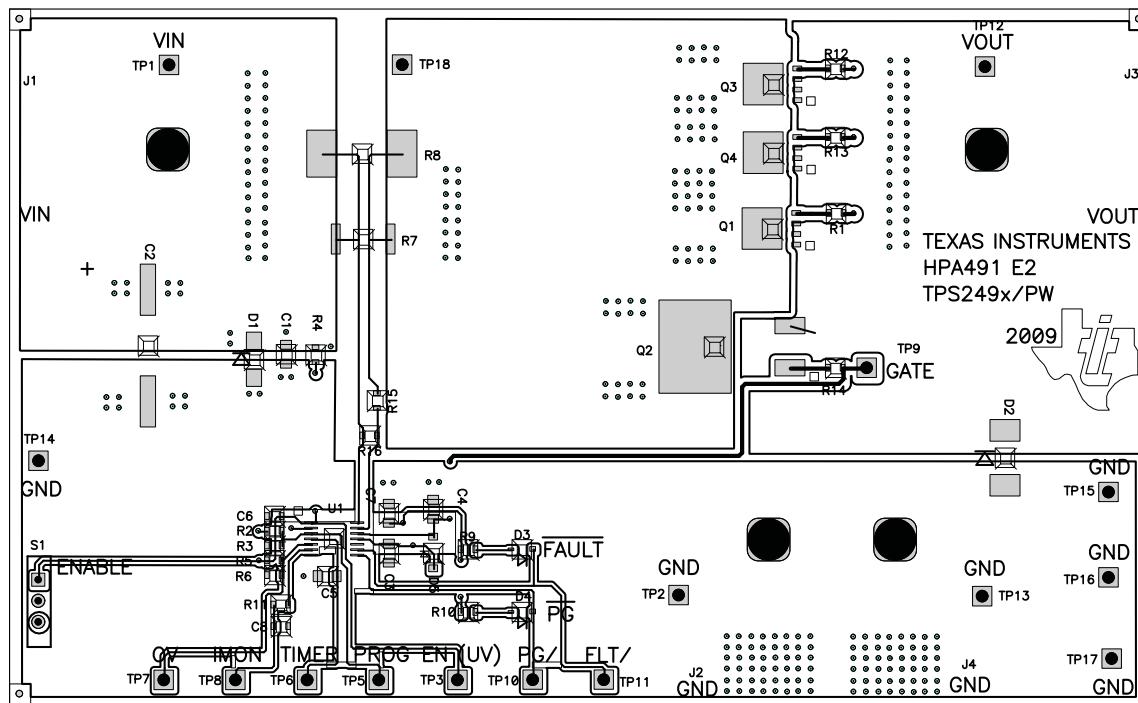


Figure 3. Board Outline (top view)

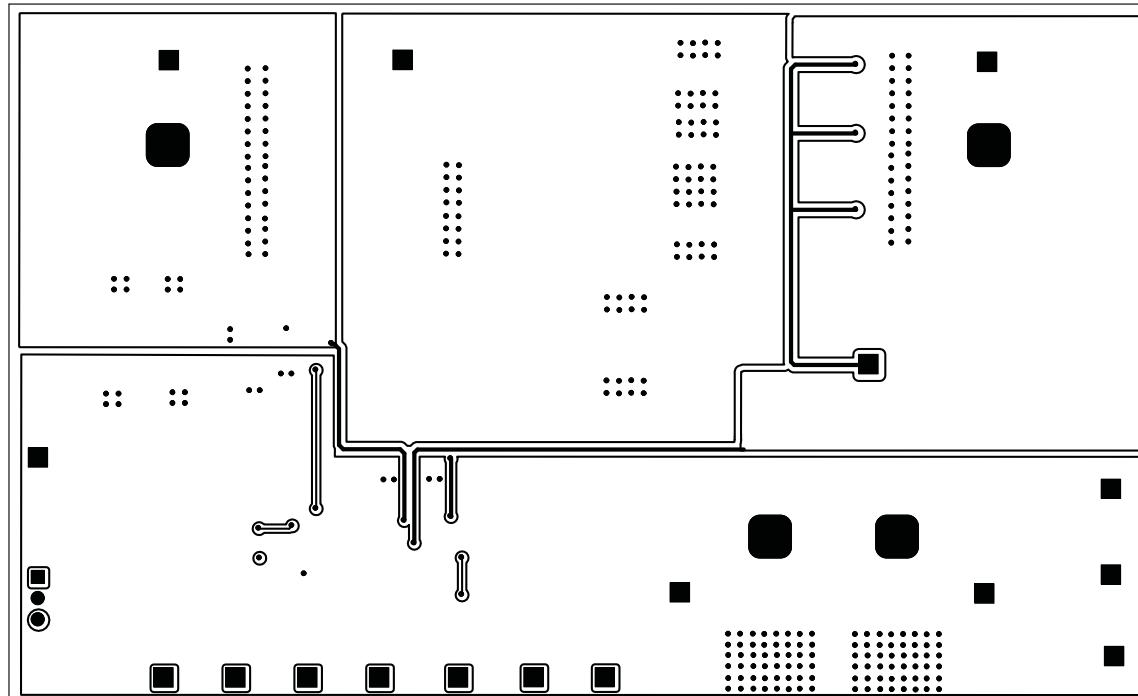


Figure 4. Board Outline (bottom view)

## 11 List of Materials

### 11.1 12-V TPS2492EVM-001/TPS2493EVM-002

This is the List of Material for the 12-V TPS2492EVM-001/TPS2493EVM-002.

**Table 3. 12-V TPS2492EVM-001/TPS2493EVM-002<sup>(1)(2)(3)(4)</sup>**

-001	-002	REF DES	DESCRIPTION	PART NUMBER	MFR
3	3	C1, C3, C7	Capacitor, ceramic, 100 V, X7R, 10%, 0.1 $\mu$ F, 0805	Std	Std
1	1	C5	Capacitor, ceramic, 25 V, X7R, 10%, 3.3 nF, 0805	Std	Std
2	2	C2, C9	Capacitor, aluminum, 100 VDC, $\pm$ 20%, 47 $\mu$ F, 0.670 inch x 0.750 inch	EEVFK2A4 70Q	Panasonic
1	1	C6	Capacitor, ceramic, 25 V, X7R, 10%, 1000 pF, 0603	Std	Std
1	1	C8	Capacitor, ceramic, 25 V, X7R, 10%, 1.5 nF, 0603	Std	Std
1	1	D1	Diode, SMT TVS 400W, 1 A, 15 V, SMA	SMAJ15A-13F	Diodes
1	1	D2	Diode, Schottky 3 A, 100 V, SMC	MBRS3100 T3G	On Semi
1	1	D3	Diode, LED, red, 20 mA, 0.9 mcd, 0.068 inch x 0.049 inch	LN1271R	Panasonic
1	1	D4	Diode, LED, green, 20 mA, 0.9 mcd, 0.068 inch x 0.049 inch	LN1371G-(TR)	Panasonic
4	4	J1, J2, J3, J4	Lug, copper, 35 A, 0.380 inch x 1.020 inch	CX35-36-CY	Panduit
2	2	Q1, Q3	MOSFET, N-channel, 25 V, 37 A, 1.3 m $\Omega$ , QFN 5X6 mm	CSD16401 Q5A-R	Ciclon
2	2	R1, R12	Resistor, chip, 1/16 W, 1%, 10 $\Omega$ , 0603	Std	Std
1	1	R15	Resistor, chip, 1/16 W, 1%, 0 $\Omega$ , 0603	Std	Std
1	1	R17	Resistor, chip, 1/16 W, 1%, 1 k $\Omega$ , 0603	Std	Std
1	1	R2	Resistor, chip, 1/16 W, 1%, 100 k $\Omega$ , 0603	Std	Std
2	2	R3, R6	Resistor, chip, 1/16 W, 1%, 15 k $\Omega$ , 0603	Std	Std
1	1	R4	Resistor, chip, 1/16 W, 1%, 143 k $\Omega$ , 0603	Std	Std
1	1	R5	Resistor, chip, 1/16 W, 1%, 7.32 k $\Omega$ , 0603	Std	Std
1	1	R8	Resistor, power metal strip, 3 W, $\pm$ 1%, 0.002 $\Omega$ , 4527	WSR-32L000FE A	Vishay Dale
2	2	R9, R10	Resistor, chip, 1/16 W, 1%, 5.1 k $\Omega$ , 0603	Std	Std

<sup>(1)</sup> These assemblies are ESD sensitive, ESD precautions shall be observed.

<sup>(2)</sup> These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

<sup>(3)</sup> These assemblies must comply with workmanship standards IPC-A-610 Class 2.

<sup>(4)</sup> Ref designators marked with an asterisk ("\*") cannot be substituted. All other components can be substituted with equivalent MFG's components.

**Table 3. 12-V TPS2492EVM-001/TPS2493EVM-002<sup>(1)(2)(3)(4)</sup> (continued)**

<b>-001</b>	<b>-002</b>	<b>REF DES</b>	<b>DESCRIPTION</b>	<b>PART NUMBER</b>	<b>MFR</b>
1	1	S1	Switch, SPDT, slide, PC mount, 500 mA, 0.400 inch x 0.100 inch	09.03201.02	EAO
17		TP1, TP2, TP3, TP4, TP5, TP6, TP7, TP8, TP9, TP10, TP11, TP12, TP13, TP14, TP15, TP16, TP17	Test point, white, thru hole, 5012, 0.125 inch x 0.125 inch	5012	Keystone
1	0	U1	Positive High-Voltage Power-Limiting Hotswap Controller, TSSOP-14	TPS2492PW	TI
0	1	U1	Positive High-Voltage Power-Limiting Hotswap Controller, TSSOP-14	TPS2493PW	TI
4	4		Screw, panhead	#10-32	0.500 inch
4	4		Washer, flat	#10	
4	4		Washer, split	M5	
4	4		Nut, hex	#10-32	
1	1	--	PCB, 5.4 inch x 3.3 inch x 0.06 inch	HPA491	Any
4	4	--	Bumpers, cylindrical, black	SJ5514-0	3M

## 12 48-V TPS2492EVM-003/TPS2493EVM-004

This is the List of Material for the 48-V TPS2492EVM-003/TPS2493EVM-004.

**Table 4. 48-V TPS2492EVM-003/TPS2493EVM-004<sup>(1)(2)(3)(4)</sup>**

<b>-003</b>	<b>-004</b>	<b>REF DES</b>	<b>DESCRIPTION</b>	<b>PART NUMBER</b>	<b>MFR</b>
3	3	C1, C3, C7	Capacitor, ceramic, 100 V, X7R, 10%, 0.1 $\mu$ F, 0805	Std	Std
1	1	C5	Capacitor, ceramic, 25 V, X7R, 10%, 0.1 $\mu$ F, 0805	Std	Std
2	2	C2, C9	Capacitor, aluminum, 100 VDC, $\pm$ 20%, 47 $\mu$ F, 0.670 inch x 0.750 inch	EEVFK2A4-70Q	Panasonic
1	1	C6	Capacitor, ceramic, 25 V, X7R, 10%, 1000 pF, 0603	Std	Std
1	1	C8	Capacitor, ceramic, 25 V, X7R, 10%, 1.5 nF, 0603	Std	Std
1	1	D1	Diode, SMT TVS 400 W, 1 A, 70 V, SMA	SMAJ70A-13F	Diodes
1	1	D2	Diode, Schottky 3 A, 100 V, SMC	MBRS3100-T3G	On Semi
1	1	D3	Diode, LED, red, 20 mA, 0.9 mcd, 0.068 inch x 0.049 inch	LN1271R	Panasonic
1	1	D4	Diode, LED, green, 20 mA, 0.9 mcd, 0.068 inch x 0.049 inch	LN1371G-(TR)	Panasonic
4	4	J1, J2, J3, J4	Lug, copper, 35 A, 0.380 inch x 1.020 inch	CX35-36-CY	Panduit
1	1	Q2	Transistor, NFET, 100 V, 33 A, 44 m $\Omega$ , D2PAK	IRF540NS-PBF	IR
1	1	R14	Resistor, chip, 1/16 W, 1%, 10 $\Omega$ , 0603	Std	Std
1	1	R15	Resistor, chip, 1/16 W, 1%, 0 $\Omega$ , 0603	Std	Std
1	1	R17	Resistor, chip, 1/16 W, 1%, 1 k $\Omega$ , 0603	Std	Std
1	1	R2	Resistor, chip, 1/16 W, 1%, 100 k $\Omega$ , 0603	Std	Std
2	2	R3, R6	Resistor, chip, 1/16 W, 1%, 15 k $\Omega$ , 0603	Std	Std
1	1	R4	Resistor, chip, 1/16 W, 1%, 604 k $\Omega$ , 0603	Std	Std
1	1	R5	Resistor, chip, 1/16 W, 1%, 4.42 k $\Omega$ , 0603	Std	Std
1	1	R7	Resistor, metal strip, 0.01 $\Omega$ , 2 W, 1%, 0.01, 2512	LRC-LRF2512L-F-01-R010-F	IRC
2	2	R9, R10	Resistor, chip, 1/16 W, 1%, 20 k $\Omega$ , 0603	Std	Std

<sup>(1)</sup> These assemblies are ESD sensitive, ESD precautions shall be observed.

<sup>(2)</sup> These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

<sup>(3)</sup> These assemblies must comply with workmanship standards IPC-A-610 Class 2.

<sup>(4)</sup> Ref designators marked with an asterisk ("\*") cannot be substituted. All other components can be substituted with equivalent MFG's components.

**Table 4. 48-V TPS2492EVM-003/TPS2493EVM-004<sup>(1)(2)(3)(4)</sup> (continued)**

<b>-003</b>	<b>-004</b>	<b>REF DES</b>	<b>DESCRIPTION</b>	<b>PART NUMBER</b>	<b>MFR</b>
1	1	S1	Switch, SPDT, slide, PC mount, 500 mA, 0.400 inch x 0.100 inch	09.03201.02	EAO
		TP1, TP2, TP3, TP4, TP5, TP6, TP7, TP8, TP9, TP10, TP11, TP12, TP13, TP14, TP15, TP16, TP17	Test point, white, thru hole, 5012, 0.125 inch x 0.125 inch	5012	Keystone
1	0	U1	Positive High-Voltage Power-Limiting Hotswap Controller, TSSOP-14	TPS2492PW	TI
0	1	U1	Positive High-Voltage Power-Limiting Hotswap Controller, TSSOP-14	TPS2493PW	TI
4	4		Screw, panhead	#10-32	0.500 inch
4	4		Washer, flat	#10	
4	4		Washer, split	M5	
4	4		Nut, hex	#10-32	
1	1	--	PCB, 5.4 inch x 3.3 inch x 0.06 inch	HPA491	Any
4	4	--	Bumpers, cylindrical, black	SJ5514-0	3M

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Should this evaluation board/kit not meet the specifications indicated in the User's Guide, the board/kit may be returned within 30 days from the date of delivery for a full refund. **THE FOREGOING WARRANTY IS THE EXCLUSIVE WARRANTY MADE BY SELLER TO BUYER AND IS IN LIEU OF ALL OTHER WARRANTIES, EXPRESSED, IMPLIED, OR STATUTORY, INCLUDING ANY WARRANTY OF MERCHANTABILITY OR FITNESS FOR ANY PARTICULAR PURPOSE.**

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## EVM Warnings and Restrictions

It is important to operate this EVM within the input voltage range of 9 V to 80 V and the output voltage range of 9 V to 80 V .

Exceeding the specified input range may cause unexpected operation and/or irreversible damage to the EVM. If there are questions concerning the input range, please contact a TI field representative prior to connecting the input power.

Applying loads outside of the specified output range may result in unintended operation and/or possible permanent damage to the EVM. Please consult the EVM User's Guide prior to connecting any load to the EVM output. If there is uncertainty as to the load specification, please contact a TI field representative.

During normal operation, some circuit components may have case temperatures greater than 50° C. The EVM is designed to operate properly with certain components above 65° C as long as the input and output ranges are maintained. These components include but are not limited to linear regulators, switching transistors, pass transistors, and current sense resistors. These types of devices can be identified using the EVM schematic located in the EVM User's Guide. When placing measurement probes near these devices during operation, please be aware that these devices may be very warm to the touch.

## STANDARD TERMS FOR EVALUATION MODULES

1. *Delivery:* TI delivers TI evaluation boards, kits, or modules, including any accompanying demonstration software, components, and/or documentation which may be provided together or separately (collectively, an "EVM" or "EVMs") to the User ("User") in accordance with the terms set forth herein. User's acceptance of the EVM is expressly subject to the following terms.
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  - 1.2 EVMs are not intended for consumer or household use. EVMs may not be sold, sublicensed, leased, rented, loaned, assigned, or otherwise distributed for commercial purposes by Users, in whole or in part, or used in any finished product or production system.
- 2 *Limited Warranty and Related Remedies/Disclaimers:*
  - 2.1 These terms do not apply to Software. The warranty, if any, for Software is covered in the applicable Software License Agreement.
  - 2.2 TI warrants that the TI EVM will conform to TI's published specifications for ninety (90) days after the date TI delivers such EVM to User. Notwithstanding the foregoing, TI shall not be liable for a nonconforming EVM if (a) the nonconformity was caused by neglect, misuse or mistreatment by an entity other than TI, including improper installation or testing, or for any EVMs that have been altered or modified in any way by an entity other than TI, (b) the nonconformity resulted from User's design, specifications or instructions for such EVMs or improper system design, or (c) User has not paid on time. Testing and other quality control techniques are used to the extent TI deems necessary. TI does not test all parameters of each EVM. User's claims against TI under this Section 2 are void if User fails to notify TI of any apparent defects in the EVMs within ten (10) business days after delivery, or of any hidden defects with ten (10) business days after the defect has been detected.
  - 2.3 TI's sole liability shall be at its option to repair or replace EVMs that fail to conform to the warranty set forth above, or credit User's account for such EVM. TI's liability under this warranty shall be limited to EVMs that are returned during the warranty period to the address designated by TI and that are determined by TI not to conform to such warranty. If TI elects to repair or replace such EVM, TI shall have a reasonable time to repair such EVM or provide replacements. Repaired EVMs shall be warranted for the remainder of the original warranty period. Replaced EVMs shall be warranted for a new full ninety (90) day warranty period.

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**Evaluation Kits are intended solely for use by technically qualified, professional electronics experts who are familiar with the dangers and application risks associated with handling electrical mechanical components, systems, and subsystems.**

**User shall operate the Evaluation Kit within TI's recommended guidelines and any applicable legal or environmental requirements as well as reasonable and customary safeguards. Failure to set up and/or operate the Evaluation Kit within TI's recommended guidelines may result in personal injury or death or property damage. Proper set up entails following TI's instructions for electrical ratings of interface circuits such as input, output and electrical loads.**

**NOTE:**

EXPOSURE TO ELECTROSTATIC DISCHARGE (ESD) MAY CAUSE DEGRADATION OR FAILURE OF THE EVALUATION KIT; TI RECOMMENDS STORAGE OF THE EVALUATION KIT IN A PROTECTIVE ESD BAG.

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### 3 Regulatory Notices:

#### 3.1 United States

##### 3.1.1 Notice applicable to EVMs not FCC-Approved:

**FCC NOTICE:** This kit is designed to allow product developers to evaluate electronic components, circuitry, or software associated with the kit to determine whether to incorporate such items in a finished product and software developers to write software applications for use with the end product. This kit is not a finished product and when assembled may not be resold or otherwise marketed unless all required FCC equipment authorizations are first obtained. Operation is subject to the condition that this product not cause harmful interference to licensed radio stations and that this product accept harmful interference. Unless the assembled kit is designed to operate under part 15, part 18 or part 95 of this chapter, the operator of the kit must operate under the authority of an FCC license holder or must secure an experimental authorization under part 5 of this chapter.

##### 3.1.2 For EVMs annotated as FCC – FEDERAL COMMUNICATIONS COMMISSION Part 15 Compliant:

#### CAUTION

This device complies with part 15 of the FCC Rules. Operation is subject to the following two conditions: (1) This device may not cause harmful interference, and (2) this device must accept any interference received, including interference that may cause undesired operation.

Changes or modifications not expressly approved by the party responsible for compliance could void the user's authority to operate the equipment.

#### FCC Interference Statement for Class A EVM devices

*NOTE: This equipment has been tested and found to comply with the limits for a Class A digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instruction manual, may cause harmful interference to radio communications. Operation of this equipment in a residential area is likely to cause harmful interference in which case the user will be required to correct the interference at his own expense.*

#### FCC Interference Statement for Class B EVM devices

*NOTE: This equipment has been tested and found to comply with the limits for a Class B digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates, uses and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following measures:*

- Reorient or relocate the receiving antenna.
- Increase the separation between the equipment and receiver.
- Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.
- Consult the dealer or an experienced radio/TV technician for help.

#### 3.2 Canada

##### 3.2.1 For EVMs issued with an Industry Canada Certificate of Conformance to RSS-210 or RSS-247

#### Concerning EVMs Including Radio Transmitters:

This device complies with Industry Canada license-exempt RSSs. Operation is subject to the following two conditions:

(1) this device may not cause interference, and (2) this device must accept any interference, including interference that may cause undesired operation of the device.

#### Concernant les EVMs avec appareils radio:

Le présent appareil est conforme aux CNR d'Industrie Canada applicables aux appareils radio exempts de licence. L'exploitation est autorisée aux deux conditions suivantes: (1) l'appareil ne doit pas produire de brouillage, et (2) l'utilisateur de l'appareil doit accepter tout brouillage radioélectrique subi, même si le brouillage est susceptible d'en compromettre le fonctionnement.

#### Concerning EVMs Including Detachable Antennas:

Under Industry Canada regulations, this radio transmitter may only operate using an antenna of a type and maximum (or lesser) gain approved for the transmitter by Industry Canada. To reduce potential radio interference to other users, the antenna type and its gain should be so chosen that the equivalent isotropically radiated power (e.i.r.p.) is not more than that necessary for successful communication. This radio transmitter has been approved by Industry Canada to operate with the antenna types listed in the user guide with the maximum permissible gain and required antenna impedance for each antenna type indicated. Antenna types not included in this list, having a gain greater than the maximum gain indicated for that type, are strictly prohibited for use with this device.

#### Concernant les EVMs avec antennes détachables

Conformément à la réglementation d'Industrie Canada, le présent émetteur radio peut fonctionner avec une antenne d'un type et d'un gain maximal (ou inférieur) approuvé pour l'émetteur par Industrie Canada. Dans le but de réduire les risques de brouillage radioélectrique à l'intention des autres utilisateurs, il faut choisir le type d'antenne et son gain de sorte que la puissance isotope rayonnée équivalente (p.i.r.e.) ne dépasse pas l'intensité nécessaire à l'établissement d'une communication satisfaisante. Le présent émetteur radio a été approuvé par Industrie Canada pour fonctionner avec les types d'antenne énumérés dans le manuel d'usage et ayant un gain admissible maximal et l'impédance requise pour chaque type d'antenne. Les types d'antenne non inclus dans cette liste, ou dont le gain est supérieur au gain maximal indiqué, sont strictement interdits pour l'exploitation de l'émetteur

#### 3.3 Japan

3.3.1 *Notice for EVMs delivered in Japan:* Please see [http://www.tij.co.jp/lsts/ti\\_ja/general/eStore/notice\\_01.page](http://www.tij.co.jp/lsts/ti_ja/general/eStore/notice_01.page) 日本国内に輸入される評価用キット、ボードについては、次のところをご覧ください。

<https://www.ti.com/ja-jp/legal/notice-for-evaluation-kits-delivered-in-japan.html>

3.3.2 *Notice for Users of EVMs Considered "Radio Frequency Products" in Japan:* EVMs entering Japan may not be certified by TI as conforming to Technical Regulations of Radio Law of Japan.

If User uses EVMs in Japan, not certified to Technical Regulations of Radio Law of Japan, User is required to follow the instructions set forth by Radio Law of Japan, which includes, but is not limited to, the instructions below with respect to EVMs (which for the avoidance of doubt are stated strictly for convenience and should be verified by User):

1. Use EVMs in a shielded room or any other test facility as defined in the notification #173 issued by Ministry of Internal Affairs and Communications on March 28, 2006, based on Sub-section 1.1 of Article 6 of the Ministry's Rule for Enforcement of Radio Law of Japan,
2. Use EVMs only after User obtains the license of Test Radio Station as provided in Radio Law of Japan with respect to EVMs, or
3. Use of EVMs only after User obtains the Technical Regulations Conformity Certification as provided in Radio Law of Japan with respect to EVMs. Also, do not transfer EVMs, unless User gives the same notice above to the transferee. Please note that if User does not follow the instructions above, User will be subject to penalties of Radio Law of Japan.

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2. 実験局の免許を取得後ご使用いただく。
3. 技術基準適合証明を取得後ご使用いただく。

なお、本製品は、上記の「ご使用にあたっての注意」を譲渡先、移転先に通知しない限り、譲渡、移転できないものとします。

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3.3.3 *Notice for EVMs for Power Line Communication:* Please see [http://www.tij.co.jp/lsts/ti\\_ja/general/eStore/notice\\_02.page](http://www.tij.co.jp/lsts/ti_ja/general/eStore/notice_02.page)  
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#### 3.4 European Union

3.4.1 *For EVMs subject to EU Directive 2014/30/EU (Electromagnetic Compatibility Directive):*

This is a class A product intended for use in environments other than domestic environments that are connected to a low-voltage power-supply network that supplies buildings used for domestic purposes. In a domestic environment this product may cause radio interference in which case the user may be required to take adequate measures.

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4 *EVM Use Restrictions and Warnings:*

4.1 EVMS ARE NOT FOR USE IN FUNCTIONAL SAFETY AND/OR SAFETY CRITICAL EVALUATIONS, INCLUDING BUT NOT LIMITED TO EVALUATIONS OF LIFE SUPPORT APPLICATIONS.

4.2 User must read and apply the user guide and other available documentation provided by TI regarding the EVM prior to handling or using the EVM, including without limitation any warning or restriction notices. The notices contain important safety information related to, for example, temperatures and voltages.

4.3 *Safety-Related Warnings and Restrictions:*

4.3.1 User shall operate the EVM within TI's recommended specifications and environmental considerations stated in the user guide, other available documentation provided by TI, and any other applicable requirements and employ reasonable and customary safeguards. Exceeding the specified performance ratings and specifications (including but not limited to input and output voltage, current, power, and environmental ranges) for the EVM may cause personal injury or death, or property damage. If there are questions concerning performance ratings and specifications, User should contact a TI field representative prior to connecting interface electronics including input power and intended loads. Any loads applied outside of the specified output range may also result in unintended and/or inaccurate operation and/or possible permanent damage to the EVM and/or interface electronics. Please consult the EVM user guide prior to connecting any load to the EVM output. If there is uncertainty as to the load specification, please contact a TI field representative. During normal operation, even with the inputs and outputs kept within the specified allowable ranges, some circuit components may have elevated case temperatures. These components include but are not limited to linear regulators, switching transistors, pass transistors, current sense resistors, and heat sinks, which can be identified using the information in the associated documentation. When working with the EVM, please be aware that the EVM may become very warm.

4.3.2 EVMs are intended solely for use by technically qualified, professional electronics experts who are familiar with the dangers and application risks associated with handling electrical mechanical components, systems, and subsystems. User assumes all responsibility and liability for proper and safe handling and use of the EVM by User or its employees, affiliates, contractors or designees. User assumes all responsibility and liability to ensure that any interfaces (electronic and/or mechanical) between the EVM and any human body are designed with suitable isolation and means to safely limit accessible leakage currents to minimize the risk of electrical shock hazard. User assumes all responsibility and liability for any improper or unsafe handling or use of the EVM by User or its employees, affiliates, contractors or designees.

4.4 User assumes all responsibility and liability to determine whether the EVM is subject to any applicable international, federal, state, or local laws and regulations related to User's handling and use of the EVM and, if applicable, User assumes all responsibility and liability for compliance in all respects with such laws and regulations. User assumes all responsibility and liability for proper disposal and recycling of the EVM consistent with all applicable international, federal, state, and local requirements.

5. *Accuracy of Information:* To the extent TI provides information on the availability and function of EVMs, TI attempts to be as accurate as possible. However, TI does not warrant the accuracy of EVM descriptions, EVM availability or other information on its websites as accurate, complete, reliable, current, or error-free.

6. *Disclaimers:*

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