

EVM User's Guide: LM851772Q1EVM-PD

适用于 USB-PD 的 LM851772-Q1 降压/升压控制器评估模块



说明

LM851772Q1EVM-PD 演示了采用 LM851772-Q1 的高功率灵活降压/升压设计。该评估模块经配置，可在 9V 至 60V 的输入电压范围内运行，并提供 5V 至 48V 稳压输出，负载电流高达 5A。该器件的大多数设置可通过跳线轻松调整或设置。

开始使用

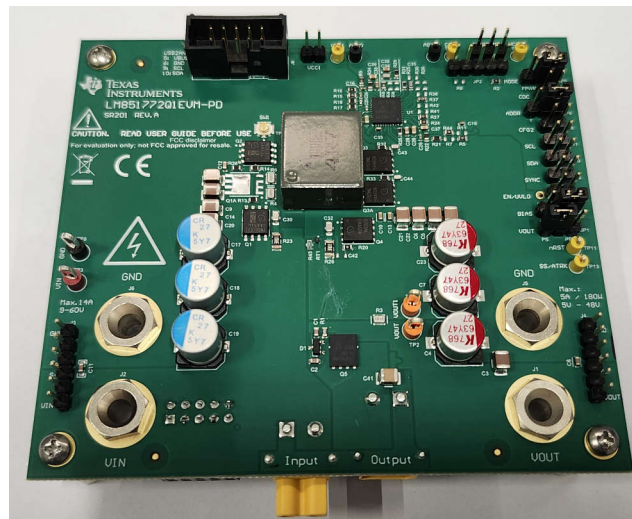
1. 将 EVM 连接到电源和负载。
2. 使用 [USB2ANY](#) 适配器配置 GUI 和 I2C 操作。
3. 安装 [LM851772-Q1](#) 配置 [GUI](#)。

特性

- 宽输入电压范围与超高 (> 95%) 峰值电源转换效率
- 可使用反馈电阻或 I2C 接口来调节输出电压
- 可轻松配置电流监控器或限制器
- 支持电缆压降补偿
- 输出断开支持
- 可编程输入欠压锁定 (UVLO) 阈值和迟滞
- 输出恒压 (CV) 和恒流 (CC) 选项
- 具有 USB2ANY 和 GUI 的 I2C 接口

应用

- USB Type-C® 电力输送
 - [集线站](#)
 - [PC 监视器](#)
 - [台式机](#)
- 无线充电
- [工业 PC](#) 和 [耐用型 PC](#)
- [直流/直流模块](#)



LM851772Q1EVM-PD

1 评估模块概述

1.1 简介

LM851772Q1EVM-PD 评估模块 (EVM) 旨在方便地评估 **LM851772-Q1** 宽 VIN 降压/升压控制器的性能。

该 EVM 已针对 USB-PD 应用进行优化，经配置，可在 9V 至 60V 的输入电压范围内运行，并提供 5V 至 48V 稳压输出，负载电流高达 5A。该 EVM 的工作开关频率为 320kHz。该器件的大多数设置可通过跳线轻松调整或设置，例如：工作模式 (PSM 或 fPWM)、辅助电源和外部时钟同步。

通过使用 I2C 接口，该 EVM 涵盖了用于配置和调整开关模式电源运行的附加参数。

该 EVM 可直接连接到 Type-C 电力输送 (PD) 控制器 **TPS26742EQ1EVM** 评估模块，以构成完整的 USB-EVM PD 应用。

1.2 套件内容

- 一个 **LM851772Q1EVM-PD** PCB
- EVM 免责声明自述文件

该 EVM 不包含 USB2ANY 接口。如需订购 USB2ANY 接口适配器，请访问 [USB2ANY 接口适配器页面](#)。

1.3 规格

表 1-1. 评估板规格

参数	值
输入电压	9.0V 至 60V
输出电压	5V 至 36V 5V 至 48V (Vin > 16V)
最大输出电流	5A
默认开关频率	320kHz
工作温度	25°C
电路板尺寸 (四层)	4.1inch × 3.6inch

1.4 器件信息

LM851772-Q1 是一款四开关降压/升压控制器。无论输入电压是高于、等于还是低于调节后的输出电压，该器件均可提供稳定的输出电压。在省电模式下，该器件支持在整个输出范围内实现出色的效率。

- 宽输入电压范围：3.5V 至 85V
- 输出电压范围为 3.3V 至 48V
- 峰值电流调节方案
- 输出电压动态跟踪
 - 数字 PWM 跟踪输入
 - 模拟跟踪输入
 - 通过 I2C 接口编程
- 最小静态电流
 - 3 μ A 的低关断 IQ
 - 25 μ A 的低工作 IQ
- 可实现高轻负载效率的运行模式选择
 - 省电突发模式
 - μ Sleep 省电模式
- 集成高压电源 LDO

2 硬件连接器、测试点和选择开关说明

本节提供了 EVM 的 I/O 连接器、跳线和测试点。

电源必须连接到输入连接器 J2 和 J6。

负载必须连接到输出连接器 J1 和 J5。

2.1 连接器说明

表 2-1. 连接器

参考指示符	说明
J1	输出电压正连接
J2	输入电压正连接
J3	输入电压正和输入电压返回测试点
J4	输出电压正和输出电压返回测试点
J5	输出电压回路连接
J6	输入电压回路连接
J7	USB-PD 控制器 EVM 控制连接器
J8	I2C/USB2ANY 连接器
J9	USB-PD 控制器 EVM 电源输入连接器
J10	USB-PD 控制器 EVM 电源输出连接器
J11	CDC 输出连接

表 2-2. USB-PD 控制器 EVM 控制连接器：J7

引脚	功能	引脚	功能
1	CDC：电流监测信号	2	PTC：温度传感器
3	PDCTRL_GOOD：温度传感器电源	4	nFLT 信号
5	EN/UVLO 信号	6	I2C：SDA
7	GND	8	I2C：SCL
9	VCC1 (通过跳线 JP12 连接)	10	GND

2.2 跳线说明

表 2-3. 跳线

参考指示符	引脚	说明	默认连接
JP1	引脚 1 至引脚 2	将 VOUT 连接到传感电阻器之前	*
	引脚 2 至引脚 3	将 VOUT 连接到传感电阻器之后	
JP2	引脚 1 至引脚 3	使用外部分压器电路时，可用于波德图信号注入	
	引脚 4 至引脚 5 (FB)	将 FB 连接到 VCC2 (如果未组装 R2) 以使用内部分压器电路	
JP3	引脚 1 至引脚 2 (VOUT1)	将 BIAS 连接至 VOUT1 (功率级输出)	
	引脚 2 至引脚 3 (VIN)	将 BIAS 连接至 VIN 小心 当 VIN 高于 55V 时，请勿将跳线设置为 VIN。	
JP4	引脚 1 至引脚 2 (GND)	将 EN/UVLO 连接至 GND	
	引脚 2 至引脚 3 (VIN)	将 EN/UVLO 连接至 VIN 小心 仅当 BIAS 也连接到 VIN 时，才可将跳线设置为 VIN	

表 2-3. 跳线 (续)

参考指示符	引脚	说明	默认连接
JP5	引脚 1 至引脚 2 (GND)	将 MODE 设置为低电平：PSM 模式	
	引脚 2 至引脚 3 (VCC2)	将 MODE 设置为高电平：FPWM 模式	*
JP6	引脚 1 至引脚 2 (SYNC)	将 SYNC 设置为 GND	
JP7	引脚 1 至引脚 2 (GND)	将 ILIMCOMP 连接到 GND	
	引脚 2 至引脚 3 (VCC2)	将 ILIMCOMP 连接至 VCC2 (禁用电流限制器)	
	开路	启用电流限制器功能	
JP8	引脚 2 至引脚 3 (CFG2)	将 CFG2 设置为 GND	
JP9	引脚 1 至引脚 2 (ADDR/AGND)	设置 I2C 启用地址 0x6A	
	引脚 2 至引脚 3 (ADDR/VCC2)	设置 I2C 启用地址 0x6B	*
JP10	引脚 2 至引脚 3 (CFG3/SDA)	将 CFG3/SDA 设置为 GND	
JP11	引脚 1 至引脚 2 (CFG4/SCL)	将 CFG4/SCL 设置为 GND	
JP12	引脚 1 至引脚 2 (VCC1)	将 VCC1 连接至接口接头 J7	

2.3 测试点说明

表 2-4. 测试点

参考指示符	说明
TP1	ISNSP 测试点
TP2	ISNSN 测试点
TP3 (VIN)	输入电压正测试点
TP4	AGND 测试点
TP5	SW1
TP6	SW2
TP7	AGND 测试点
TP8 (GND)	输入电压返回测试点
TP9	VCC2 测试点
TP10	VCC1 测试点
TP11	nRST 测试点
TP12	COMP 测试点
TP13	SS/ATRK 测试点

3 实现结果

3.1 测试设置

图 3-1 展示了用于评估 LM851772Q1EVM-PD 的典型测试设置。

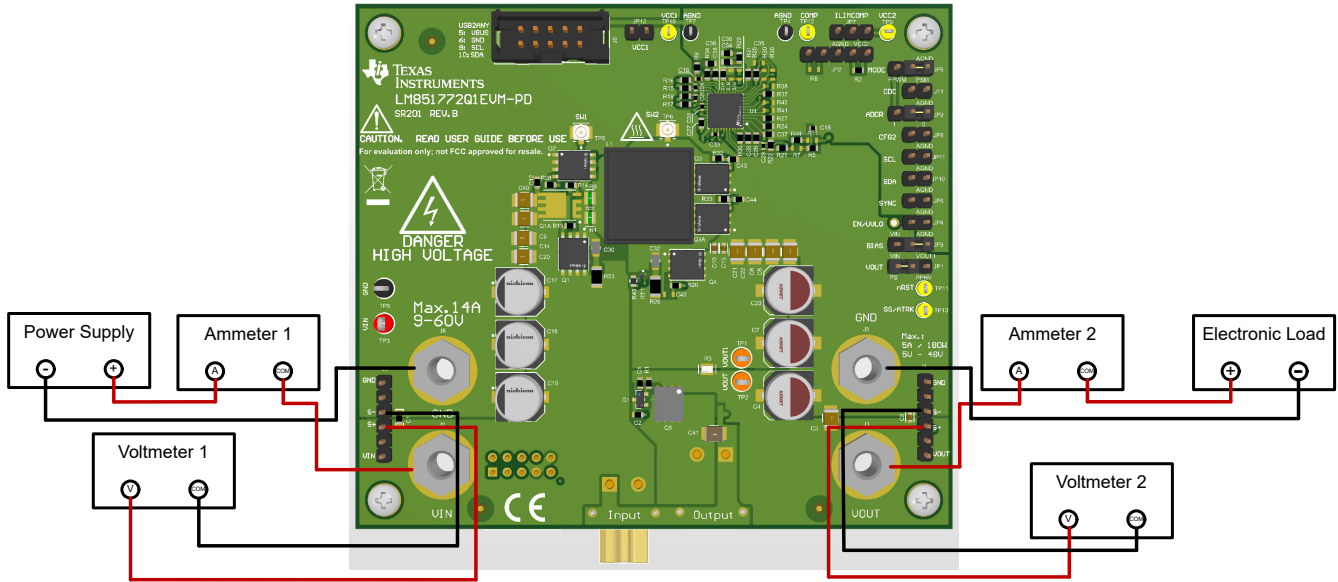


图 3-1. 典型的 EVM 连接图

3.2 测试程序

1. 将电源电流限值设置为 15A。
2. 关闭电源。
3. 将电源的正输出连接到 J2，负输出连接到 J6。
4. 将负载连接到 J1 实现正连接，连接到 J5 实现负连接。
5. 将电源电压设置为 8V，将电子负载设置为 0.1A。确保电子负载电压通过标称 5V 输出进行调节。
6. 缓慢增大负载，同时监控 J4-VOUT 和 J4-GND 之间的输出电压。当输出负载增加到 3A 时，输出电压必须保持标称 4V 输出的稳压。
7. 从 8V 至 60V 缓慢扫描输入电压。输出电压必须通过标称 5V 输出进行调节。
8. 通过 I2C 和 GUI 将输出电压设置为 48V
9. 将负载电流增至 5A。
10. 从 36V 至 20V 缓慢扫描输入电压。确保输出电压必须通过标称 48V 输出进行调节。
11. 将输入电压降至 0V 以关闭降压/升压转换器，然后关闭负载。

3.3 注意事项



警告

在全功率低输入下长时间运行会导致 FET (Q1 至 Q8) 发热。电路板表面会变热。请勿触摸。接触会导致烫伤。



小心

在无人看管的情况下，请勿让 EVM 处于通电状态。

3.4 测试数据和性能曲线

3.4.1 热性能

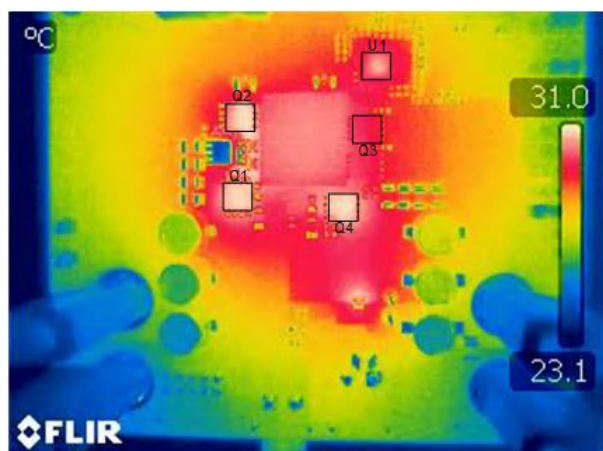


图 3-2. 热像图 : $V_{IN} = 12.0V$, $V_{OUT} = 5.0V$, $I_{OUT} = 5.0A$, 无强制空气冷却

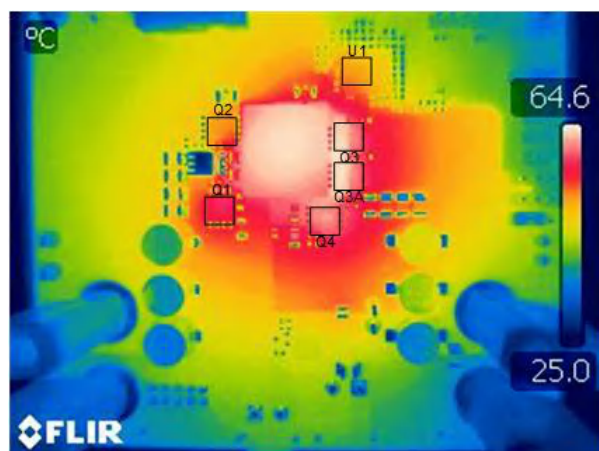


图 3-3. 热像图 : $V_{IN} = 12.0V$, $V_{OUT} = 28.0V$, $I_{OUT} = 5.0A$, 无强制空气冷却

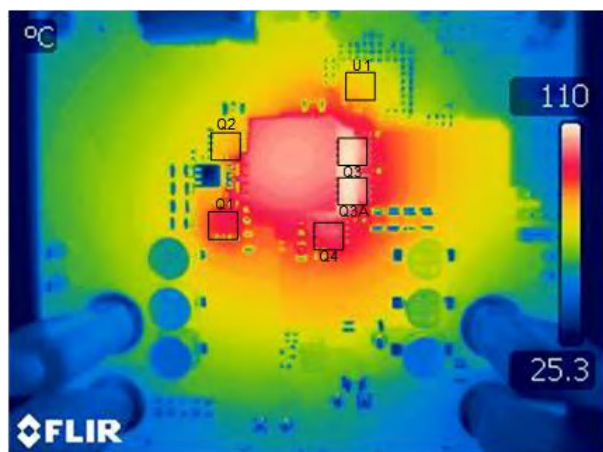


图 3-4. 热像图 : $V_{IN} = 12.0V$, $V_{OUT} = 36.0V$, $I_{OUT} = 5.0A$, 无强制空气冷却

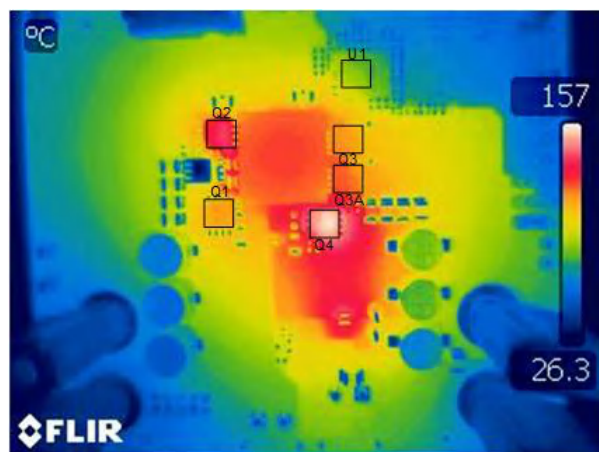


图 3-5. 热像图 : $V_{IN} = 12.0V$, $V_{OUT} = 36.0V$, 负载 = 短路, 无强制空气冷却

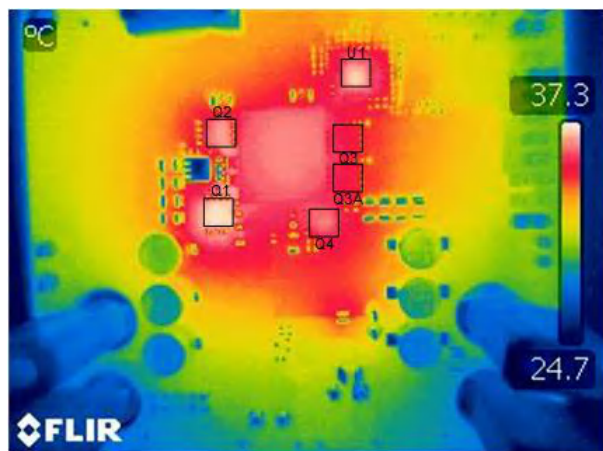


图 3-6. 热像图 : $V_{IN} = 20.0V$, $V_{OUT} = 5.0V$, $I_{OUT} = 5.0A$, 无强制空气冷却

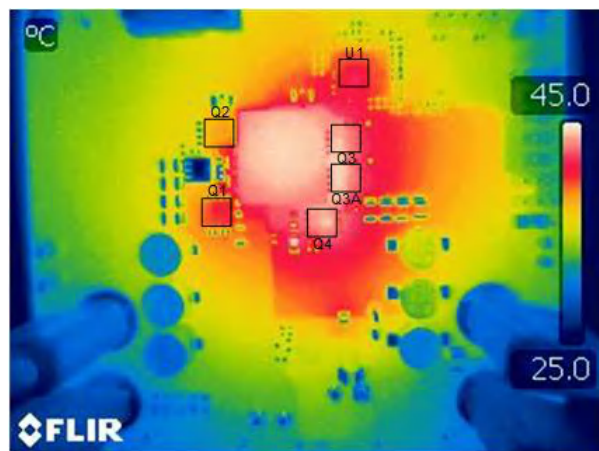


图 3-7. 热像图 : $V_{IN} = 20.0V$, $V_{OUT} = 28.0V$, $I_{OUT} = 5.0A$, 无强制空气冷却

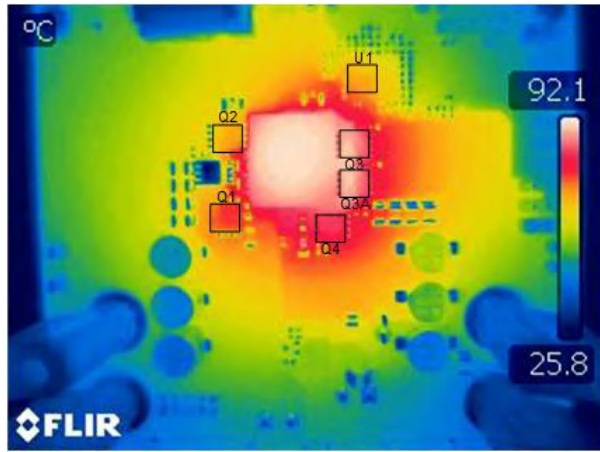


图 3-8. 热像图 : $V_{IN} = 20.0V$, $V_{OUT} = 48.0V$, $I_{OUT} = 5.0A$, 无强制空气冷却

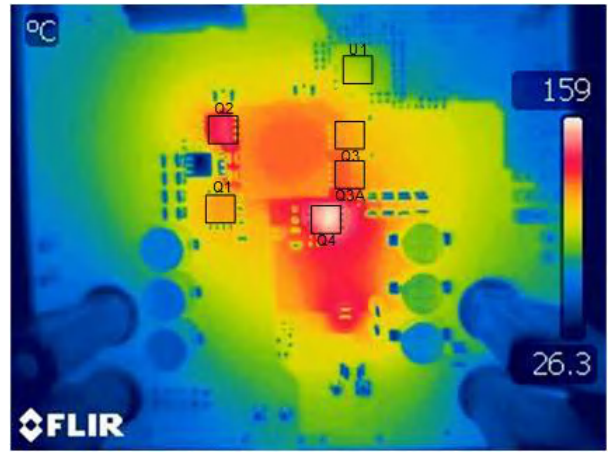


图 3-9. 热像图 : $V_{IN} = 20.0V$, $V_{OUT} = 48.0V$, 负载 = 短路, 无强制空气冷却

3.4.2 效率

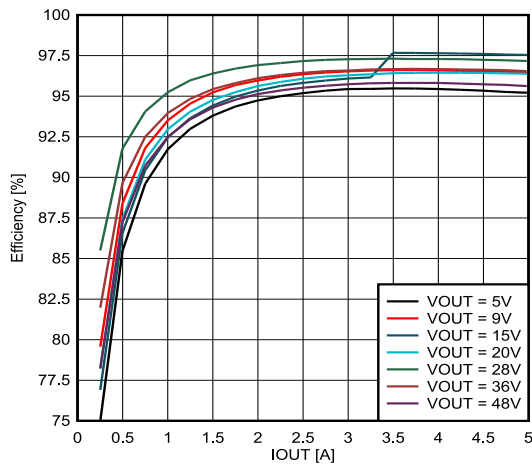


图 3-10. 效率与输出电流间的关系曲线 (FPWM 模式下, $V_{in} = 12V$ 时)

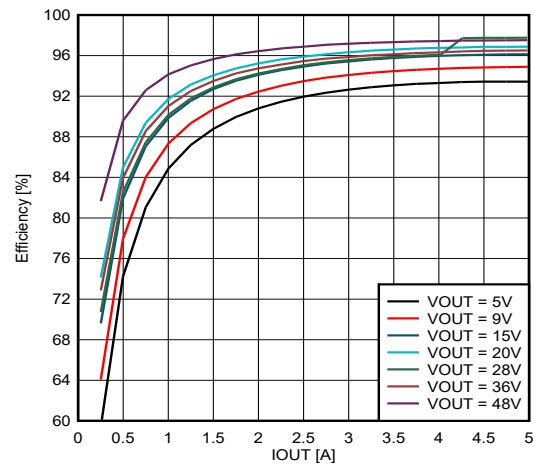


图 3-11. 效率与输出电流间的关系曲线 (FPWM 模式下, $V_{in} = 20V$ 时)

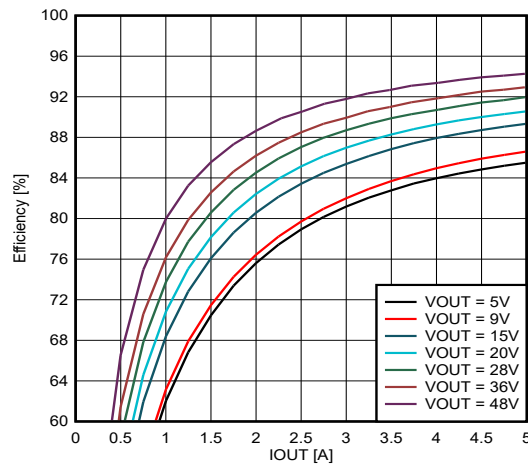


图 3-12. 效率与输出电流间的关系曲线 (FPWM 模式下, $V_{in} = 48V$ 时)

3.4.3 稳态波形

备注

除非另有说明，否则所有测量均在 FPWM 模式下完成。

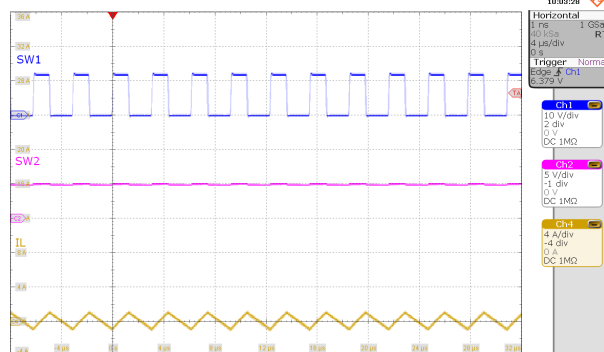


图 3-13. SW1、SW2、 I_L ($V_{IN} = 12V$, $V_{OUT} = 5V$, $I_{OUT} = 0A$)

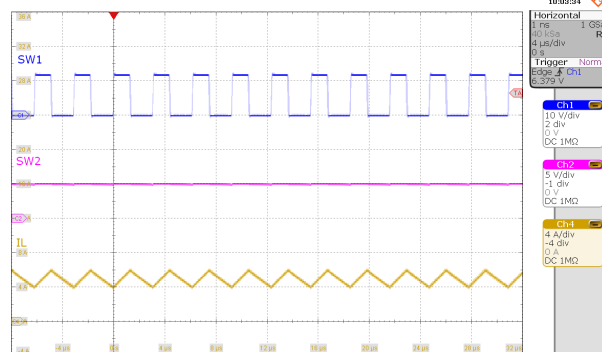


图 3-14. SW1、SW2、 I_L ($V_{IN} = 12V$, $V_{OUT} = 5V$, $I_{OUT} = 5A$)

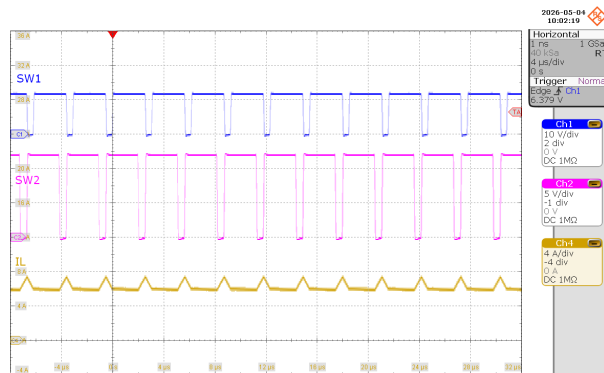


图 3-15. SW1、SW2、 I_L ($V_{IN} = 12V$, $V_{OUT} = 12V$, $I_{OUT} = 0A$)

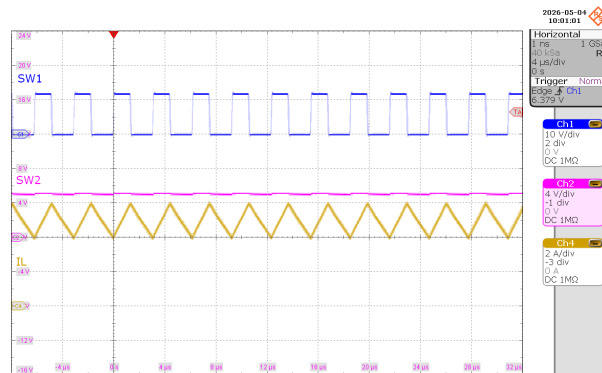


图 3-16. SW1、SW2、 I_L ($V_{IN} = 12V$, $V_{OUT} = 12V$, $I_{OUT} = 5A$)

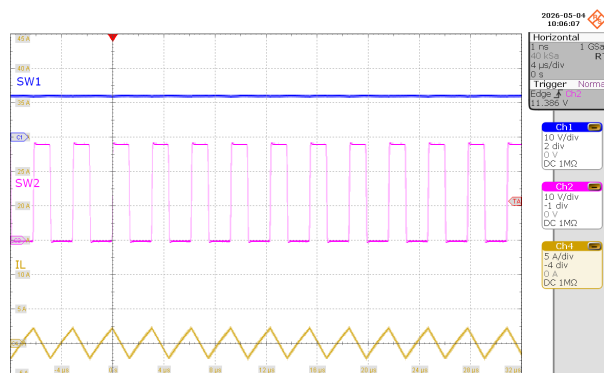


图 3-17. SW1、SW2、 I_L ($V_{IN} = 12V$, $V_{OUT} = 28V$, $I_{OUT} = 0A$)

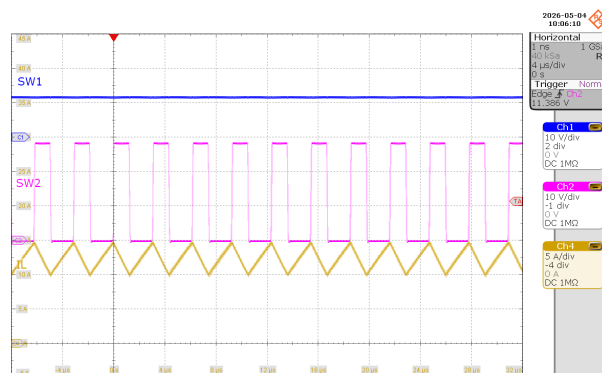


图 3-18. SW1、SW2、 I_L ($V_{IN} = 12V$, $V_{OUT} = 28V$, $I_{OUT} = 5A$)

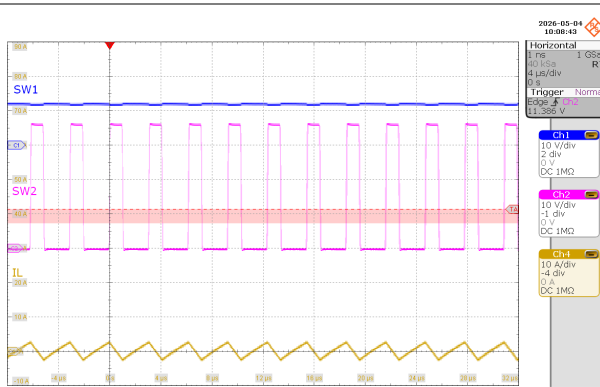


图 3-19. SW1、SW2、 I_L ($V_{IN} = 12V$, $V_{OUT} = 36V$, $I_{OUT} = 5A$)

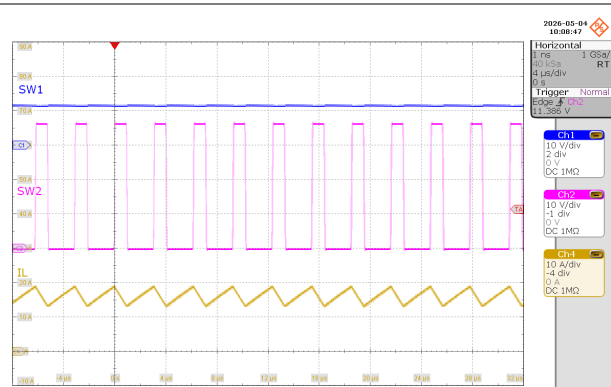


图 3-20. SW1、SW2、 I_L ($V_{IN} = 12V$, $V_{OUT} = 36V$, $I_{OUT} = 5A$)

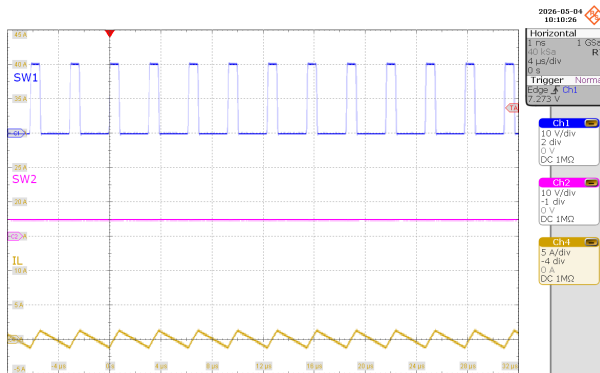


图 3-21. SW1、SW2、 I_L ($V_{IN} = 20V$, $V_{OUT} = 5V$, $I_{OUT} = 0A$)

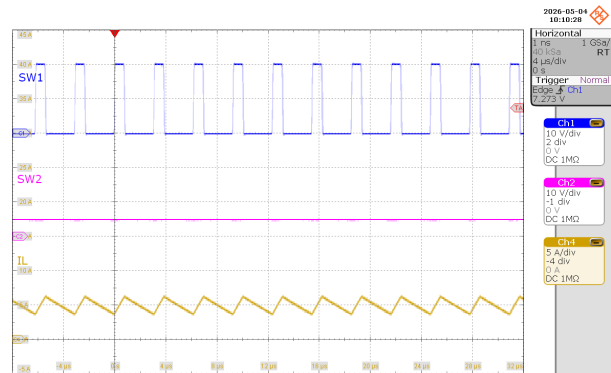


图 3-22. SW1、SW2、 I_L ($V_{IN} = 20V$, $V_{OUT} = 5V$, $I_{OUT} = 5A$)

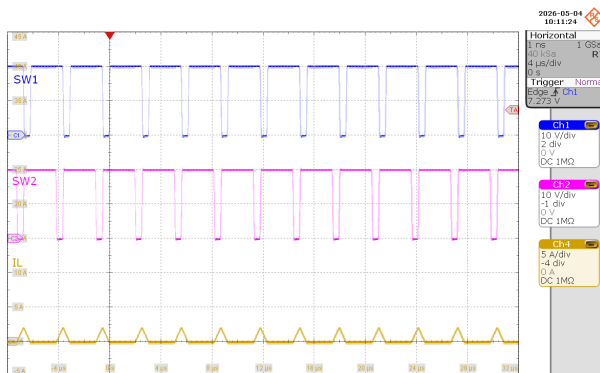


图 3-23. SW1、SW2、 I_L ($V_{IN} = 20V$, $V_{OUT} = 20V$, $I_{OUT} = 0A$)

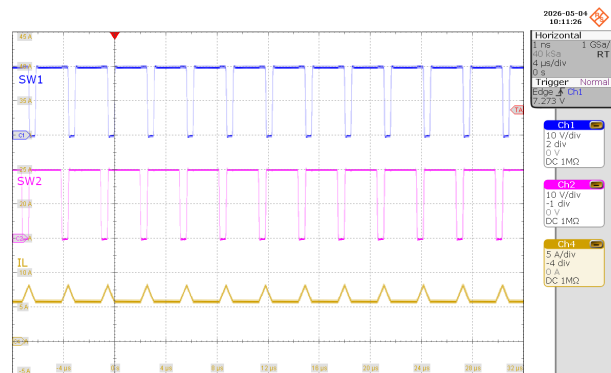


图 3-24. SW1、SW2、 I_L ($V_{IN} = 20V$, $V_{OUT} = 20V$, $I_{OUT} = 5A$)

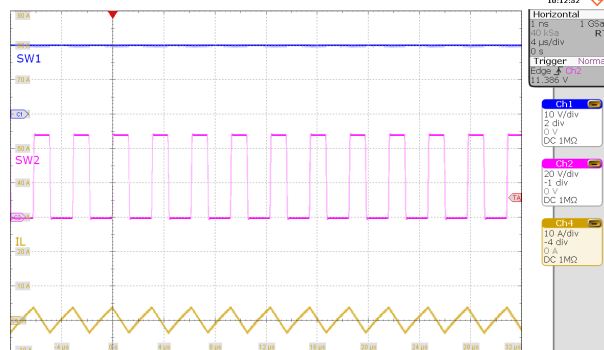


图 3-25. SW1、SW2、 I_L ($V_{IN} = 20V$, $V_{OUT} = 48V$, $I_{OUT} = 0A$)

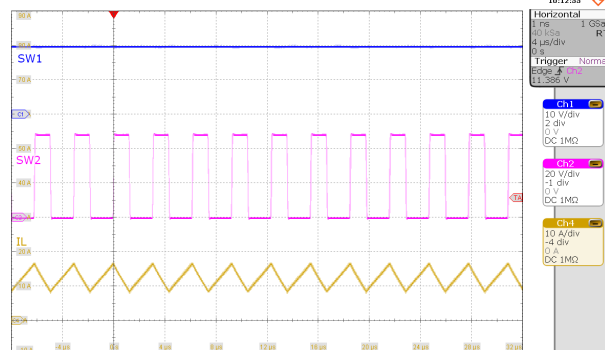


图 3-26. SW1、SW2、 I_L ($V_{IN} = 20V$, $V_{OUT} = 48V$, $I_{OUT} = 5A$)

3.4.4 阶跃负载响应

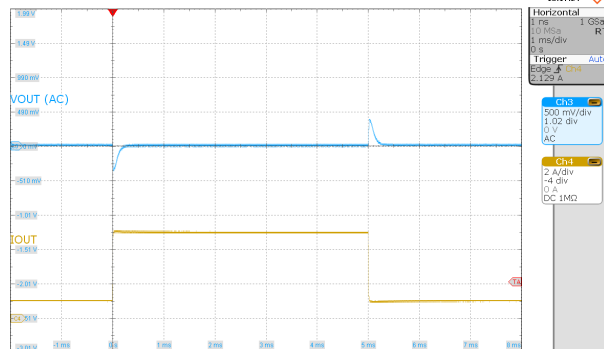


图 3-27. 负载阶跃 ($V_{IN} = 12V$, $V_{OUT} = 5V$, $I_{OUT} = 1A-5A$)

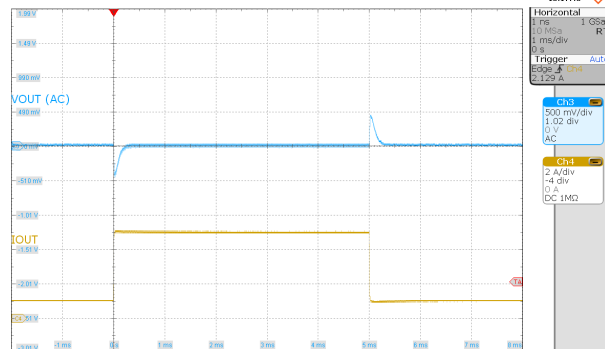


图 3-28. 负载阶跃 ($V_{IN} = 12V$, $V_{OUT} = 12V$, $I_{OUT} = 1A-5A$)

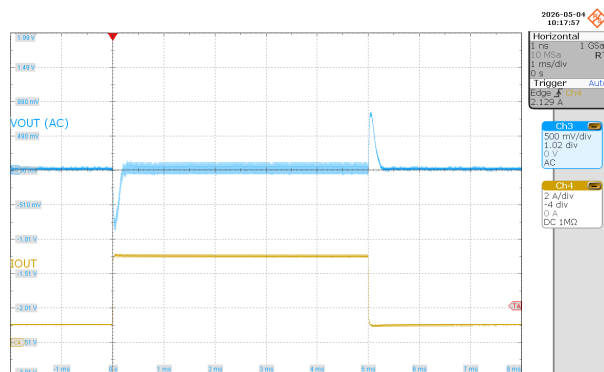


图 3-29. 负载阶跃 ($V_{IN} = 12V$, $V_{OUT} = 28V$, $I_{OUT} = 1A-5A$)

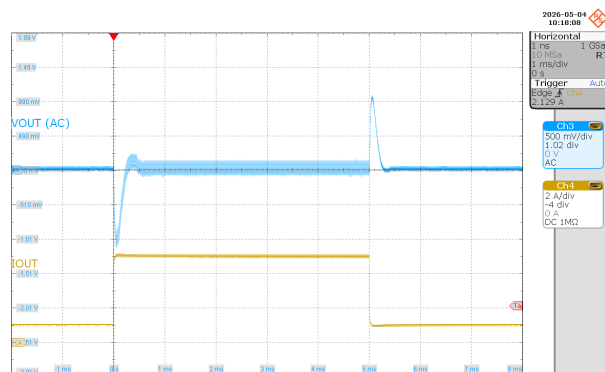


图 3-30. 负载阶跃 ($V_{IN} = 12V$, $V_{OUT} = 36V$, $I_{OUT} = 1A-5A$)

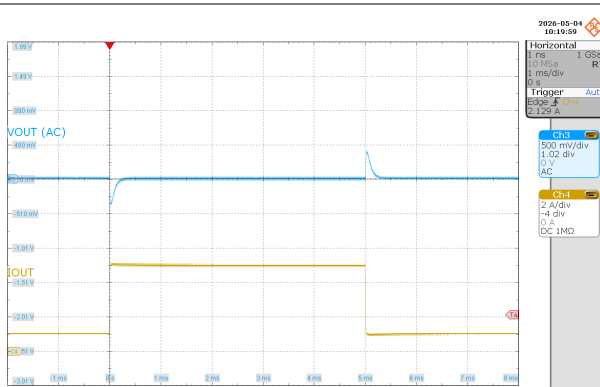


图 3-31. 负载阶跃 ($V_{IN} = 20V$, $V_{OUT} = 5V$, $I_{OUT} = 1A-5A$)

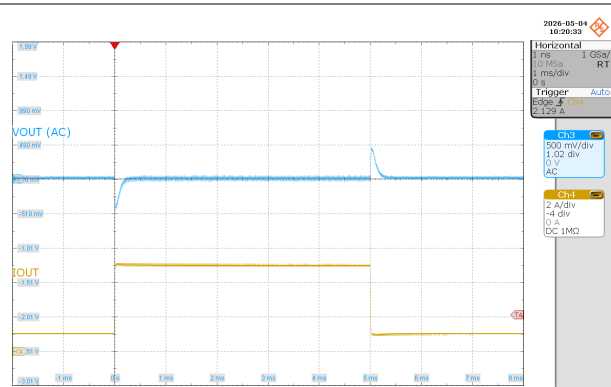


图 3-32. 负载阶跃 ($V_{IN} = 20V$, $V_{OUT} = 20V$, $I_{OUT} = 1A-5A$)

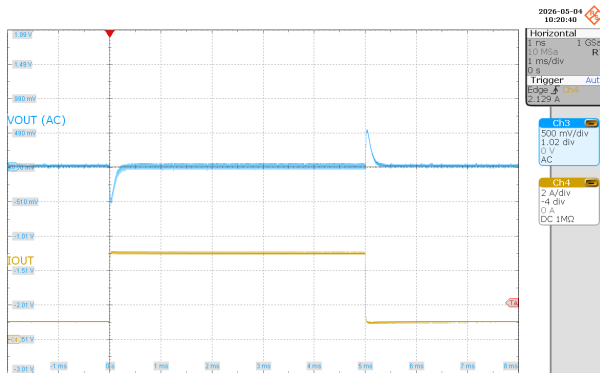


图 3-33. 负载阶跃 ($V_{IN} = 20V$, $V_{OUT} = 28V$, $I_{OUT} = 1A-5A$)

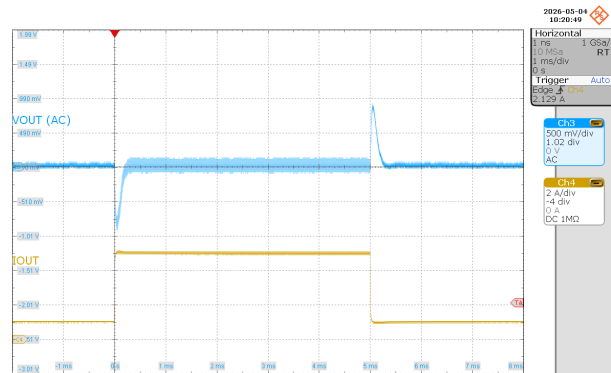


图 3-34. 负载阶跃 ($V_{IN} = 20V$, $V_{OUT} = 48V$, $I_{OUT} = 1A-5A$)

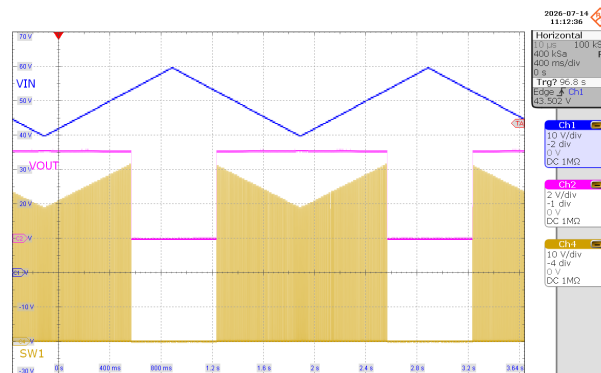


图 3-35. V_{IN} 电压从 40V 斜升至 60V — 当达到 55V 时触发 IVP 并停止开关动作

3.4.5 交流环路响应曲线

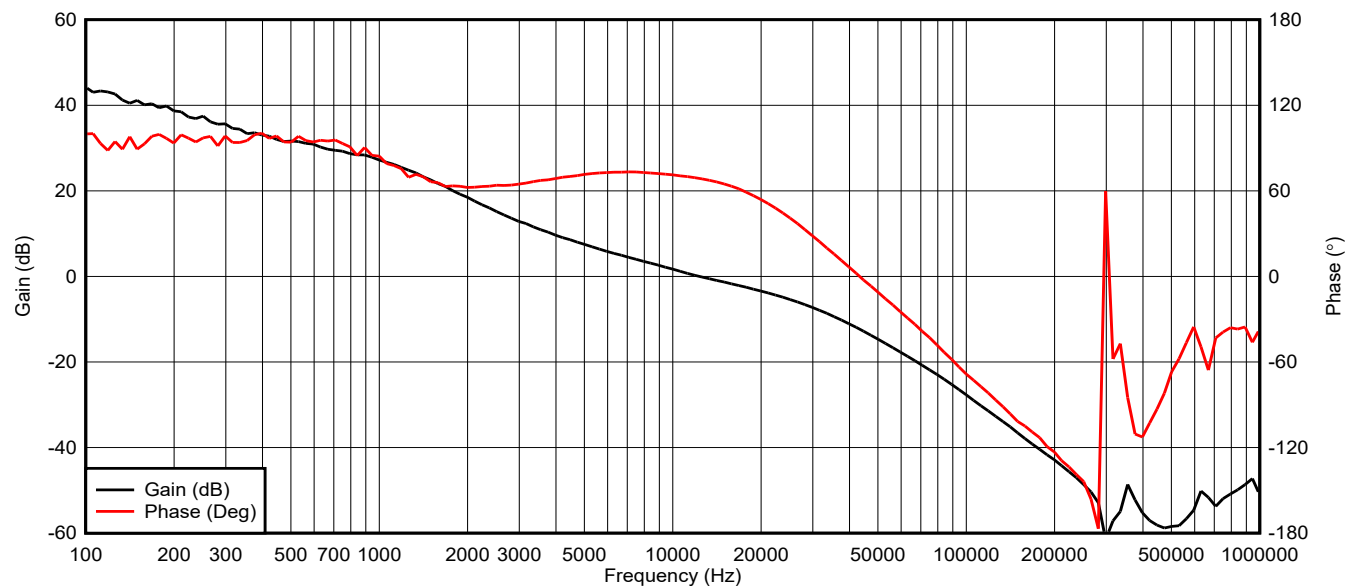


图 3-36. 控制环路响应, $V_{IN} = 12.0V$, $V_{OUT} = 5.0V$, $I_{OUT} = 5.0A$

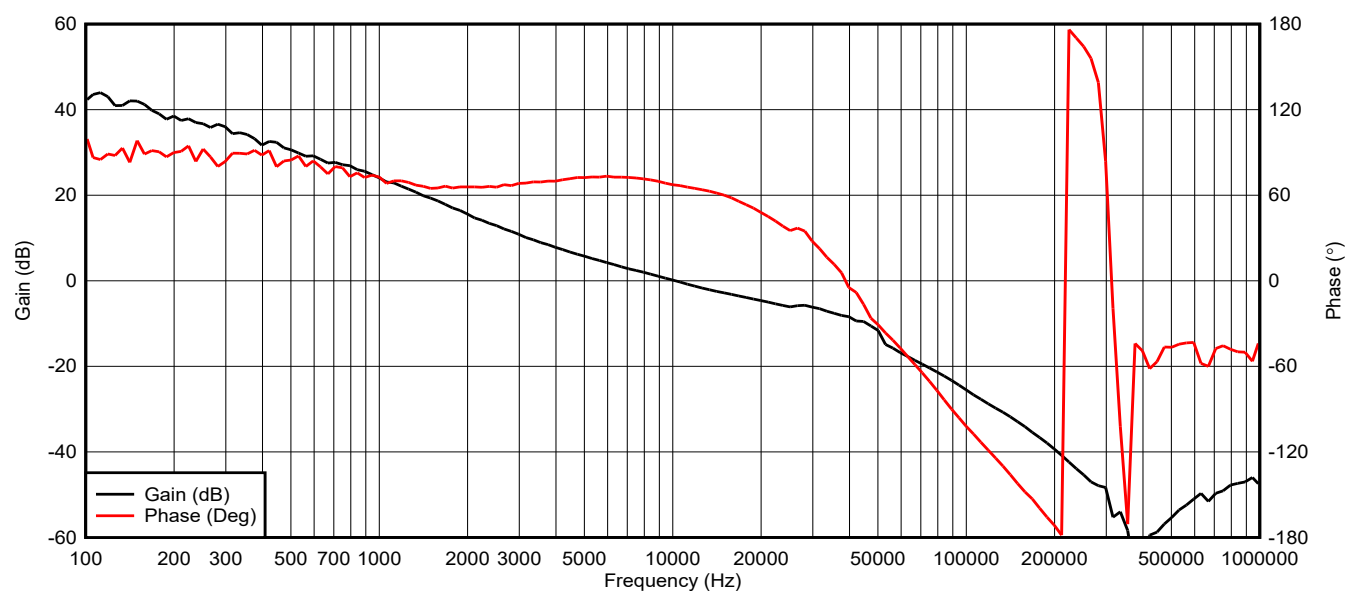


图 3-37. 控制环路响应, $V_{IN} = 12.0V$, $V_{OUT} = 12.0V$, $I_{OUT} = 5.0A$

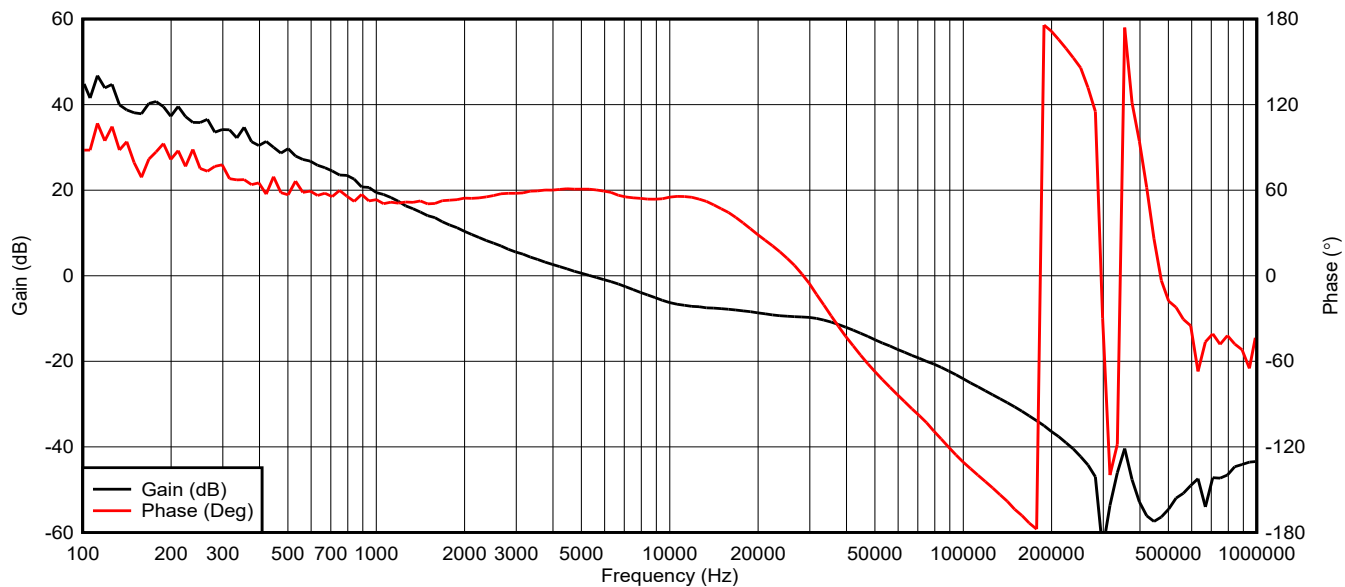


图 3-38. 控制环路响应, $V_{IN} = 12.0V$, $V_{OUT} = 28V$, $I_{OUT} = 5.0A$

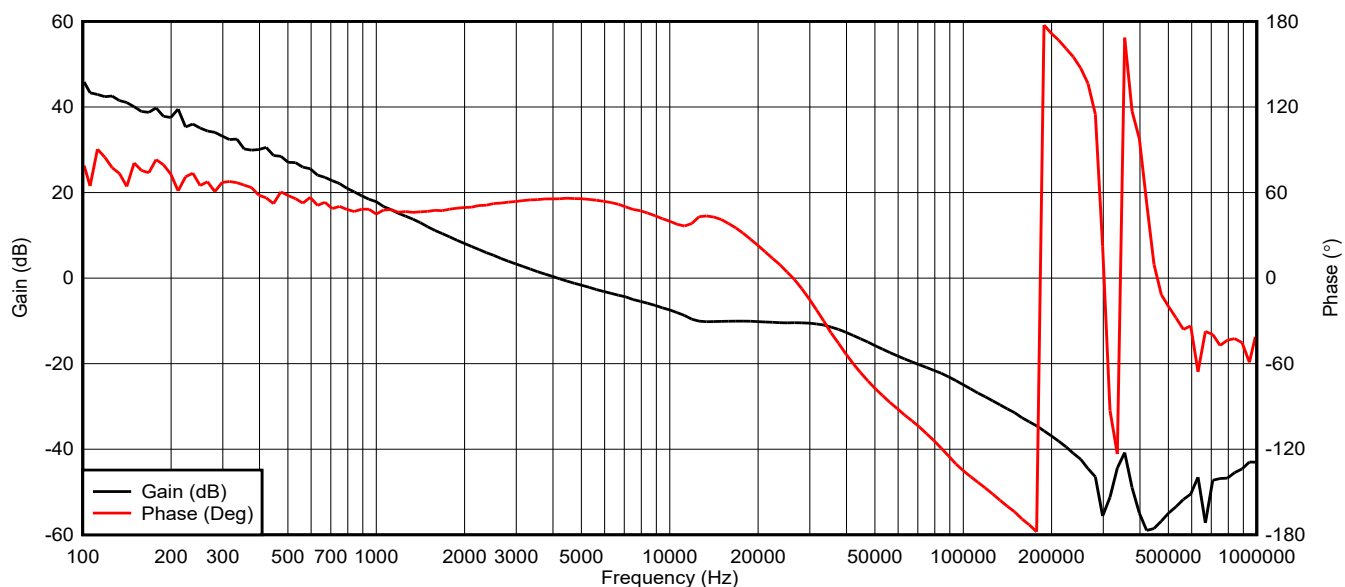
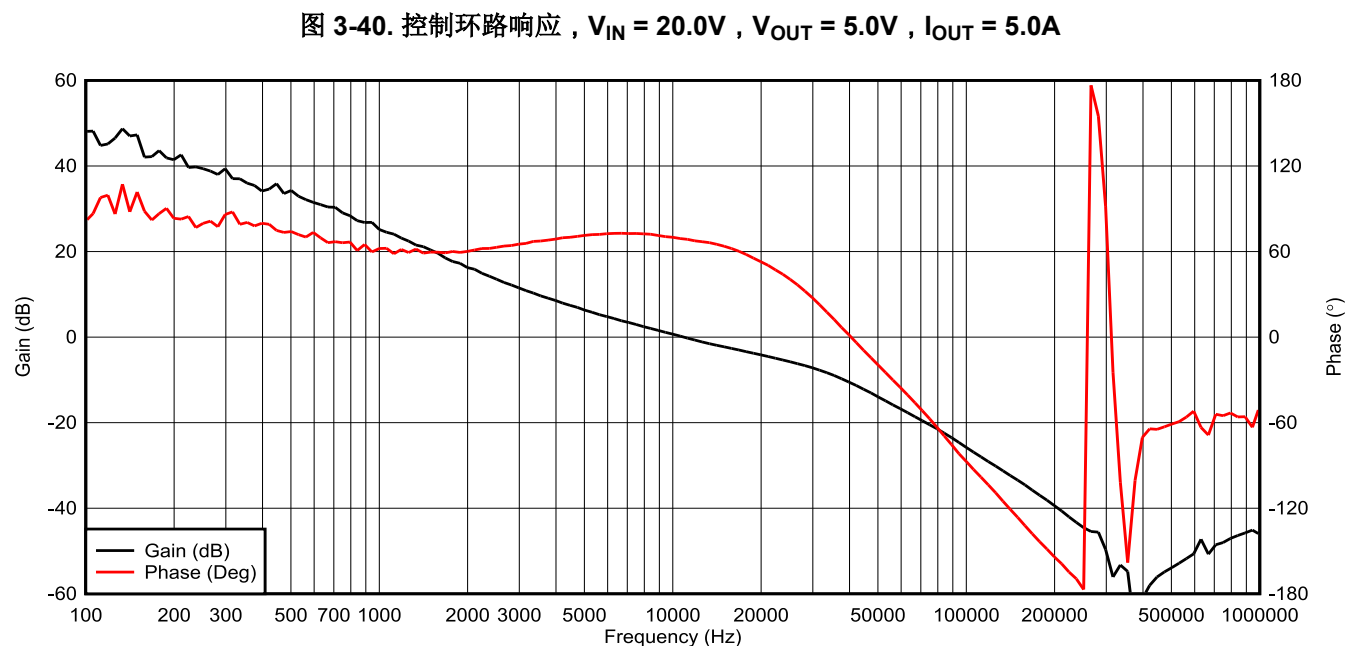
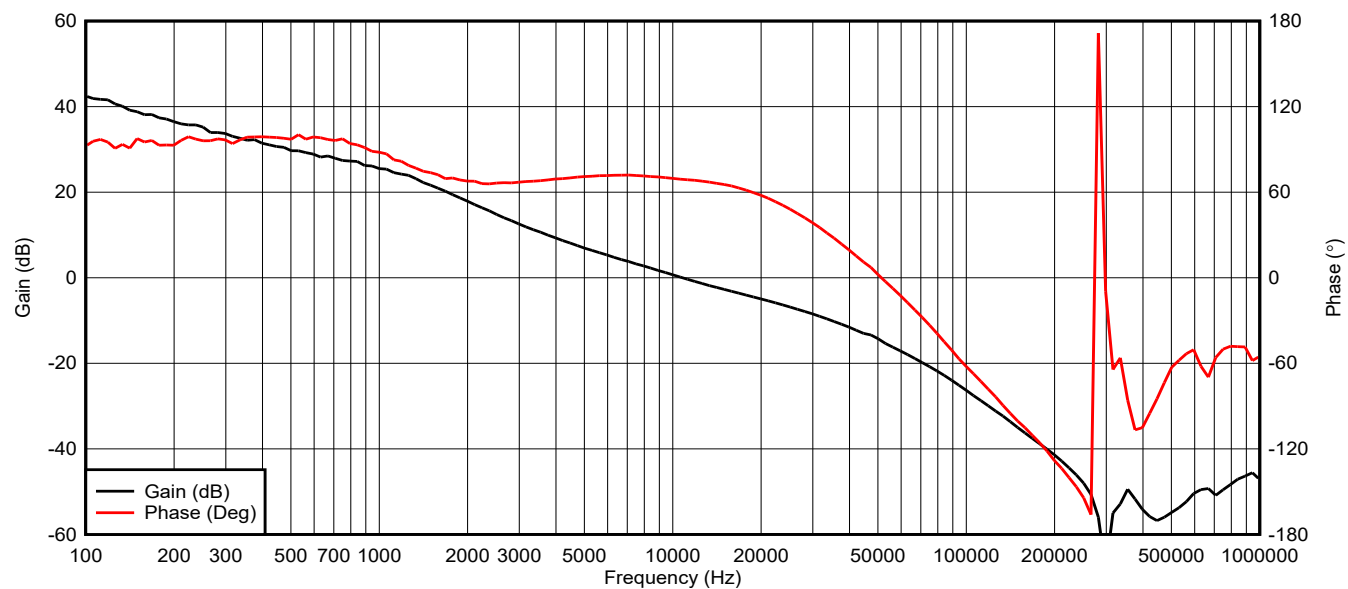


图 3-39. 控制环路响应, $V_{IN} = 12.0V$, $V_{OUT} = 36V$, $I_{OUT} = 5.0A$



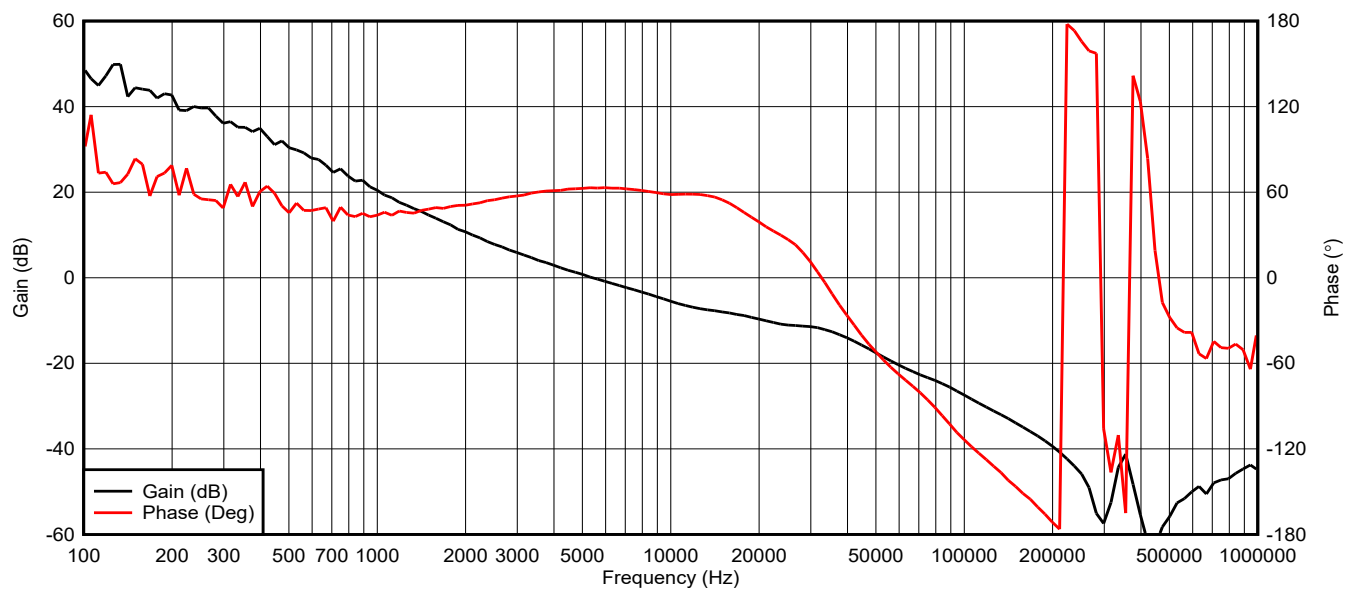
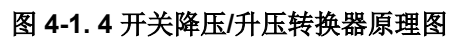


图 3-42. 控制环路响应, $V_{IN} = 20.0V$, $V_{OUT} = 48.0V$, $I_{OUT} = 5.0A$

4.1 原理图



4.2 电路板布局

图 4-2 至 图 4-7 展示了 LM851772Q1EVM-PD PCB 设计。

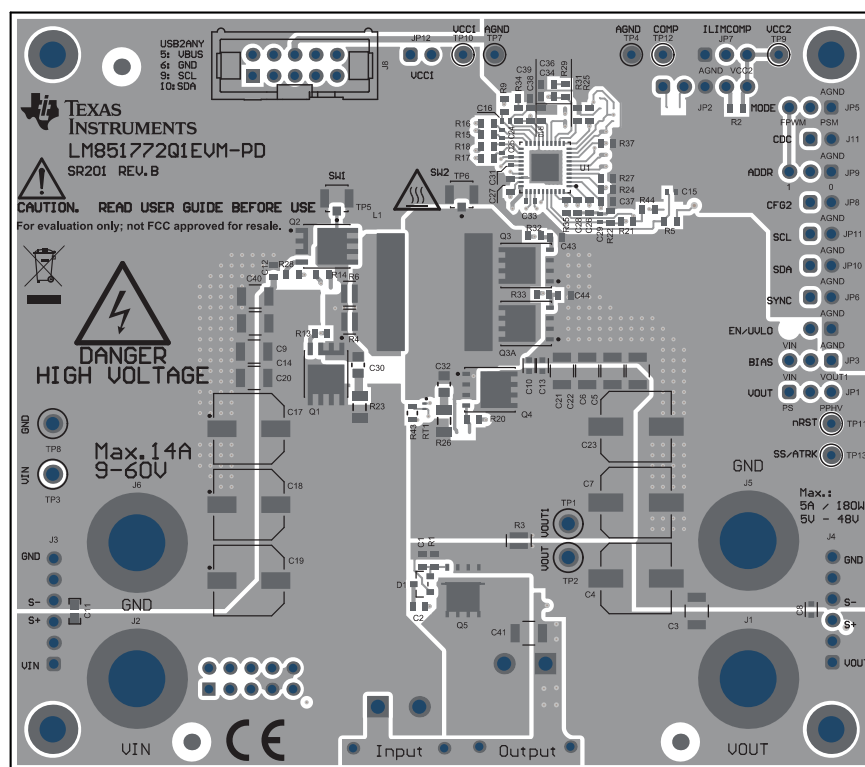


图 4-2. 顶部丝印

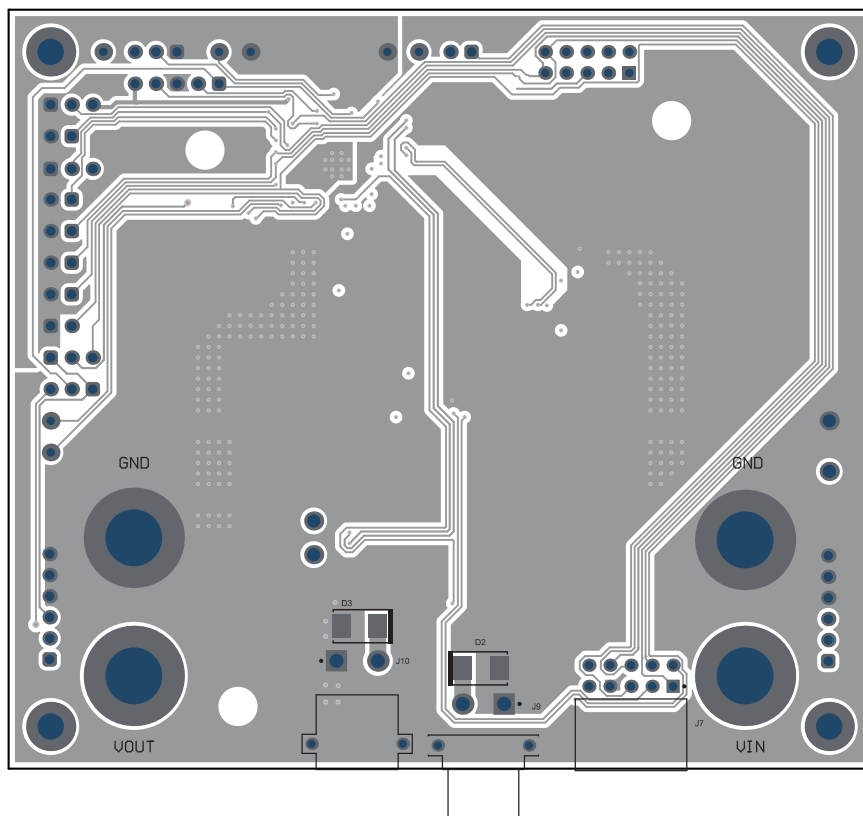


图 4-3. 底部丝印

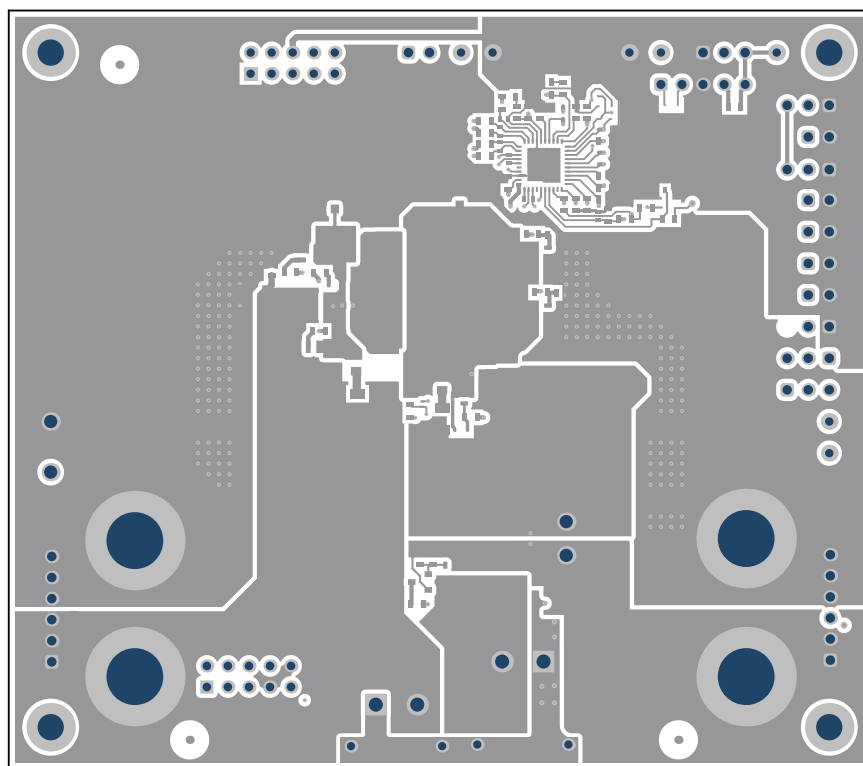


图 4-4. 顶层

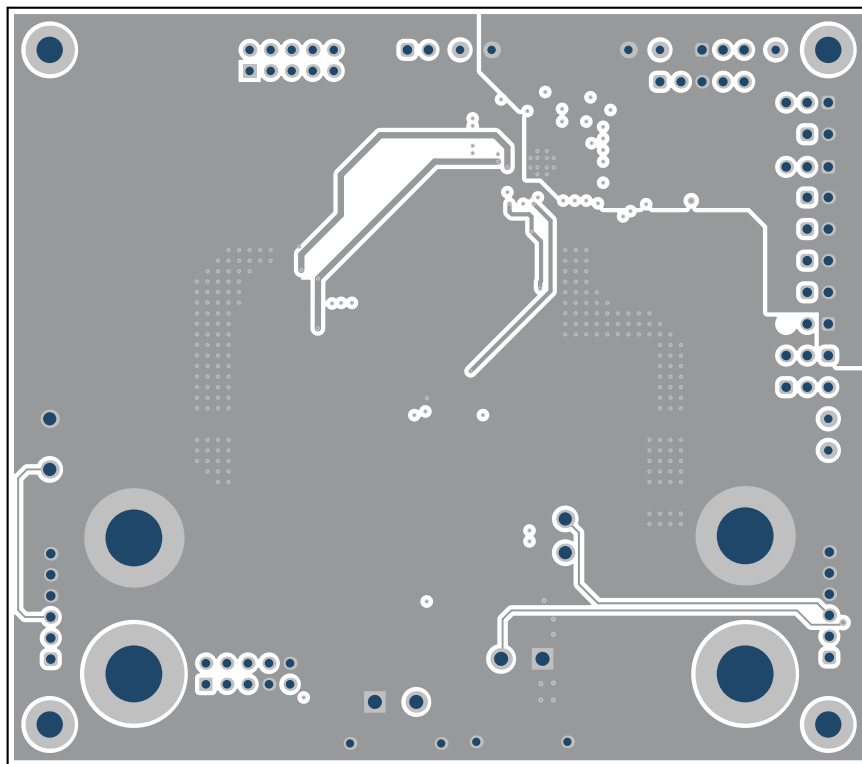


图 4-5. 中间层 1

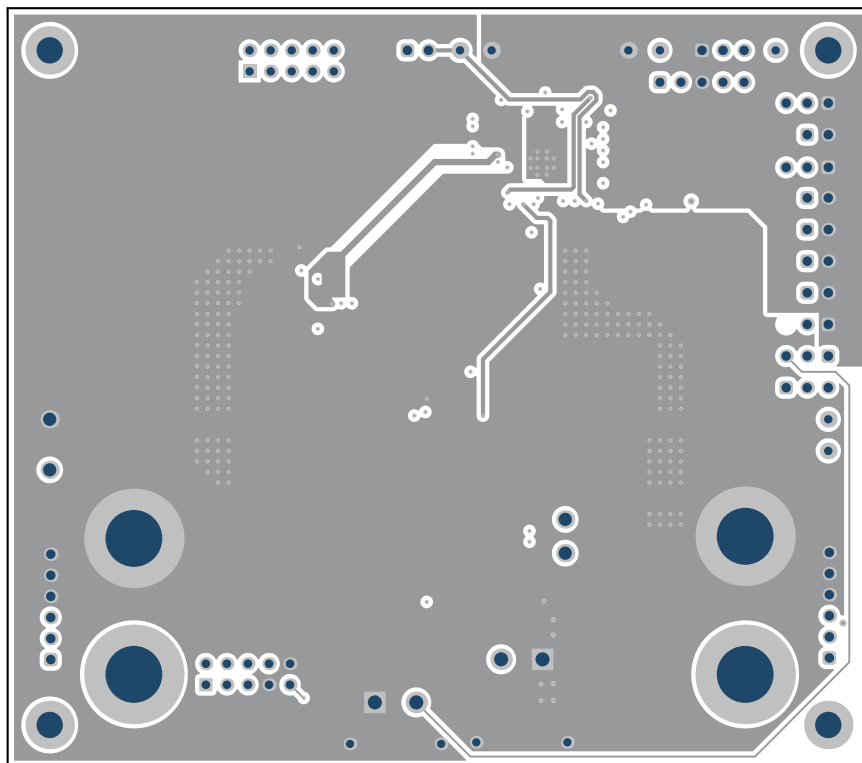


图 4-6. 中间层 2

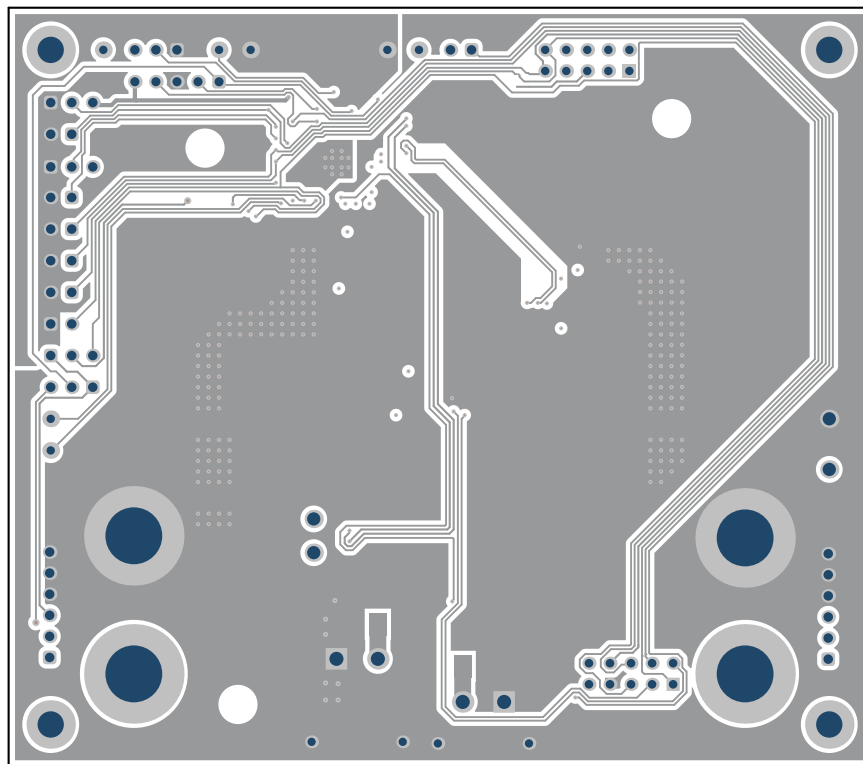


图 4-7. 底层

4.3 物料清单

位号	数量	值	器件型号	制造商	说明
C1、C2、C12、C28	4	0.1uF	CGA3E3X7S2A104K080AB	TDK	电容, 陶瓷, 0.1μF, 100V, +/- 10%, X7S, AEC-Q200 1级, 0603
C3、C5、C6、C9、C14、C20、C21、C22、C40	9	10μF	CGA6P1X7R2A106K250AC	TDK	10μF ±10% 100V 陶瓷电容器 X7R 1210 (公制 3225)
C4、C7、C23	3	27μF	A768KE276M1JLAE054	KEMET	电容铝制聚合物 27uF 63V 20% 焊接圆柱形 54mΩ 1175mA 2000 小时 125°C T/R
C8、C10、C13	3		GRM188R72A104KA35D	Murata	0.1μF ±10% 100V 陶瓷电容器 X7R 0603 (公制 1608)
C11	1	0.1uF	GCJ188R72A104KA01D	MuRata	电容, 陶瓷, 0.1μF, 100V, +/-10%, X7R, AEC-Q200 1级, 0603
C15、C16、C29	3	0.1uF	GRM155R62A104KE14D	MuRata	电容, 陶瓷, 0.1uF, 100V, +/-10%, X5R, 0402
C17、C18、C19	3	27μF	PCR1K270MCL1GS	Nichicon	27μF 80V 铝聚合物电容径向, Can - SMD 38mΩ 4000 小时, 125°C
C24	1	0.1uF	GRM155R71H104ME14D	MuRata	电容, 陶瓷, 0.1 μF, 50V, +/-20%, X7R, 0402
C25	1	270pF	GRM1555C1H271JA01D	MuRata	电容, 陶瓷, 270pF, 50V, +/-5%, C0G/NP0, 0402
C26、C27	2	22μF	GRT188R61A226ME13D	Murata	多层陶瓷电容器, 22uF, 10V, X5R ±20%, 0603, 纸质 T/R
C30	1	680pF	08051A681JAT2A	AVX	电容, 陶瓷, 680pF, 100V, +/-5%, C0G/NP0, 0805
C31、C33	2	0.1uF	GCM155R71H104KE02D	MuRata	电容, 陶瓷, 0.1μF, 50V, +/- 10%, X7R, AEC-Q200 1级, 0402
C32	1	1000pF	C0805C102J1GACTU	Kemet	电容, 陶瓷, 1000pF, 100V, +/-5%, C0G/NP0, 0805
C34	1	220pF	06035A221FAT2A	AVX	电容, 陶瓷, 220pF, 50V, +/-1%, C0G/NP0, 0603
C36	1	0.012uF	C0603C123K5RACTU	Kemet	电容, 陶瓷, 0.012μF, 50V, +/-10%, X7R, AEC-Q200 1级, 0603
C37	1	0.02uF	CC0603KRX7R9BB203	Yageo	电容, 陶瓷, 0.02μF, 50V, +/-10%, X7R, 0603
C38	1	4700pF	06031C472JAT2A	AVX	电容, 陶瓷, 4,700pF, 100V, +/-5%, X7R, 0603
C39	1	0.047μF	06035C473JAT2A	AVX	电容, 陶瓷, 0.047μF, 50V, +/-5%, X7R, 0603
C41	1	10μF	C3225X7R2A106K250AC	TDK	10μF ±10% 100V 陶瓷电容器 X7R 1210 (公制 3225)
C43、C44	2	680pF	C0603C681J5GACTU	Kemet	电容, 陶瓷, 680pF, 50V, +/-5%, C0G/NP0, 0603
D1	1		BAS70-04E3-18	Vishay	二极管阵列, 1 对串联肖特基, 70V, 200mA (DC), 表面贴装 TO-236-3, SC-59, SOT-23-3
D2、D3	2	48V	SMBJ48A-13F	Diodes Inc.	二极管, TVS, 单向, 48V, 77.4Vc, SMB
J1、J2、J5、J6	4		108-0740-001	Cinch Connectivity	标准香蕉插孔, 非绝缘, 15A
J3、J4	2		61300611121	Würth Elektronik	接头, 2.54mm, 6x1, 金, TH
J7	1		SSW-105-02G-D-RA	Samtec	10 位置插座连接器 0.100" (2.54mm) 穿孔, 直角镀金

位号	数量	值	器件型号	制造商	说明
J8	1		N2510-6002-RB	3M	接头 (有罩) , 100mil , 5x2 , 高温 , 镀金 , TH
J9	1		XT30PW-F	Amass	插座 , 直流电源 , XT30 , 母头 , 引脚 : 2 , 在 PCB 上 , THT , 黄色 , 15A
J10	1		XT30PW-M	Amass	插座 , 直流电源 , XT30 , 公头 , 引脚 : 2 , 在 PCB 上 , THT , 黄色 , 15A , 500V
J11、JP6、JP8、JP10、JP11、JP12	6		61300211121	Würth Elektronik	接头 , 2.54mm , 2x1 , 金 , TH
JP1、JP3、JP4、JP5、JP7、JP9	6		61300311121	Würth Elektronik	接头 , 2.54mm , 3x1 , 金 , TH
JP2	1		61300511121	Würth Elektronik	接头 , 2.54mm , 5x1 , 金 , TH
L1	1	4.7uH	74439370047	Würth Elektronik	电感器 , 屏蔽 , 金属复合物 , 4.7μH , 17A , 0.00385Ω , SMD
Q1、Q2	2		IAUCN08S7L033ATMA1	Infineon	MOSFET N 沟道 80V 130A (Tj) 118W (Tc) 表面贴装 PG-TDSON-8-34
Q3、Q3A、Q4 ⁽¹⁾	3		ISC0703NLSATMA1	Infineon	MOSFET N 沟道 60V 23A (Ta)、135A (Tc) 3W (Ta)、100W (Tc) 表面贴装 PG-TDSON-8
Q5	1	60V	CSD18540Q5B	德州仪器 (TI)	MOSFET , N 沟道 , 60V , 100A , DNK0008A (VSON-CLIP-8)
R1	1	5.10k	RC0603FR-075K1L	Yageo	电阻 , 5.10k , 1% , 0.1W , 0603
R2、R5、R9、R10、R15、R16、R20、R24、R27、R44	10	0	RMCF0603ZT0R00	Stackpole Electronics Inc	电阻 , 0 , 1% , 0.1W , AEC-Q200 0 级 , 0603
R3	1	10m	KRL2012E-C-R010F-T05	Susumu	10mΩ , ±1% , 1W , 片上电阻 , 宽 , 0805 (公制 2012) , 0508 , 汽车 AEC-Q200 , 电流检测 , 金属箔
R4、R6	2	5m	KRL2012E-M-R005F-T5	Susumu	5mΩ , ±1% , 1W , 片上电阻 , 宽 , 0805 (公制 2012) , 0508 , 汽车 AEC-Q200 , 电流检测 , 金属箔
R13、R14、R28	3	3	CRCW06033R00JNEA	Vishay-Dale	电阻 , 3.0 , 5% , 0.1W , AEC-Q200 0 级 , 0603
R17、R18	2	10	CRCW060310R0FKEAHP	Vishay-Dale	电阻 , 10.0 , 1% , 0.25W , AEC-Q200 0 级 , 0603
R21	1	75.0k	RC0603FR-0775KL	Yageo	电阻 , 75.0k , 1% , 0.1W , 0603
R22	1	12.7k	RC0603FR-0712K7L	Yageo	电阻 , 12.7k , 1% , 0.1W , 0603
R23、R26	2	3	CRCW12063R00JNEA	Vishay-Dale	电阻 , 3.0 , 5% , 0.25W , AEC-Q200 0 级 , 1206
R25	1	10.0k	RT0603BRD0710KL	Yageo America	电阻 , 10.0k , 0.1% , 0.1W , 0603
R29	1	7.15k	CRCW06037K15FKEA	Vishay-Dale	电阻 , 7.15k , 1% , 0.1W , AEC-Q200 0 级 , 0603
R31	1	97.6k	RC0603FR-0797K6L	Yageo	电阻 , 97.6k , 1% , 0.1W , 0603
R32、R33	2	3.9	CRCW06033R90JNEA	Vishay-Dale	电阻 , 3.9 , 5% , 0.1W , AEC-Q200 0 级 , 0603
R34	1	20.0k	CRCW060320K0FKEA	Vishay-Dale	电阻 , 20.0k , 1% , 0.1W , AEC-Q200 0 级 , 0603
R35	1	390k	RC0603FR-07390KL	Yageo	电阻 , 390k , 1% , 0.1W , 0603
R37	1	20.5k	CRCW060320K5FKEA	Vishay-Dale	电阻 , 20.5k , 1% , 0.1W , AEC-Q200 0 级 , 0603

位号	数量	值	器件型号	制造商	说明
R43	1	10.0k	TNPW060310K0BEEA	Vishay-Dale	电阻, 10.0k, 0.1%, 0.1W, AEC-Q200 1 级, 0603
RT1	1	10k	TMP6131DECR	德州仪器 (TI)	具有 0402 和 0603 封装选项、容差为 $\pm 1\%$ 的 $10k\Omega$ 线性热敏电阻, 2X1SON, -40°C 到 125°C
U1	1		LM851772QRHARQ1	德州仪器 (TI)	具有 I2C 接口的 80V VIN 4 开关降压/升压控制器
C35	0	56pF	06035A560FAT2A	AVX	电容, 陶瓷, 56pF, 50V, +/-1%, C0G/NP0, 0603
C42	0	4700pF	C0603C472K5RACTU	Kemet	电容, 陶瓷, 4700pF, 50V, +/-10%, X7R, 0603
Q1A	0		IAUCN08S7L033ATMA1	Infineon	MOSFET N 沟道 80V 130A (Tj) 118W (Tc) 表面贴装 PG-TDSON-8-34
R7、R11、R36、R38	0	0	RMCF0603ZT0R00	Stackpole Electronics Inc	电阻, 0, 1%, 0.1W, AEC-Q200 0 级, 0603
R8	0	10	RC0603FR-0710RL	Yageo	电阻, 10.0, 1%, 0.1W, 0603
R12	0	82.0k	RC0603FR-0782KL	Yageo	电阻, 82.0k, 1%, 0.1W, 0603
R19	0	4.30k	RC0603FR-074K3L	Yageo	电阻, 4.30k, 1%, 0.1W, 0603
R30	0	40.2k	CRCW060340K2FKEA	Vishay-Dale	电阻, 40.2k, 1%, 0.1W, AEC-Q200 0 级, 0603
R39、R40	0	2.00k	RC0603FR-072KL	Yageo	电阻, 2.00k, 1%, 0.1W, 0603
R41	0	3.83k	CRCW06033K83FKEA	Vishay-Dale	电阻, 3.83k, 1%, 0.1W, AEC-Q200 0 级, 0603
R42	0	8.25k	RC0603FR-078K25L	Yageo	电阻, 8.25k, 1%, 0.1W, 0603

(1) Q3、Q3A、Q4 的替代器件：IAUC60N06S5L073

注意：EVM 修订版 B 更新：

- R37 设置为 20.5k
- 更新了警告标志上的丝印

5 其他信息

5.1 通用德州仪器 (TI) 高压评估模块 (TI HV EVM) 用户安全指南



务必遵循 TI 的设置和应用说明，包括在建议的电气额定电压和功率限制范围内使用所有接口元件。务必采取电气安全防护措施，确保自身和周围人员的人身安全。如需更多信息，请联系 TI 的产品信息中心 <http://ti.com/customer support>。

保存所有警告和说明以供将来参考。

警告

务必遵循警告和说明，否则可能引发电击和灼伤危险，进而造成财产损失或人员伤亡。

TI HV EVM 一词是指通常以开放式框架、敞开式印刷电路板装配形式提供的电子器件。该器件严格用于开发实验室环境，仅供了解开发和应用高压电路相关电气安全风险且接受过专门培训、具有专业知识背景的合格专业用户使用。德州仪器 (TI) 严禁任何其他不合规的使用和/或应用。如果不满足资格，则立即停止进一步使用 HV EVM。

1. 工作区安全：

- 保持工作区整洁有序。
- 每次电路通电时，都必须由具有资质的观察员在场监督。
- TI HV EVM 及接口电子元件通电区域必须设有有效的防护栏和标识；指示可能存在高压操作，以避免意外接触。
- 开发环境中使用的所有接口电路、电源、评估模块、仪器、仪表、示波器和其他相关装置如果超过 50Vrms/75VDC，则必须置于紧急断电 EPO 保护电源板内。
- 使用稳定且不导电的工作台。
- 使用充分绝缘的夹钳和导线来连接测量探针和仪器。尽量不要徒手进行测试。

2. 电气安全：

- 作为一项预防措施，假定整个 EVM 可能存在用户可完全接触到的高电压是一种好的工程做法。
- 执行任何电气测量或其他诊断测量之前，需切断 TI HV EVM 及其全部输入、输出和电气负载的电源。再次确认 TI HV EVM 已安全断电。
- 确认 EVM 断电后，根据所需的电路配置、接线、测量设备连接和其他应用需求执行进一步操作，同时仍假定 EVM 电路和测量仪器均带电。
- EVM 准备就绪后，根据需要将 EVM 通电。

警告

EVM 通电后，请勿触摸 EVM 或电路，因为 EVM 或电路可能存在高压，会造成电击危险。

3. 人身安全

- 穿戴人员防护装备（例如乳胶手套或具有侧护板的安全眼镜）或者用带有互锁机构的透明塑料箱装好 EVM，避免意外接触。

安全使用限制条件：

勿将 EVM 作为整体或部分生产单元使用。

5.2 商标

USB Type-C® is a registered trademark of USB Implementers Forum, Inc..

所有商标均为其各自所有者的财产。

6 修订历史记录

注：以前版本的页码可能与当前版本的页码不同

Changes from Revision * (July 2026) to Revision A (July 2026)	Page
• 在表 1-1 中添加了工作温度规格.....	2
• 更新了原理图：CFG2 设置的 R37 值.....	16
• 将物料清单中的 R37 更新为 20.5k.....	21

STANDARD TERMS FOR EVALUATION MODULES

1. **Delivery:** TI delivers TI evaluation boards, kits, or modules, including any accompanying demonstration software, components, and/or documentation which may be provided together or separately (collectively, an "EVM" or "EVMs") to the User ("User") in accordance with the terms set forth herein. User's acceptance of the EVM is expressly subject to the following terms.
 - 1.1 EVMs are intended solely for product or software developers for use in a research and development setting to facilitate feasibility evaluation, experimentation, or scientific analysis of TI semiconductors products. EVMs have no direct function and are not finished products. EVMs shall not be directly or indirectly assembled as a part or subassembly in any finished product. For clarification, any software or software tools provided with the EVM ("Software") shall not be subject to the terms and conditions set forth herein but rather shall be subject to the applicable terms that accompany such Software
 - 1.2 EVMs are not intended for consumer or household use. EVMs may not be sold, sublicensed, leased, rented, loaned, assigned, or otherwise distributed for commercial purposes by Users, in whole or in part, or used in any finished product or production system.
2. **Limited Warranty and Related Remedies/Disclaimers:**
 - 2.1 These terms do not apply to Software. The warranty, if any, for Software is covered in the applicable Software License Agreement.
 - 2.2 TI warrants that the TI EVM will conform to TI's published specifications for ninety (90) days after the date TI delivers such EVM to User. Notwithstanding the foregoing, TI shall not be liable for a nonconforming EVM if (a) the nonconformity was caused by neglect, misuse or mistreatment by an entity other than TI, including improper installation or testing, or for any EVMs that have been altered or modified in any way by an entity other than TI, (b) the nonconformity resulted from User's design, specifications or instructions for such EVMs or improper system design, or (c) User has not paid on time. Testing and other quality control techniques are used to the extent TI deems necessary. TI does not test all parameters of each EVM. User's claims against TI under this Section 2 are void if User fails to notify TI of any apparent defects in the EVMs within ten (10) business days after delivery, or of any hidden defects with ten (10) business days after the defect has been detected.
 - 2.3 TI's sole liability shall be at its option to repair or replace EVMs that fail to conform to the warranty set forth above, or credit User's account for such EVM. TI's liability under this warranty shall be limited to EVMs that are returned during the warranty period to the address designated by TI and that are determined by TI not to conform to such warranty. If TI elects to repair or replace such EVM, TI shall have a reasonable time to repair such EVM or provide replacements. Repaired EVMs shall be warranted for the remainder of the original warranty period. Replaced EVMs shall be warranted for a new full ninety (90) day warranty period.

WARNING

Evaluation Kits are intended solely for use by technically qualified, professional electronics experts who are familiar with the dangers and application risks associated with handling electrical mechanical components, systems, and subsystems.

User shall operate the Evaluation Kit within TI's recommended guidelines and any applicable legal or environmental requirements as well as reasonable and customary safeguards. Failure to set up and/or operate the Evaluation Kit within TI's recommended guidelines may result in personal injury or death or property damage. Proper set up entails following TI's instructions for electrical ratings of interface circuits such as input, output and electrical loads.

NOTE:

EXPOSURE TO ELECTROSTATIC DISCHARGE (ESD) MAY CAUSE DEGRADATION OR FAILURE OF THE EVALUATION KIT; TI RECOMMENDS STORAGE OF THE EVALUATION KIT IN A PROTECTIVE ESD BAG.

3 Regulatory Notices:

3.1 United States

3.1.1 Notice applicable to EVMs not FCC-Approved:

FCC NOTICE: This kit is designed to allow product developers to evaluate electronic components, circuitry, or software associated with the kit to determine whether to incorporate such items in a finished product and software developers to write software applications for use with the end product. This kit is not a finished product and when assembled may not be resold or otherwise marketed unless all required FCC equipment authorizations are first obtained. Operation is subject to the condition that this product not cause harmful interference to licensed radio stations and that this product accept harmful interference. Unless the assembled kit is designed to operate under part 15, part 18 or part 95 of this chapter, the operator of the kit must operate under the authority of an FCC license holder or must secure an experimental authorization under part 5 of this chapter.

3.1.2 For EVMs annotated as FCC – FEDERAL COMMUNICATIONS COMMISSION Part 15 Compliant:

CAUTION

This device complies with part 15 of the FCC Rules. Operation is subject to the following two conditions: (1) This device may not cause harmful interference, and (2) this device must accept any interference received, including interference that may cause undesired operation.

Changes or modifications not expressly approved by the party responsible for compliance could void the user's authority to operate the equipment.

FCC Interference Statement for Class A EVM devices

NOTE: This equipment has been tested and found to comply with the limits for a Class A digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. Operation of this equipment in a residential area is likely to cause harmful interference in which case the user will be required to correct the interference at his own expense.

FCC Interference Statement for Class B EVM devices

NOTE: This equipment has been tested and found to comply with the limits for a Class B digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates, uses and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following measures:

- Reorient or relocate the receiving antenna.
- Increase the separation between the equipment and receiver.
- Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.
- Consult the dealer or an experienced radio/TV technician for help.

3.2 Canada

3.2.1 For EVMs issued with an Industry Canada Certificate of Conformance to RSS-210 or RSS-247

Concerning EVMs Including Radio Transmitters:

This device complies with Industry Canada license-exempt RSSs. Operation is subject to the following two conditions:

(1) this device may not cause interference, and (2) this device must accept any interference, including interference that may cause undesired operation of the device.

Concernant les EVMs avec appareils radio:

Le présent appareil est conforme aux CNR d'Industrie Canada applicables aux appareils radio exempts de licence. L'exploitation est autorisée aux deux conditions suivantes: (1) l'appareil ne doit pas produire de brouillage, et (2) l'utilisateur de l'appareil doit accepter tout brouillage radioélectrique subi, même si le brouillage est susceptible d'en compromettre le fonctionnement.

Concerning EVMs Including Detachable Antennas:

Under Industry Canada regulations, this radio transmitter may only operate using an antenna of a type and maximum (or lesser) gain approved for the transmitter by Industry Canada. To reduce potential radio interference to other users, the antenna type and its gain should be so chosen that the equivalent isotropically radiated power (e.i.r.p.) is not more than that necessary for successful communication. This radio transmitter has been approved by Industry Canada to operate with the antenna types listed in the user guide with the maximum permissible gain and required antenna impedance for each antenna type indicated. Antenna types not included in this list, having a gain greater than the maximum gain indicated for that type, are strictly prohibited for use with this device.

Concernant les EVMs avec antennes détachables

Conformément à la réglementation d'Industrie Canada, le présent émetteur radio peut fonctionner avec une antenne d'un type et d'un gain maximal (ou inférieur) approuvé pour l'émetteur par Industrie Canada. Dans le but de réduire les risques de brouillage radioélectrique à l'intention des autres utilisateurs, il faut choisir le type d'antenne et son gain de sorte que la puissance isotrope rayonnée équivalente (p.i.r.e.) ne dépasse pas l'intensité nécessaire à l'établissement d'une communication satisfaisante. Le présent émetteur radio a été approuvé par Industrie Canada pour fonctionner avec les types d'antenne énumérés dans le manuel d'usage et ayant un gain admissible maximal et l'impédance requise pour chaque type d'antenne. Les types d'antenne non inclus dans cette liste, ou dont le gain est supérieur au gain maximal indiqué, sont strictement interdits pour l'exploitation de l'émetteur.

3.3 Japan

3.3.1 *Notice for EVMs delivered in Japan:* Please see http://www.tij.co.jp/sds/ti_ja/general/eStore/notice_01.page 日本国内に輸入される評価用キット、ボードについては、次のところをご覧ください。

<https://www.ti.com/ja-jp/legal/notice-for-evaluation-kits-delivered-in-japan.html>

3.3.2 *Notice for Users of EVMs Considered "Radio Frequency Products" in Japan:* EVMs entering Japan may not be certified by TI as conforming to Technical Regulations of Radio Law of Japan.

If User uses EVMs in Japan, not certified to Technical Regulations of Radio Law of Japan, User is required to follow the instructions set forth by Radio Law of Japan, which includes, but is not limited to, the instructions below with respect to EVMs (which for the avoidance of doubt are stated strictly for convenience and should be verified by User):

1. Use EVMs in a shielded room or any other test facility as defined in the notification #173 issued by Ministry of Internal Affairs and Communications on March 28, 2006, based on Sub-section 1.1 of Article 6 of the Ministry's Rule for Enforcement of Radio Law of Japan,
2. Use EVMs only after User obtains the license of Test Radio Station as provided in Radio Law of Japan with respect to EVMs, or
3. Use of EVMs only after User obtains the Technical Regulations Conformity Certification as provided in Radio Law of Japan with respect to EVMs. Also, do not transfer EVMs, unless User gives the same notice above to the transferee. Please note that if User does not follow the instructions above, User will be subject to penalties of Radio Law of Japan.

【無線電波を送信する製品の開発キットをお使いになる際の注意事項】 開発キットの中には技術基準適合証明を受けていないものがあります。技術適合証明を受けていないもののご使用に際しては、電波法遵守のため、以下のいずれかの措置を取っていただく必要がありますのでご注意ください。

1. 電波法施行規則第6条第1項第1号に基づく平成18年3月28日総務省告示第173号で定められた電波暗室等の試験設備でご使用いただく。
2. 実験局の免許を取得後ご使用いただく。
3. 技術基準適合証明を取得後ご使用いただく。

なお、本製品は、上記の「ご使用にあたっての注意」を譲渡先、移転先に通知しない限り、譲渡、移転できないものとします。

上記を遵守頂けない場合は、電波法の罰則が適用される可能性があることをご留意ください。 日本テキサス・インスツルメンツ株式会社
東京都新宿区西新宿 6 丁目 2 4 番 1 号
西新宿三井ビル

3.3.3 *Notice for EVMs for Power Line Communication:* Please see http://www.tij.co.jp/sds/ti_ja/general/eStore/notice_02.page

電力線搬送波通信についての開発キットをお使いになる際の注意事項については、次のところをご覧ください。<https://www.ti.com/ja-jp/legal/notice-for-evaluation-kits-for-power-line-communication.html>

3.4 European Union

3.4.1 *For EVMs subject to EU Directive 2014/30/EU (Electromagnetic Compatibility Directive):*

This is a class A product intended for use in environments other than domestic environments that are connected to a low-voltage power-supply network that supplies buildings used for domestic purposes. In a domestic environment this product may cause radio interference in which case the user may be required to take adequate measures.

4 EVM Use Restrictions and Warnings:

4.1 EVMS ARE NOT FOR USE IN FUNCTIONAL SAFETY AND/OR SAFETY CRITICAL EVALUATIONS, INCLUDING BUT NOT LIMITED TO EVALUATIONS OF LIFE SUPPORT APPLICATIONS.

4.2 User must read and apply the user guide and other available documentation provided by TI regarding the EVM prior to handling or using the EVM, including without limitation any warning or restriction notices. The notices contain important safety information related to, for example, temperatures and voltages.

4.3 Safety-Related Warnings and Restrictions:

4.3.1 User shall operate the EVM within TI's recommended specifications and environmental considerations stated in the user guide, other available documentation provided by TI, and any other applicable requirements and employ reasonable and customary safeguards. Exceeding the specified performance ratings and specifications (including but not limited to input and output voltage, current, power, and environmental ranges) for the EVM may cause personal injury or death, or property damage. If there are questions concerning performance ratings and specifications, User should contact a TI field representative prior to connecting interface electronics including input power and intended loads. Any loads applied outside of the specified output range may also result in unintended and/or inaccurate operation and/or possible permanent damage to the EVM and/or interface electronics. Please consult the EVM user guide prior to connecting any load to the EVM output. If there is uncertainty as to the load specification, please contact a TI field representative. During normal operation, even with the inputs and outputs kept within the specified allowable ranges, some circuit components may have elevated case temperatures. These components include but are not limited to linear regulators, switching transistors, pass transistors, current sense resistors, and heat sinks, which can be identified using the information in the associated documentation. When working with the EVM, please be aware that the EVM may become very warm.

4.3.2 EVMs are intended solely for use by technically qualified, professional electronics experts who are familiar with the dangers and application risks associated with handling electrical mechanical components, systems, and subsystems. User assumes all responsibility and liability for proper and safe handling and use of the EVM by User or its employees, affiliates, contractors or designees. User assumes all responsibility and liability to ensure that any interfaces (electronic and/or mechanical) between the EVM and any human body are designed with suitable isolation and means to safely limit accessible leakage currents to minimize the risk of electrical shock hazard. User assumes all responsibility and liability for any improper or unsafe handling or use of the EVM by User or its employees, affiliates, contractors or designees.

4.4 User assumes all responsibility and liability to determine whether the EVM is subject to any applicable international, federal, state, or local laws and regulations related to User's handling and use of the EVM and, if applicable, User assumes all responsibility and liability for compliance in all respects with such laws and regulations. User assumes all responsibility and liability for proper disposal and recycling of the EVM consistent with all applicable international, federal, state, and local requirements.

5. *Accuracy of Information:* To the extent TI provides information on the availability and function of EVMs, TI attempts to be as accurate as possible. However, TI does not warrant the accuracy of EVM descriptions, EVM availability or other information on its websites as accurate, complete, reliable, current, or error-free.

6. Disclaimers:

6.1 EXCEPT AS SET FORTH ABOVE, EVMS AND ANY MATERIALS PROVIDED WITH THE EVM (INCLUDING, BUT NOT LIMITED TO, REFERENCE DESIGNS AND THE DESIGN OF THE EVM ITSELF) ARE PROVIDED "AS IS" AND "WITH ALL FAULTS." TI DISCLAIMS ALL OTHER WARRANTIES, EXPRESS OR IMPLIED, REGARDING SUCH ITEMS, INCLUDING BUT NOT LIMITED TO ANY EPIDEMIC FAILURE WARRANTY OR IMPLIED WARRANTIES OF MERCHANTABILITY OR FITNESS FOR A PARTICULAR PURPOSE OR NON-INFRINGEMENT OF ANY THIRD PARTY PATENTS, COPYRIGHTS, TRADE SECRETS OR OTHER INTELLECTUAL PROPERTY RIGHTS.

6.2 EXCEPT FOR THE LIMITED RIGHT TO USE THE EVM SET FORTH HEREIN, NOTHING IN THESE TERMS SHALL BE CONSTRUED AS GRANTING OR CONFERRING ANY RIGHTS BY LICENSE, PATENT, OR ANY OTHER INDUSTRIAL OR INTELLECTUAL PROPERTY RIGHT OF TI, ITS SUPPLIERS/LICENSORS OR ANY OTHER THIRD PARTY, TO USE THE EVM IN ANY FINISHED END-USER OR READY-TO-USE FINAL PRODUCT, OR FOR ANY INVENTION, DISCOVERY OR IMPROVEMENT, REGARDLESS OF WHEN MADE, CONCEIVED OR ACQUIRED.

7. *USER'S INDEMNITY OBLIGATIONS AND REPRESENTATIONS.* USER WILL DEFEND, INDEMNIFY AND HOLD TI, ITS LICENSORS AND THEIR REPRESENTATIVES HARMLESS FROM AND AGAINST ANY AND ALL CLAIMS, DAMAGES, LOSSES, EXPENSES, COSTS AND LIABILITIES (COLLECTIVELY, "CLAIMS") ARISING OUT OF OR IN CONNECTION WITH ANY HANDLING OR USE OF THE EVM THAT IS NOT IN ACCORDANCE WITH THESE TERMS. THIS OBLIGATION SHALL APPLY WHETHER CLAIMS ARISE UNDER STATUTE, REGULATION, OR THE LAW OF TORT, CONTRACT OR ANY OTHER LEGAL THEORY, AND EVEN IF THE EVM FAILS TO PERFORM AS DESCRIBED OR EXPECTED.

8. *Limitations on Damages and Liability:*

8.1 *General Limitations.* IN NO EVENT SHALL TI BE LIABLE FOR ANY SPECIAL, COLLATERAL, INDIRECT, PUNITIVE, INCIDENTAL, CONSEQUENTIAL, OR EXEMPLARY DAMAGES IN CONNECTION WITH OR ARISING OUT OF THESE TERMS OR THE USE OF THE EVMS , REGARDLESS OF WHETHER TI HAS BEEN ADVISED OF THE POSSIBILITY OF SUCH DAMAGES. EXCLUDED DAMAGES INCLUDE, BUT ARE NOT LIMITED TO, COST OF REMOVAL OR REINSTALLATION, ANCILLARY COSTS TO THE PROCUREMENT OF SUBSTITUTE GOODS OR SERVICES, RETESTING, OUTSIDE COMPUTER TIME, LABOR COSTS, LOSS OF GOODWILL, LOSS OF PROFITS, LOSS OF SAVINGS, LOSS OF USE, LOSS OF DATA, OR BUSINESS INTERRUPTION. NO CLAIM, SUIT OR ACTION SHALL BE BROUGHT AGAINST TI MORE THAN TWELVE (12) MONTHS AFTER THE EVENT THAT GAVE RISE TO THE CAUSE OF ACTION HAS OCCURRED.

8.2 *Specific Limitations.* IN NO EVENT SHALL TI'S AGGREGATE LIABILITY FROM ANY USE OF AN EVM PROVIDED HEREUNDER, INCLUDING FROM ANY WARRANTY, INDEMNITY OR OTHER OBLIGATION ARISING OUT OF OR IN CONNECTION WITH THESE TERMS, , EXCEED THE TOTAL AMOUNT PAID TO TI BY USER FOR THE PARTICULAR EVM(S) AT ISSUE DURING THE PRIOR TWELVE (12) MONTHS WITH RESPECT TO WHICH LOSSES OR DAMAGES ARE CLAIMED. THE EXISTENCE OF MORE THAN ONE CLAIM SHALL NOT ENLARGE OR EXTEND THIS LIMIT.

9. *Return Policy.* Except as otherwise provided, TI does not offer any refunds, returns, or exchanges. Furthermore, no return of EVM(s) will be accepted if the package has been opened and no return of the EVM(s) will be accepted if they are damaged or otherwise not in a resalable condition. If User feels it has been incorrectly charged for the EVM(s) it ordered or that delivery violates the applicable order, User should contact TI. All refunds will be made in full within thirty (30) working days from the return of the components(s), excluding any postage or packaging costs.
10. *Governing Law:* These terms and conditions shall be governed by and interpreted in accordance with the laws of the State of Texas, without reference to conflict-of-laws principles. User agrees that non-exclusive jurisdiction for any dispute arising out of or relating to these terms and conditions lies within courts located in the State of Texas and consents to venue in Dallas County, Texas. Notwithstanding the foregoing, any judgment may be enforced in any United States or foreign court, and TI may seek injunctive relief in any United States or foreign court.

Mailing Address: Texas Instruments, Post Office Box 655303, Dallas, Texas 75265
Copyright © 2023, Texas Instruments Incorporated

重要通知和免责声明

TI“按原样”提供技术和可靠性数据（包括数据表）、设计资源（包括参考设计）、应用或其他设计建议、网络工具、安全信息和其他资源，不保证没有瑕疵且不做任何明示或暗示的担保，包括但不限于对适销性、与某特定用途的适用性或不侵犯任何第三方知识产权的暗示担保。

这些资源可供使用 TI 产品进行设计的熟练开发人员使用。您将自行承担以下全部责任：(1) 针对您的应用选择合适的 TI 产品，(2) 设计、验证并测试您的应用，(3) 确保您的应用满足相应标准以及任何其他安全、安保法规或其他要求。

这些资源如有变更，恕不另行通知。TI 授权您仅可将这些资源用于研发本资源所述的 TI 产品的相关应用。严禁以其他方式对这些资源进行复制或展示。您无权使用任何其他 TI 知识产权或任何第三方知识产权。对于因您对这些资源的使用而对 TI 及其代表造成的任何索赔、损害、成本、损失和债务，您将全额赔偿，TI 对此概不负责。

TI 提供的产品受 [TI 销售条款](#)、[TI 通用质量指南](#) 或 [ti.com](#) 上其他适用条款或 TI 产品随附的其他适用条款的约束。TI 提供这些资源并不会扩展或以其他方式更改 TI 针对 TI 产品发布的适用的担保或担保免责声明。除非德州仪器 (TI) 明确将某产品指定为定制产品或客户特定产品，否则其产品均为按确定价格收入目录的标准通用器件。

TI 反对并拒绝您可能提出的任何其他或不同的条款。

版权所有 © 2026，德州仪器 (TI) 公司

最后更新日期：2025 年 10 月