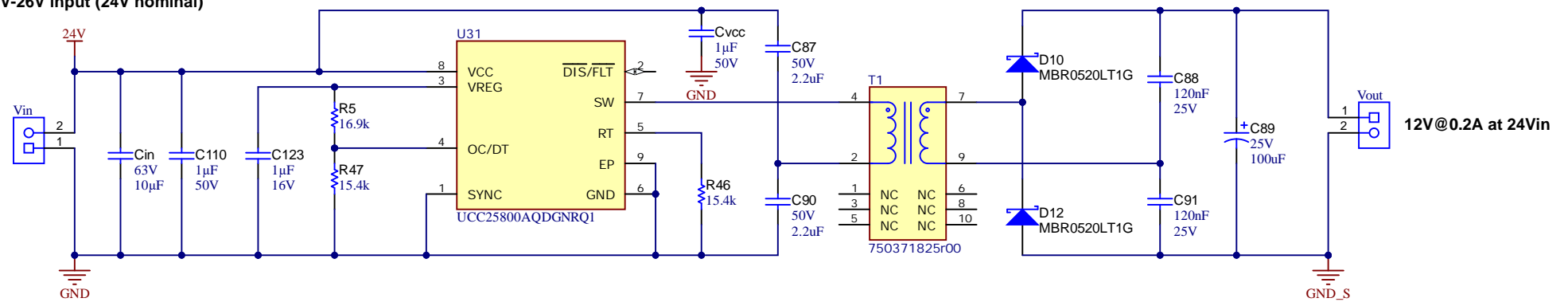


22V-26V input (24V nominal)



Assembly note:

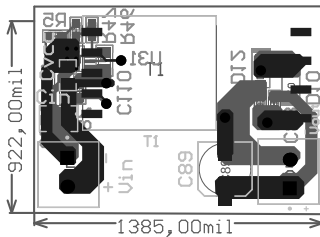
Built on PMP31333 Rev_A PCB

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Orderable:	Designed for: Public Release	Mod. Date: 10/12/2024
TID #: N/A	Project Title: LLC Resonant Converter	
Number: PMP31333	Rev: A	Sheet Title: Isolated 12V@0.2A LLC Converter
SVN Rev: Not in version control	Assembly Variant: [No Variations]	Sheet: 1 of 1
Drawn By: Ohad Bonne	File: PMP31333 Rev_A Schematic.SchDoc	Size: A4
Engineer: Bonneh / Scibilia	Contact: http://www.ti.com/support	

Layer	Name	Material	Thickness	Constant	Board Layer Stack
	Top Overlay				
	Top Solder	Solder Resist	0,40mil	3.5	
1	Top Layer		1,40mil		
	Dielectric1	FR-4 High Tg	59,20mil	4.8	
2	Bottom Layer		1,40mil		
	Bottom Solder	Solder Resist	0,40mil	3.5	
	Bottom Overlay				

DESIGN INFORMATION	
MIN. TRACK WIDTH: <u>10</u> MIL	
MIN. CLEARANCE: <u>7.874</u> MIL	
MIN. VIA PAD SIZE: <u>24</u> MIL	
MINIMUM ANNULAR RING 0.05mm (2MIL) EXTERNAL	
PER IPC-D-275 CLASS 2 LEVEL C	
REGISTRATION TOLERANCES: METAL +/- <u>5</u> MIL, HOLES +/- <u>3</u> MIL	
HOLE SIZE TOLERANCE (UNLESS OTHERWISE SPECIFIED): +/- <u>3</u> MIL	
MATERIAL:	
<input type="checkbox"/> FR-4 <input checked="" type="checkbox"/> FR-4 High Tg <input type="checkbox"/> OTHER _____	
THICKNESS: <input checked="" type="checkbox"/> 62 MIL (1.6mm) +/-10% <input type="checkbox"/> OTHER _____	
TOLERANCE: <input checked="" type="checkbox"/> ANSI IPC-6012 TYPE 3 CLASS 2	
<input type="checkbox"/> OTHER +/- _____	
BOW & TWIST: <input checked="" type="checkbox"/> ANSI IPC-6012 TYPE 3 CLASS 2	
<input type="checkbox"/> OTHER +/- _____	
DRILLING:	
REFERENCE: <input checked="" type="checkbox"/> AS SHOWN <input checked="" type="checkbox"/> NC_DRILL FILES	
PTH COPPER THICKNESS: <input checked="" type="checkbox"/> 20-30 um <input type="checkbox"/> OTHER _____	
BOARD FINISH:	
SILKSCREEN: <input checked="" type="checkbox"/> TOP <input checked="" type="checkbox"/> BOTTOM	
SILKSCREEN COLOR: <input checked="" type="checkbox"/> WHITE <input type="checkbox"/> OTHER _____	
SOLDER RESIST COLOR: <input checked="" type="checkbox"/> GREEN <input type="checkbox"/> OTHER _____	
<input checked="" type="checkbox"/> MATTE <input type="checkbox"/> SEMI-GLOSS	
SURFACE FINISH: <input checked="" type="checkbox"/> IMMERSION GOLD (ENIG) <input type="checkbox"/> ENIG	
<input type="checkbox"/> IMM. TIN/SILVER OR EQUIV <input type="checkbox"/> OTHER _____	
ARRAY/PANEL: <input type="checkbox"/> CUT AND TRIM PER M1 BOARD OUTLINE	
<input type="checkbox"/> N.C. ROUTE <input checked="" type="checkbox"/> V. SCORE	
CERTIFICATION: MATERIALS AND WORKMANSHIP FOR ALL PCBs	
TO MEET OR EXCEED THE REQUIREMENTS OF:	
<input checked="" type="checkbox"/> ANSI IPC-A-600F CLASS -> <input type="checkbox"/> 1 <input checked="" type="checkbox"/> 2 <input type="checkbox"/> 3	
<input checked="" type="checkbox"/> RoHS <input type="checkbox"/> OTHER _____ PER ORDER	
ALL BOARDS MUST MEET OR EXCEED UL94-V0 REQUIREMENTS.	
PCB MUST BEAR THE UL94V-0 UL REGISTERED MATERIAL ID NUMBER	
ADDITIONAL REQUIREMENTS:	
MICROSECTION: <input type="checkbox"/> YES	
BARE BOARD ELEC. TEST: <input type="checkbox"/> NONE <input checked="" type="checkbox"/> REQUIRED <input type="checkbox"/> PER ORDER	
<input type="checkbox"/> XX MIL VIAS REQUIRE NON-CONDUCTIVE FILL AND PLANARIZE	
<input type="checkbox"/> XX MIL VIAS REQUIRE CONDUCTIVE FILL AND PLANARIZE	
<input type="checkbox"/> OUTER XX MIL TRACES REQUIRE 50 OHM SINGLE-ENDED IMPEDANCE	
<input type="checkbox"/> LAYER 2 & 3 (INNER LAYERS) XX MIL WIDE, XX MIL SPACE	
TRACES REQUIRE 100 OHM DIFFERENTIAL IMPEDANCE	
PROJECT TITLE:	
LLC Resonant Converter	
DESIGNED FOR:	
Public Release	
FILE NAME:	
PMP31333 Rev_A PCB.PcbDoc	
ENGINEER:	LAYOUT BY:
Bonneh / Scibilia	Ohad Bonneh
SCALE: 1.00	ALTUM DESIGNER VERSION:
	24.10.1.45



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