

具有USB接口、单端模拟输入/输出和S/PDIF的立体声音频编解码器

查询样品: PCM2906C

特性

- 片载 USB 接口:
 - 具有全速收发器
 - 完全符合 USB 2.0 规范
 - 由 USB-IF 认证
 - 用于回放的 **USB** 自适应模式
 - 用于记录的 **USB** 异步模式
 - 总线供电
- 16 位 Δ-Σ ADC 和 DAC
- 采样速率:
 - DAC: 32, 44.1, 48 kHz
 - ADC:
 - 8, 11.025, 16, 22.05, 32, 44.1, 48 kHz
- 具有单个 12-MHz 时钟源的片载时钟发生器
- **S/PDIF** 输入/输出
- 单电源:
 - 5 V 典型值 (V_{BUS})
- 立体声 ADC:
 - **V**_{BUS} 时的模拟性能 **= 5V**:
 - THD+N = 0.01%
 - SNR = 89 dB
 - 动态范围 = 89 dB
 - 数字抽取滤波器:
 - 通频带纹波 = ±0.05 dB
 - 阻带衰减 = -65 dB
 - 单端电压输入
 - 包含抗混淆滤波器
 - 包含数字 HPF

- 立体声 DAC:
 - V_{BUS} 上的模拟性能 = 5V:
 - THD+N = 0.005%
 - SNR = 96 dB
 - 动态范围 = 93 dB
 - 过采样数字滤波器:
 - 通频带纹波 = ±0.1 dB
 - 阻带衰减 **= −43 dB**
 - 单端电压输出
 - 包含模拟 LPF
- 多功能:
 - 人机接口 (HID) 功能:
 - 音量控制和静音
 - 终止标识功能
- 28-引脚 SSOP 封装

应用

- USB 音频扬声器
- USB 耳机
- USB 显示器
- USB 音频接口盒

说明

PCM2906C 是德州仪器的含有一个USB兼容全速协议 控制器和S/PDIF的单片, USB, 立体声编码器。 USB 协议控制器无需软件编码。 PCM2906C 采用 SpAct™ 架构, 这是 TI 用于从 USB 数据包数据恢复音频时钟的独特系统。 采用SpAct 的片载模拟PLL支持具有低时钟抖动以及独立回放和录音采样率的回放和录音。



Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet.

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This integrated circuit can be damaged by ESD. Texas Instruments recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage.

ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.

PACKAGING/ORDERING INFORMATION(1)

PRODUCT	PACKAGE-LEAD	PACKAGE DESIGNATOR	SPECIFIED TEMPERATURE RANGE	PACKAGE MARKING	ORDERING NUMBER	TRANSPORT MEDIA, QUANTITY
					PCM2906CDB	Rails, 47
PCM2906CDB	SSOP-28	DB	–25°C to +85°C	PCM2906C	PCM2906CDBR	Tape and Reel, 2000

⁽¹⁾ For the most current package and ordering information, see the Package Option Addendum at the end of this document, or see the TI website at www.ti.com.

ABSOLUTE MAXIMUM RATINGS(1)

Over operating free-air temperature range (unless otherwise noted).

	PARAMETER	PCM2906C	UNIT	
Supply voltage, V _{BUS}		-0.3 to 6.5	V	
Ground voltage difference	s, AGNDC, AGNDP, AGNDX, DGND, DGNDU	±0.1	V	
Digital insult valtage	SEL0, SEL1, DIN	-0.3 to 6.5	V	
Digital input voltage	D+, D-, HID0, HID1, HID2, XTI, XTO, DOUT, SSPND	-0.3 to $(V_{DDI} + 0.3) < 4$	V	
Analan innutualtana	V _{IN} L, V _{IN} R, V _{COM} , V _{OUT} R, V _{OUT} L	-0.3 to $(V_{CCCI} + 0.3) < 4$	V	
Analog input voltage	V _{CCCI} , V _{CCP1I} , V _{CCP2I} , V _{CCXI} , V _{DDI}	-0.3 to 4	V	
Input current (any pins ex	cept supplies)	±10	mA	
Ambient temperature unde	er bias	-40 to +125	°C	
Storage temperature, T _{stg}		-55 to +150	°C	
Junction temperature, T _J		+150	°C	
Lead temperature (solderi	ng, 5s)	+260	°C	
Package temperature (IR	reflow, peak)	+250	°C	

⁽¹⁾ Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

THERMAL INFORMATION

		PCM2906C	
	THERMAL METRIC ⁽¹⁾	DB (SSOP)	UNITS
		28 PINS	
θ_{JA}	Junction-to-ambient thermal resistance	64.5	
θ_{JCtop}	Junction-to-case (top) thermal resistance	24.5	
θ_{JB}	Junction-to-board thermal resistance	25.4	°C // //
ΨЈТ	Junction-to-top characterization parameter	2.0	°C/W
ΨЈВ	Junction-to-board characterization parameter	25.0	
θ_{JCbot}	Junction-to-case (bottom) thermal resistance	N/A	

(1) 有关传统和新的热度量的更多信息,请参阅 IC 封装热度量 应用报告 SPRA953。



ELECTRICAL CHARACTERISTICS

All specifications at $T_A = +25$ °C, V_{BUS} , = 5 V, $f_S = 44.1$ kHz, $f_{IN} = 1$ kHz, and 16-bit data (unless otherwise noted).

			PC	PCM2906C			
	PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNIT	
DIGITAL IN	NPUT/OUTPUT						
	Host interface	Apply USB Revision 2.0, full speed					
	Audio data format	USB isochronous data format					
NPUT LO	GIC						
V _{IH} ⁽¹⁾			2		3.3		
V _{IL} ⁽¹⁾					0.8		
/ _{IH} ^{(2) (3)}			2.52		3.3		
/ _{IL} (2) (3)					0.9		
/ _{IH} ⁽⁴⁾	Input logic level		2		5.25	VDC	
/ _{IL} ⁽⁴⁾					0.8		
/ _{IH} ⁽⁵⁾			2.52		5.25		
/ _{IL} ⁽⁵⁾					0.9		
H ⁽¹⁾⁽²⁾⁽⁴⁾		V _{IN} = 3.3 V			±10	μΑ	
L (1)(2)(4)		V _{IN} = 0 V			±10		
(3)		V _{IN} = 3.3 V		50	80		
L (3)	Input logic current	V _{IN} = 0 V			±10		
H ⁽⁵⁾		V _{IN} = 3.3 V		65	100		
L ⁽⁵⁾		V _{IN} = 0 V			±10		
OUTPUT L	OGIC				•		
/ _{OH} ⁽¹⁾			2.8				
/ _{OL} ⁽¹⁾					0.3		
/OH (6)	Output landa laural	$I_{OH} = -4 \text{ mA}$	2.8			\/50	
/ _{OL} ⁽⁶⁾	Output logic level	I _{OL} = 4 mA			0.5	VDC	
OH (7)		$I_{OH} = -2 \text{ mA}$	2.8				
/ _{OL} ⁽⁷⁾		I _{OL} = 2 mA			0.5		
	REQUENCY				!		
	Input clock frequency, XTI		11.994	12	12.006	MHz	

⁽¹⁾ Pins 1, 2: D+, D-. (2) Pin 21: XTI. (3) Pins 5, 6, 7: HID0, HID1, HID2. (4) Pins 8, 9: SEL0, SEL1. (5) Pin 24: DN.

⁽⁶⁾ Pin 25: DOUT. (7) Pin 28: SSPND.

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ELECTRICAL CHARACTERISTICS (continued)

All specifications at $T_A = +25$ °C, V_{BUS} , = 5 V, $f_S = 44.1$ kHz, $f_{IN} = 1$ kHz, and 16-bit data (unless otherwise noted).

			PCM2906C			
	PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNIT
ADC CH	ARACTERISTICS				*	
	Resolution			8, 16		Bits
	Audio data channel			1, 2		Channel
ADC Clo	ck Frequency		,			
fs	Sampling frequency		8, 11.025, 16,	22.05, 32, 44	1.1, 48	kHz
ADC DC	Accuracy				*	
	Gain mismatch, channel-to-channel			±1	±5	% of FSR
	Gain error			±2	±10	% of FSR
	Bipolar zero error			±0		% of FSR
ADC Dyr	namic Performance ⁽⁸⁾				1	
		$V_{IN} = -1 \text{ dB}^{(9)}, V_{CCCI} = 3.67 \text{ V}$		0.01	0.02	%
THD+N	Total harmonic distortion plus noise	$V_{IN} = -1 dB^{(10)}$		0.1		%
	pido noise	$V_{IN} = -60 \text{ dB}$		5		%
	Dynamic range	A-weighted	81	89		dB
SNR	Signal-to-noise ratio	A-weighted	81	89		dB
	Channel separation		80	85		dB
Analog I	nput		<u>.</u>			
	Input voltage			0.6 V _{CCCI}		V _{PP}
	Center voltage			0.5 V _{CCCI}		V
	Input impedance			30		kΩ
	Antialiasing filter frequency	–3 dB	150			kHz
	response			-0.08		dB
ADC Dig	ital Filter Performance					
	Passband				0.454 f _s	Hz
	Stop band		0.583 f _s			Hz
	Passband ripple				±0.05	dB
	Stop-band attenuation		-65			dB
td	Delay time			17.4/f _s		S
	HPF frequency response	–3 dB	0.0	078f _s /1000		Hz

⁽⁸⁾ f_{IN} = 1 kHz, using the System Two™ audio measurement system by Audio Precision™ in RMS mode with 20-kHz LPF, 400-Hz HPF in calculation.

⁽⁹⁾ Using external voltage regulator for V_{CCCI} (see Figure 36).

⁽¹⁰⁾ Using internal voltage regulator for V_{CCCI} (see Figure 37).



ELECTRICAL CHARACTERISTICS (continued)

All specifications at $T_A = +25^{\circ}$ C, $V_{BUS} = 5$ V, $f_S = 44.1$ kHz, $f_{IN} = 1$ kHz, and 16-bit data (unless otherwise noted).

			PC	CM2906C		
	PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNIT
DAC CH	ARACTERISTICS				*	
	Resolution			8, 16		Bits
	Audio data channel			1, 2		Channel
DAC Clo	ck Frequency		1			
f _s	Sampling frequency		3	2, 44.1, 48		kHz
DAC DC	Accuracy		*			
	Gain mismatch, channel-to-channel			±1	±5	% of FSR
	Gain error			±2	±10	% of FSR
	Bipolar zero error			±2		% of FSR
DAC Dyr	namic Performance ⁽¹¹⁾		1			
TUD . N	Total harmonic distortion	V _{OUT} = 0 dB		0.005	0.016	%
THD+N	plus noise	$V_{OUT} = -60 \text{ dB}$		3		%
	Dynamic range	EIAJ, A-weighted	87	93		dB
SNR	Signal-to-noise ratio	EIAJ, A-weighted	90	96		dB
	Channel separation		86	92		dB
Analog C	Output				*	
V _O	Output voltage			0.6 V _{CCCI}		V_{PP}
	Center voltage			0.5 V _{CCCI}		V
	Load impedance	AC coupling	10			kΩ
	LDE (–3 dB		250		kHz
	LPF frequency response	f = 20 kHz		-0.03		dB
DAC Dig	ital Filter Performance		<u> </u>			
	Passband				0.445 f _s	Hz
	Stop band		0.555 f _s			Hz
	Passband ripple				±0.1	dB
	Stop-band attenuation		-43			dB
t _d	Delay time			14.3 f _s		s
POWER-	SUPPLY REQUIREMENTS		<u> </u>			
V _{BUS}	Voltage range		4.35	5	5.25	VDC
	Committee accument	ADC, DAC operation		56	67	mA
	Supply current	Suspend mode ⁽¹²⁾		250		μΑ
Б	Devices disable of the	ADC, DAC operation		280	352	mW
P_D	Power dissipation	Suspend mode ⁽¹²⁾		1.25		mW
	Internal power-supply voltage (13)		3.1	3.3	3.5	VDC
TEMPER	ATURE RANGE					
	Operating temperature rang	ne	-25		+85	°C

⁽¹¹⁾ f_{OUT} = 1 kHz, using the System Two audio measurement system by Audio Precision in RMS mode with 20-kHz LPF, 400-Hz HPF.
(12) In USB suspend state.
(13) Pins 10, 17, 19, 23, 27: V_{CCCI}, V_{CCP1I}, V_{CCP2I}, V_{CCXI}, V_{DDI}.

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PIN ASSIGNMENTS

DB PACKAGE SSOP-28 (TOP VIEW)

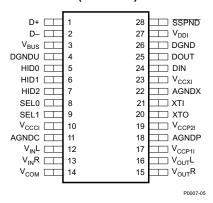
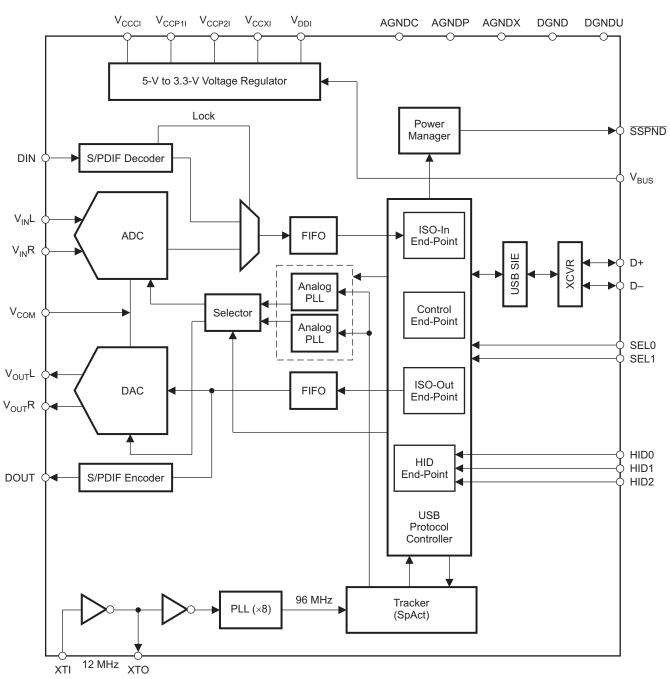


Table 1. TERMINAL FUNCTIONS

NAME	NO.	I/O	DESCRIPTION			
AGNDC	11	_	Analog ground for codec			
AGNDP	18	_	Analog ground for PLL			
AGNDX	22	_	nalog ground for oscillator			
D-	2	I/O	USB differential input/output minus ⁽¹⁾			
D+	1	I/O	USB differential input/output plus ⁽¹⁾			
DGND	26	_	Digital ground			
DGNDU	4	_	Digital ground for USB transceiver			
DIN	24	I	S/PDIF input ⁽²⁾			
DOUT	25	0	S/PDIF output			
HID0	5	I	HID key state input (mute), active-high ⁽³⁾			
HID1	6	I	HID key state input (volume up), active-high ⁽³⁾			
HID2	7	I	HID key state input (volume down), active-high ⁽³⁾			
SEL0	8	I	Must be set to high (4)			
SEL1	9	I	Must be set to high (4)			
SSPND	28	0	Suspend flag, active-low (Low: suspend, High: operational)			
V _{BUS}	3	_	Connect to USB power (V _{BUS})			
V _{CCCI}	10	_	Internal analog power supply for codec ⁽⁵⁾			
V _{CCP1I}	17	_	Internal analog power supply for PLL (5)			
V _{CCP2I}	19	_	Internal analog power supply for PLL (5)			
V _{CCXI}	23	_	Internal analog power supply for oscillator ⁽⁵⁾			
V _{COM}	14	_	Common for ADC/DAC (V _{CCCI} /2) ⁽⁵⁾			
V_{DDI}	27	_	Internal digital power supply ⁽⁵⁾			
V _{IN} L	12	I	ADC analog input for L-channel			
V _{IN} R	13	I	ADC analog input for R-channel			
V _{OUT} L	16	0	DAC analog output for L-channel			
V _{OUT} R	15	0	DAC analog output for R-channel			
XTI	21	I	Crystal oscillator input ⁽⁶⁾			
XTO	20	0	Crystal oscillator output			

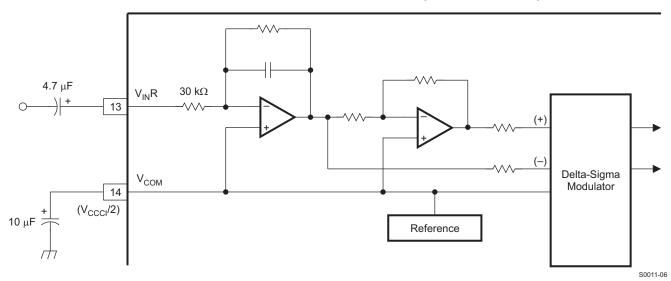
- (1) LV-TTL level.
- (2) 3.3-V CMOS-level input with internal pulldown, 5-V tolerant.
- (3) 3.3-V CMOS-level input with internal pulldown. This pin informs the PC of serviceable control signals such as mute, volume up, or volume down, which have no direct connection with the internal DAC or ADC. See the *Interface* #3 and *End-Points* sections.
- (4) TTL Schmitt trigger, 5-V tolerant.
- (5) Connect a decoupling capacitor to GND.
- (6) 3.3-V CMOS-level input.

FUNCTIONAL BLOCK DIAGRAM





BLOCK DIAGRAM OF ANALOG FRONT-END (RIGHT CHANNEL)





TYPICAL CHARACTERISTICS: ADC

All specifications at $T_A = +25$ °C, $V_{BUS} = 5$ V, $f_s = 44.1$ kHz, $f_{IN} = 1$ kHz, 16-bit data, using REG103xA-A, unless otherwise noted.

Dynamic Range and SNR - dB

TOTAL HARMONIC DISTORTION + NOISE AT -1 dB

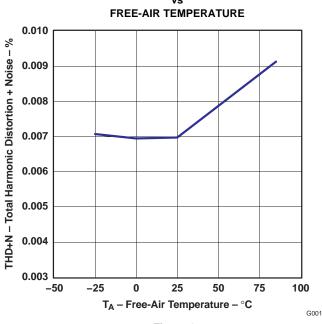


Figure 1.

TOTAL HARMONIC DISTORTION + NOISE AT -1 dB vs

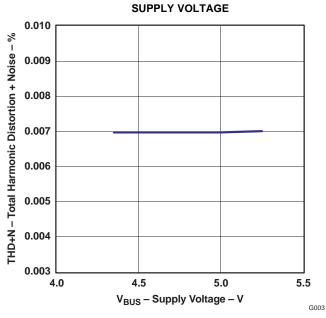


Figure 3.

DYNAMIC RANGE and SNR vs

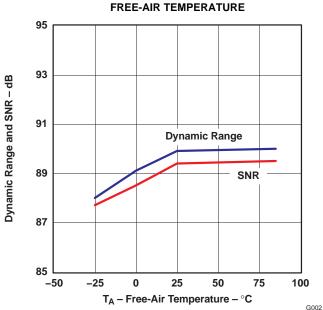


Figure 2.

DYNAMIC RANGE and SNR vs

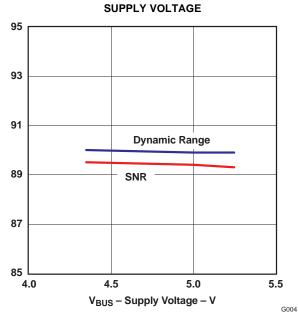


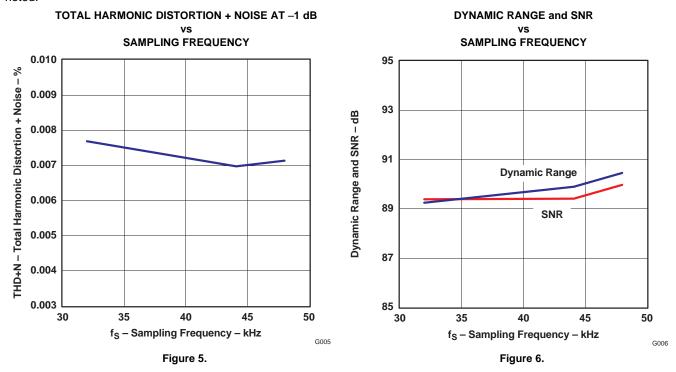
Figure 4.

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TEXAS INSTRUMENTS

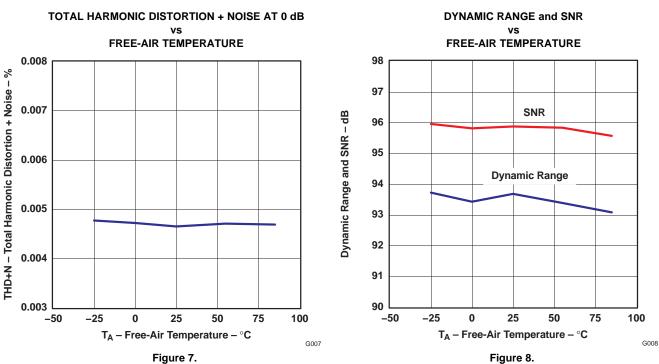
TYPICAL CHARACTERISTICS: ADC (continued)

All specifications at $T_A = +25$ °C, $V_{BUS} = 5$ V, $f_s = 44.1$ kHz, $f_{IN} = 1$ kHz, 16-bit data, using REG103xA-A, unless otherwise noted.



TYPICAL CHARACTERISTICS: DAC

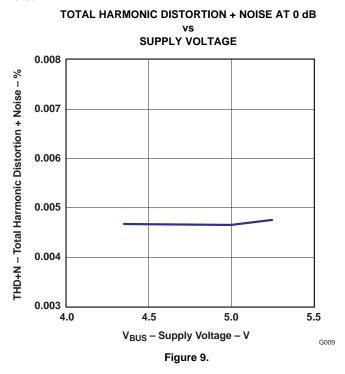
All specifications at $T_A = +25^{\circ}C$, $V_{BUS} = 5$ V, $f_s = 44.1$ kHz, $f_{IN} = 1$ kHz, 16-bit data, using REG103xA-A, unless otherwise noted.

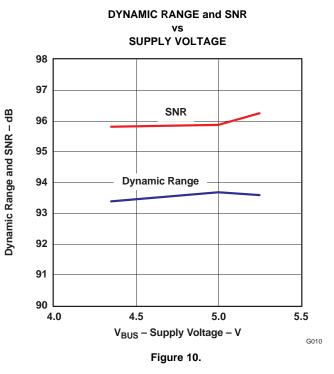




TYPICAL CHARACTERISTICS: DAC (continued)

All specifications at $T_A = +25$ °C, $V_{BUS} = 5$ V, $f_s = 44.1$ kHz, $f_{IN} = 1$ kHz, 16-bit data, using REG103xA-A, unless otherwise noted.





TOTAL HARMONIC DISTORTION + NOISE AT 0 dB
vs
SAMPLING FREQUENCY

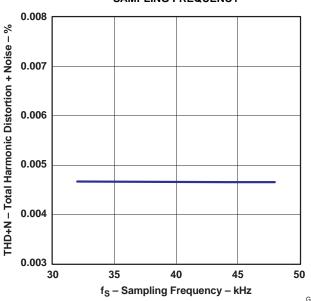


Figure 11.

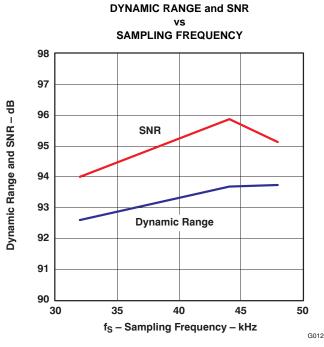


Figure 12.

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TYPICAL CHARACTERISTICS: SUPPLY CURRENT

All specifications at $T_A = +25^{\circ}C$, $V_{BUS} = 5$ V, $f_s = 44.1$ kHz, $f_{IN} = 1$ kHz, 16-bit data, using REG103xA-A, unless otherwise noted.

OPERATIONAL and SUSPEND SUPPLY CURRENT

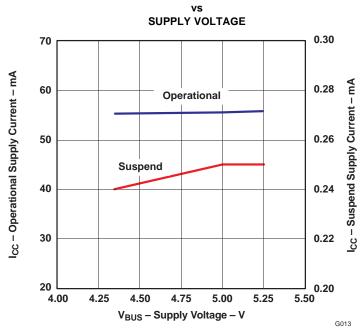
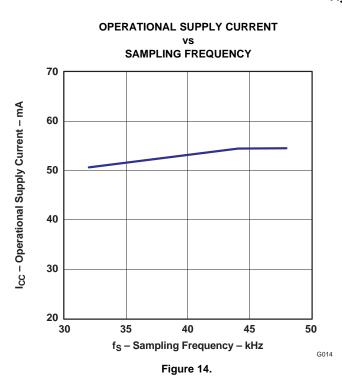
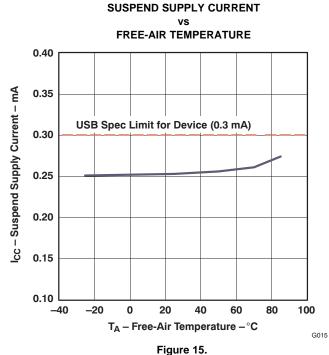


Figure 13.

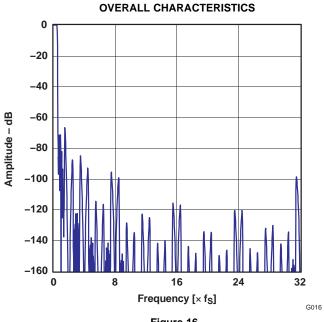


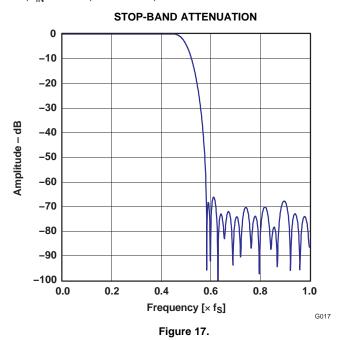




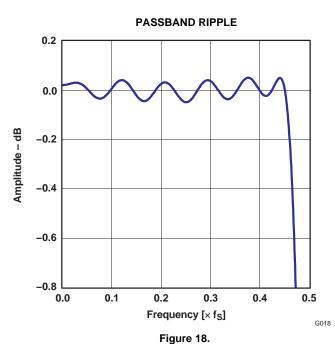
TYPICAL CHARACTERISTICS: ADC DIGITAL DECIMATION FILTER FREQUENCY RESPONSE

All specifications at $T_A = +25$ °C, $V_{BUS} = 5$ V, $f_s = 44.1$ kHz, $f_{IN} = 1$ kHz, 16-bit data, unless otherwise noted.









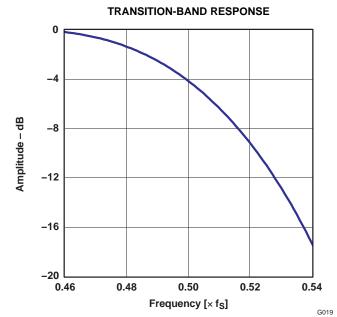


Figure 19.

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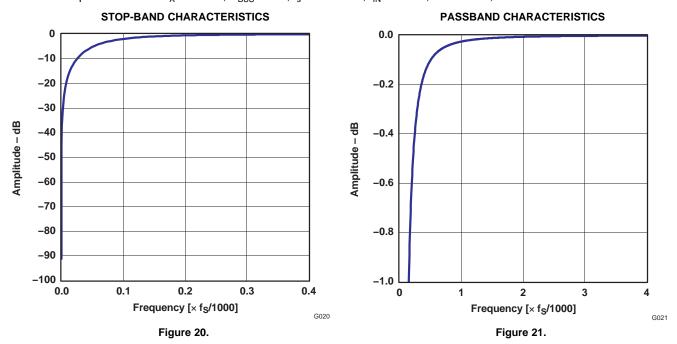


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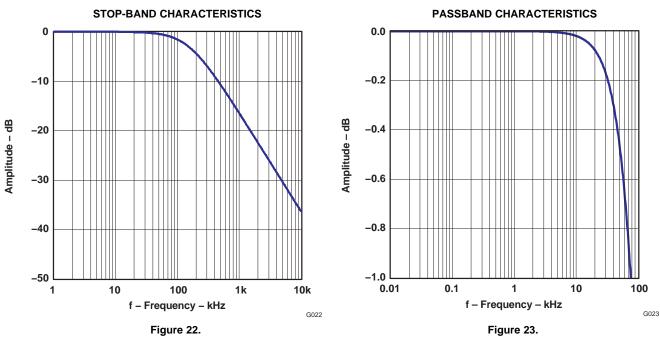
TYPICAL CHARACTERISTICS: ADC DIGITAL HIGH-PASS FILTER FREQUENCY RESPONSE

All specifications at $T_A = +25$ °C, $V_{BUS} = 5$ V, $f_s = 44.1$ kHz, $f_{IN} = 1$ kHz, 16-bit data, unless otherwise noted.



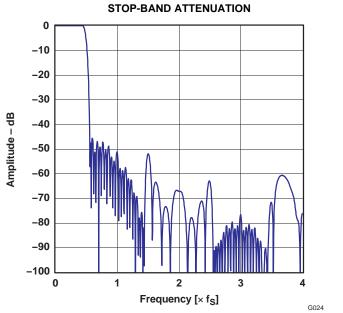
TYPICAL CHARACTERISTICS: ADC ANALOG ANTIALIASING FILTER FREQUENCY RESPONSE

All specifications at $T_A = +25$ °C, $V_{BUS} = 5$ V, $f_s = 44.1$ kHz, $f_{IN} = 1$ kHz, 16-bit data, unless otherwise noted.



TYPICAL CHARACTERISTICS: DAC DIGITAL INTERPOLATION FILTER FREQUENCY RESPONSE

All specifications at $T_A = +25$ °C, $V_{BUS} = 5$ V, $f_s = 44.1$ kHz, $f_{IN} = 1$ kHz, 16-bit data, unless otherwise noted.



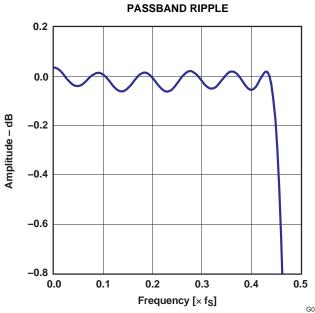
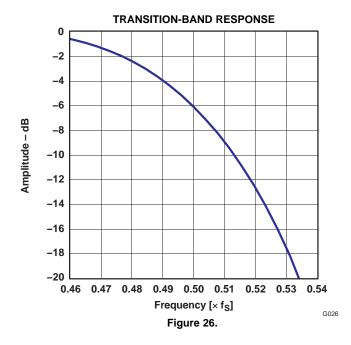


Figure 24. Figure 25.



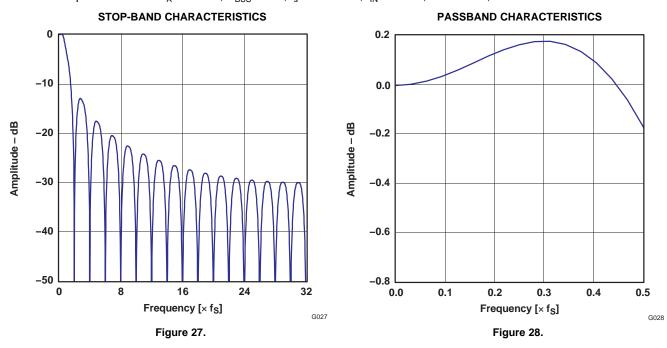


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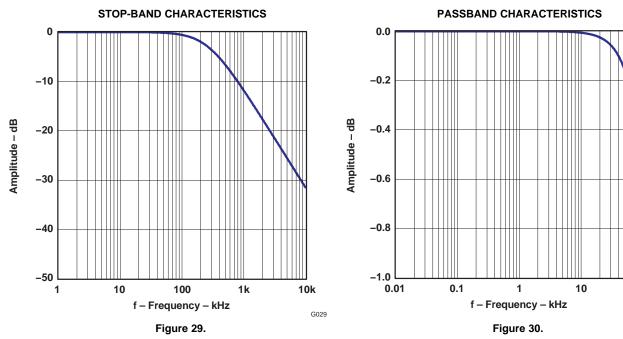
TYPICAL CHARACTERISTICS: DAC ANALOG FIR FILTER FREQUENCY RESPONSE

All specifications at $T_A = +25^{\circ}C$, $V_{BUS} = 5$ V, $f_s = 44.1$ kHz, $f_{IN} = 1$ kHz, 16-bit data, unless otherwise noted.



TYPICAL CHARACTERISTICS: DAC ANALOG LOW-PASS FILTER FREQUENCY RESPONSE

All specifications at $T_A = +25$ °C, $V_{BUS} = 5$ V, $f_s = 44.1$ kHz, $f_{IN} = 1$ kHz, 16-bit data, unless otherwise noted.



100

G030

DETAILED DESCRIPTION

USB INTERFACE

Control data and audio data are transferred to the PCM2906C via D+ (pin 1) and D- (pin 2). All data to/from the PCM2906C are transferred at full speed. The device descriptor contains the information described in Table 2.

Table 2. Device Descriptor

USB revision	2.0 compliant
Device class	0x00 (device defined interface level)
Device sub class	0x00 (not specified)
Device protocol	0x00 (not specified)
Max packet size for end-point 0	8 byte
Vendor ID	0x08BB
Product ID	0x29C6
Device release number	1.0 (0x0100)
Number of configurations	1
Vendor string	String #1 (see Table 4)
Product string	String #2 (see Table 4)
Serial number	Not supported

The configuration descriptor contains the information described in Table 3.

Table 3. Configuration Descriptor

Interface	Four interfaces
Power attribute	0x80 (Bus powered, no remote wakeup)
Max power	0xFA (500 mA)

The string descriptor contains the information described in Table 4.

Table 4. String Descriptor

#0	0x0409
#1	BurrBrown from Texas Instruments
#2	USB AUDIO CODEC

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DEVICE CONFIGURATION

Figure 31 illustrates the USB audio function topology. The PCM2906C has four interfaces. Each interface consists of alternative settings.

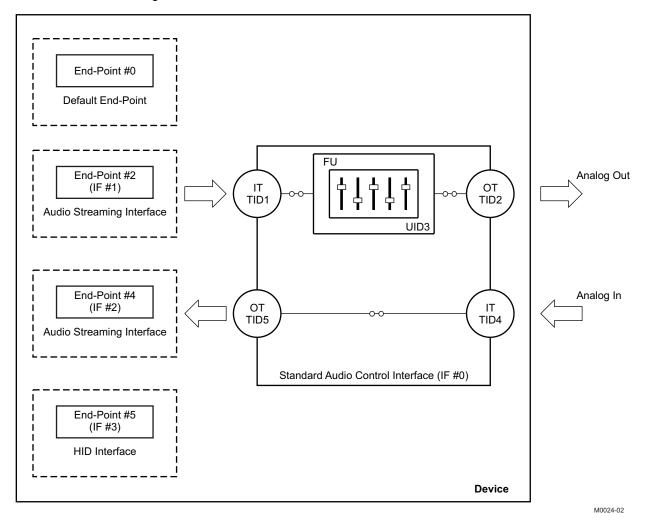


Figure 31. USB Audio Function Topology



Interface #0

Interface #0 is the control interface. Alternative setting #0 is the only possible setting for interface #0. Alternative setting #0 describes the standard audio control interface. The audio control interface consists of a single terminal. The PCM2906C has the following five terminals:

- Input terminal (IT #1) for isochronous-out stream
- · Output terminal (OT #2) for audio analog output
- Feature unit (FU #3) for DAC digital attenuator
- Input terminal (IT #4) for audio analog input
- Output terminal (OT #5) for isochronous-in stream

Input terminal #1 is defined as a *USB stream* (terminal type 0x0101). Input terminal #1 can accept two-channel audio streams consisting of left and right channels. Output terminal #2 is defined as a *speaker* (terminal type 0x0301). Input terminal #4 is defined as a *line connector* (terminal type 0x0603). Output terminal #5 is defined as a *USB stream* (terminal type 0x0101). Output terminal #5 can generate two-channel audio streams composed of left and right channel data. Feature unit #3 supports the following sound control features:

- Volume control
- Mute control

The built-in digital volume controller can be manipulated by an audio-class-specific request from 0 dB to -64 dB in 1-dB steps. Changes are made by incrementing or decrementing by one step (1 dB) for every 1/f_S time interval until the volume level has reached the requested value. Each channel can be set for different values. The master volume control is not supported. A request to the master volume is stalled and ignored. The built-in digital mute controller can be manipulated by audio-class-specific request. A master mute control request is acceptable. A request to an individual channel is stalled and ignored.

Interface #1

Interface #1 is the audio streaming data-out interface. Interface #1 has the five alternative settings listed in Table 5. Alternative setting #0 is the zero-bandwidth setting. All other alternative settings are operational settings.

Table 5. Interface #1 Alternative Settings

ALTERNATIVE SETTING	DATA FORMAT			TRANSFER MODE	SAMPLING RATE (kHz)
00			Zero bandwidth		
01	16-bit	Stereo	Twos complement (PCM)	Adaptive	32, 44.1, 48
02	16-bit	Mono	Twos complement (PCM)	Adaptive	32, 44.1, 48
03	8-bit	Stereo	Twos complement (PCM)	Adaptive	32, 44.1, 48
04	8-bit	Mono	Twos complement (PCM)	Adaptive	32, 44.1, 48

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Interface #2

Interface #2 is the audio streaming data-in interface. Interface #2 has the 19 alternative settings listed in Table 6. Alternative setting #0 is the zero-bandwidth setting. All other alternative settings are operational settings.

Table 6. Interface #2 Alternative Settings

ALTERNATIVE SETTING	DATA FORMAT		TRANSFER MODE	SAMPLING RATE (kHz)	
00			Zero Bandwidth		
01	16-bit	Stereo	Twos complement (PCM)	Asynchronous	48
02	16-bit	Mono	Twos complement (PCM)	Asynchronous	48
03	16-bit	Stereo	Twos complement (PCM)	Asynchronous	44.1
04	16-bit	Mono	Twos complement (PCM)	Asynchronous	44.1
05	16-bit	Stereo	Twos complement (PCM)	Asynchronous	32
06	16-bit	Mono	Twos complement (PCM)	Asynchronous	32
07	16-bit	Stereo	Twos complement (PCM)	Asynchronous	22.05
08	16-bit	Mono	Twos complement (PCM)	Asynchronous	22.05
09	16-bit	Stereo	Twos complement (PCM)	Asynchronous	16
0A	16-bit	Mono	Twos complement (PCM)	Asynchronous	16
0B	8-bit	Stereo	Twos complement (PCM)	Asynchronous	16
0C	8-bit	Mono	Twos complement (PCM)	Asynchronous	16
0D	8-bit	Stereo	Twos complement (PCM)	Asynchronous	8
0E	8-bit	Mono	Twos complement (PCM)	Asynchronous	8
0F	16-bit	Stereo	Twos complement (PCM)	Synchronous	11.025
10	16-bit	Mono	Twos complement (PCM)	Synchronous	11.025
11	8-bit	Stereo	Twos complement (PCM)	Synchronous	11.025
12	8-bit	Mono	Twos complement (PCM)	Synchronous	11.025

Interface #3

Interface #3 is the interrupt data-in interface. Alternative setting #0 is the only possible setting for interface #3. Interface #3 consists of the HID consumer control device and reports the status of these three key parameters:

- Mute (0xE209)
- Volume up (0xE909)
- Volume down (0xEA09)

End-Points

The PCM2906C has the following four end-points:

- Control end-point (EP #0)
- Isochronous-out audio data stream end-point (EP #2)
- Isochronous-in audio data stream end-point (EP #4)
- HID end-point (EP #5)

The control end-point is a default end-point. The control end-point is used to control all functions of the PCM2906C by the standard USB request and USB audio class specific request from the host. The isochronous-out audio data stream end-point is an audio sink end-point, which receives the PCM audio data. The isochronous-out audio data stream end-point accepts the adaptive transfer mode. The isochronous-in audio data stream end-point is an audio source end-point that transmits the PCM audio data. The isochronous-in audio data stream end-point uses the asynchronous transfer mode. The HID end-point is an interrupt-in end-point. The HID end-point reports HID0, HID1, and HID2 pin status every 32 ms.

The human interface device (HID) pins are defined as consumer control devices. The HID function is designed as an independent end-point from both isochronous-in and -out end-points. Therefore, the result obtained from the HID operation depends on the host software. Typically, the HID function is used as the primary audio-out device.



Clock and Reset

The PCM2906C requires a 12-MHz (± 500 ppm) clock for the USB and audio functions. The clock can be generated by a built-in oscillator with a 12-MHz crystal resonator. The 12-MHz crystal resonator must be connected to XTI (pin 21) and XTO (pin 20) with one high-value (1-M Ω) resistor and two small capacitors, the capacitance of which depends on the load capacitance of the crystal resonator. An external clock can be supplied to XTI (pin 21). If an external clock is used, XTO (pin 20) must be left open. Because there is no clock disabling signal, use of the external clock supply is not recommended. SSPND (pin 28) is unable to use clock disabling.

The PCM2906C has an internal power-on reset circuit, which triggers automatically when V_{BUS} (pin 3) exceeds 2.5 V typical (2.7 V to 2.2 V). Approximately 700 μ s is required until internal reset release.

Digital Audio Interface

The PCM2906 employs S/PDIF for both input and output. Isochronous-out data from the host are encoded to the S/PDIF output and the DAC analog output. Input data are selected from either the S/PDIF or ADC analog input. When the device detects S/PDIF input and successfully locks the received data, the isochronous-in transfer data source automatically selected is S/PDIF; otherwise, the data source selected is the ADC analog input.

This feature is a customer option. It is the responsibility of the user to implement this feature.

Supported Input/Output Data

The following data formats are accepted by S/PDIF for input and output. All other data formats are unusable as S/PDIF.

- 48-kHz 16-bit stereo
- 44.1-kHz 16-bit stereo
- 32-kHz 16-bit stereo

Any mismatch of the sampling rate between the input S/PDIF signal and the host command is not acceptable. Any mismatch of the data format between the input S/PDIF signal and the host command may cause unexpected results, with the following exceptions:

- Recording in monaural format from stereo data input at the same data rate
- · Recording in 8-bit format from 16-bit data input at the same data rate

A combination of these two conditions is not acceptable.

For playback, all possible data-rate sources are converted to the 16-bit stereo format at the same source data rate.

Channel Status Information

The channel status information is fixed as consumer application, PCM mode, copyright, and digital/digital converter. All other bits are fixed as 0's except for the sample frequency, which is set automatically according to the data received through the USB.

Copyright Management

Isochronous-in data are affected by the serial copy management system (SCMS). When the control bit indicates that the received digital audio data are original, the input digital audio data are transferred to the host. If the data are indicated as first generation or higher, the transferred data are routed to the analog input.

Digital audio data output is always encoded as original with SCMS control.



INTERFACE SEQUENCE

Power-On, Attach, and Playback Sequence

The PCM2906C is ready for setup when the reset sequence has finished and the USB device is attached. After a connection has been established by setup, the PCM2906C is ready to accept USB audio data. While waiting for the audio data (idle state), the analog output is set to bipolar zero (BPZ).

When receiving the audio data, the PCM2906C stores the first audio packet, which contains 1-ms audio data, into the internal storage buffer. The PCM2906C starts playing the audio data when detecting the next start-of-frame (SOF) packet, as illustrated in Figure 32.

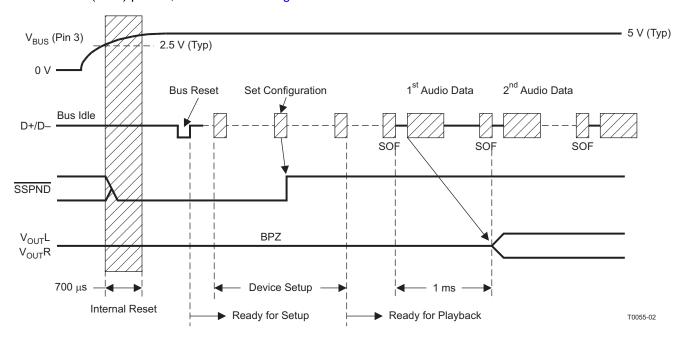


Figure 32. Initial Sequence

Play, Stop, and Detach Sequence

When the host finishes or aborts the playback, the PCM2906C stops playing after the last audio data have played, as shown in Figure 33.

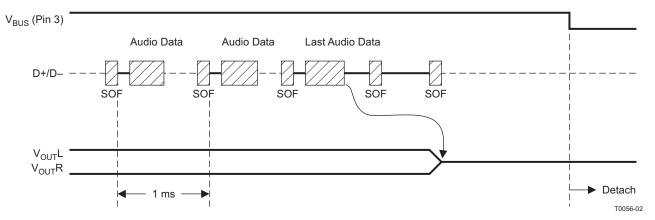


Figure 33. Play, Stop, and Detach Sequence

Record Sequence

The PCM2906C starts audio capture into the internal memory after receiving the SET_INTERFACE command, as shown in Figure 34.

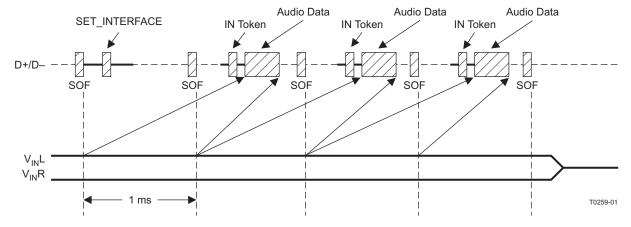


Figure 34. Record Sequence

Suspend and Resume Sequence

The PCM2906C enters the suspend state after a constant idle state on the USB bus (approximately 5 ms), as shown in Figure 35. While the PCM2906C enters the suspend state, the SSPND flag (pin 28) is asserted. The PCM2906C wakes up immediately upon detecting a non-idle state on the USB.

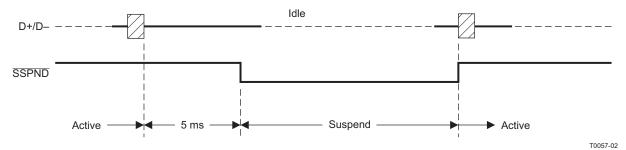


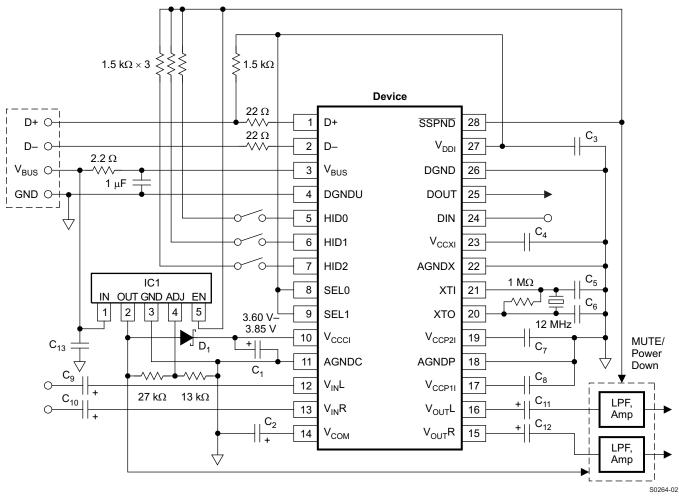
Figure 35. Suspend and Resume Sequence

TEXAS INSTRUMENTS

APPLICATION INFORMATION

TYPICAL CIRCUIT CONNECTION 1

Figure 36 illustrates a typical circuit connection for a simple application. The circuit illustrated is for information only. The entire board design should be considered to meet the USB specification as a USB-compliant product.



NOTE: C₁, C₂: 10 μF

 $C_3,\,C_4,\,C_7,\,C_8,\,C_{13}\!\!:$ 1 μF (These capacitors must be less than 2 $\mu F.)$

C₅, C₆: 10 pF to 33 pF (depending on crystal resonator)

 $C_9,\,C_{10},\,C_{11},\,C_{12}$: The capacitance may vary depending on design.

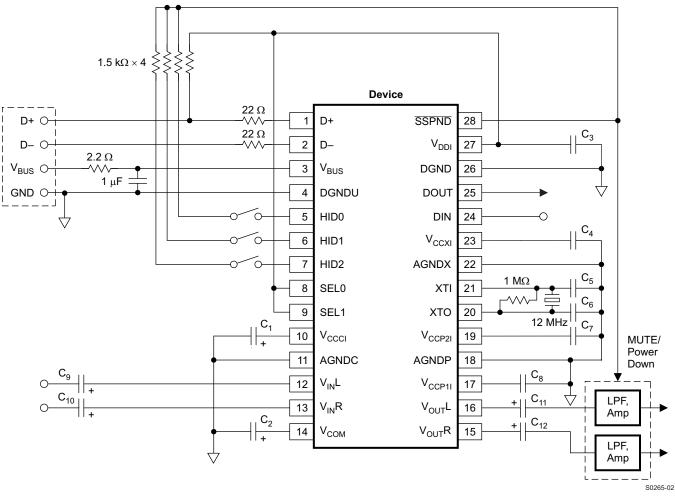
IC1: REG103xA-A (TI) or equivalent. Analog performance may vary depending on IC1.

D₁: Schottky barrier diode ($V_F \le 350 \text{ mV}$ at 10 mA, $I_R \le 2 \mu A$ at 4 V)

Figure 36. Bus-Powered Configuration for High-Performance Application

TYPICAL CIRCUIT CONNECTION 2

Figure 37 illustrates a typical circuit connection for a simple application. The circuit illustrated is for information only. The entire board design should be considered to meet the USB specification as a USB-compliant product.



NOTE: C₁, C₂: 10 μF

 $C_3,\,C_4,\,C_7,\,C_8{:}$ 1 μF (These capacitors must be less than 2 $\mu F.)$

C₅, C₆: 10 pF to 33 pF (depending on crystal resonator)

C₉, C₁₀, C₁₁, C₁₂: The capacitance may vary depending on design. In this case, the analog performance of the ADC may be degraded.

Figure 37. Bus-Powered Configuration

OPERATING ENVIRONMENT

For current information on the PCM2906C operating environment, see the *Updated Operating Environments for PCM270X, PCM290X Applications* application report, SLAA374.



PACKAGE OPTION ADDENDUM

10-Dec-2020

PACKAGING INFORMATION

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Orderable Device	Status (1)	Package Type	Package Drawing	Pins	Package Qty	Eco Plan	Lead finish/ Ball material	MSL Peak Temp	Op Temp (°C)	Device Marking (4/5)	Samples
PCM2906CDB	ACTIVE	SSOP	DB	28	50	RoHS & Green	NIPDAU	Level-1-260C-UNLIM	-25 to 85	PCM2906C	Samples
PCM2906CDBR	ACTIVE	SSOP	DB	28	2000	RoHS & Green	NIPDAU	Level-1-260C-UNLIM	-25 to 85	PCM2906C	Samples

(1) The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

(2) RoHS: TI defines "RoHS" to mean semiconductor products that are compliant with the current EU RoHS requirements for all 10 RoHS substances, including the requirement that RoHS substance do not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, "RoHS" products are suitable for use in specified lead-free processes. TI may reference these types of products as "Pb-Free".

RoHS Exempt: TI defines "RoHS Exempt" to mean products that contain lead but are compliant with EU RoHS pursuant to a specific EU RoHS exemption.

Green: TI defines "Green" to mean the content of Chlorine (Cl) and Bromine (Br) based flame retardants meet JS709B low halogen requirements of <=1000ppm threshold. Antimony trioxide based flame retardants must also meet the <=1000ppm threshold requirement.

- (3) MSL, Peak Temp. The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.
- (4) There may be additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.
- (5) Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "~" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.
- (6) Lead finish/Ball material Orderable Devices may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead finish/Ball material values may wrap to two lines if the finish value exceeds the maximum column width.

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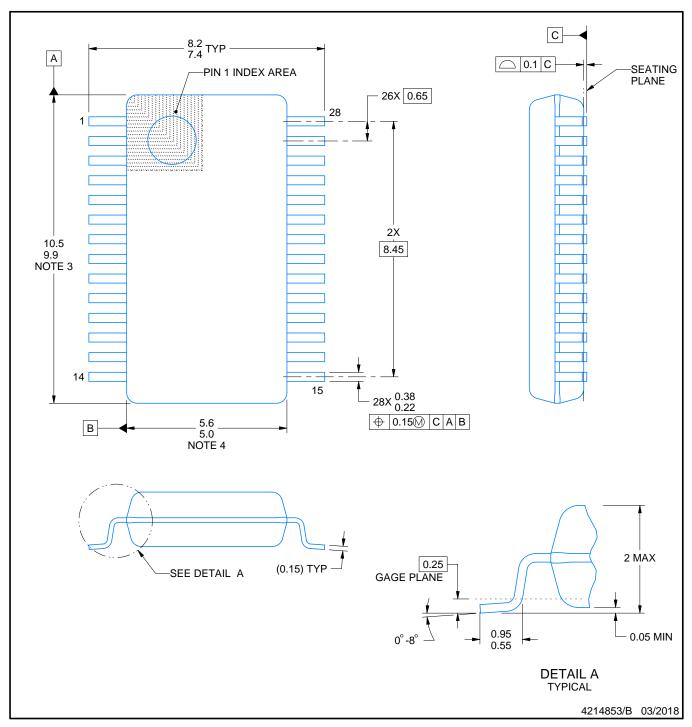




10-Dec-2020



SMALL OUTLINE PACKAGE



NOTES:

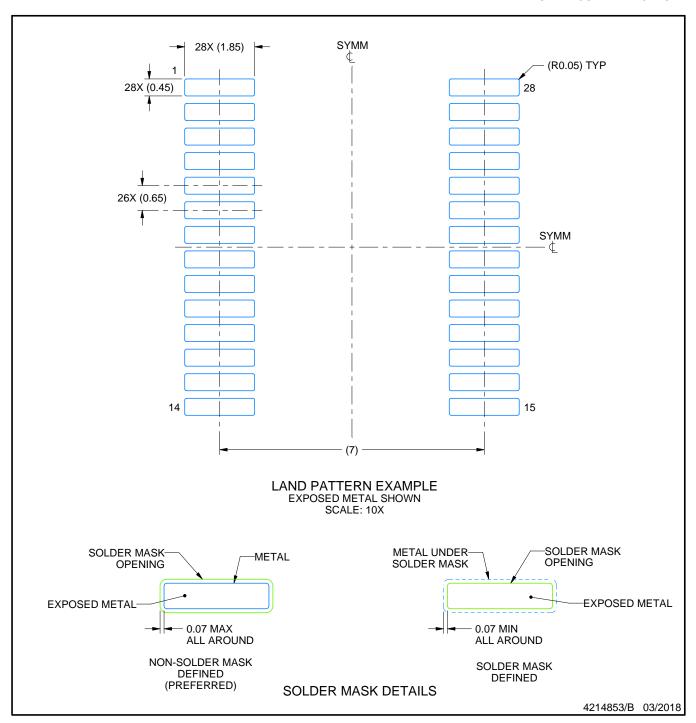
- 1. All linear dimensions are in millimeters. Any dimensions in parenthesis are for reference only. Dimensioning and tolerancing per ASME Y14.5M.

 2. This drawing is subject to change without notice.

 3. This dimension does not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, or gate burrs shall not
- exceed 0.15 mm per side.
- 4. This dimension does not include interlead flash. Interlead flash shall not exceed 0.25 mm per side.
- 5. Reference JEDEC registration MO-150.



SMALL OUTLINE PACKAGE



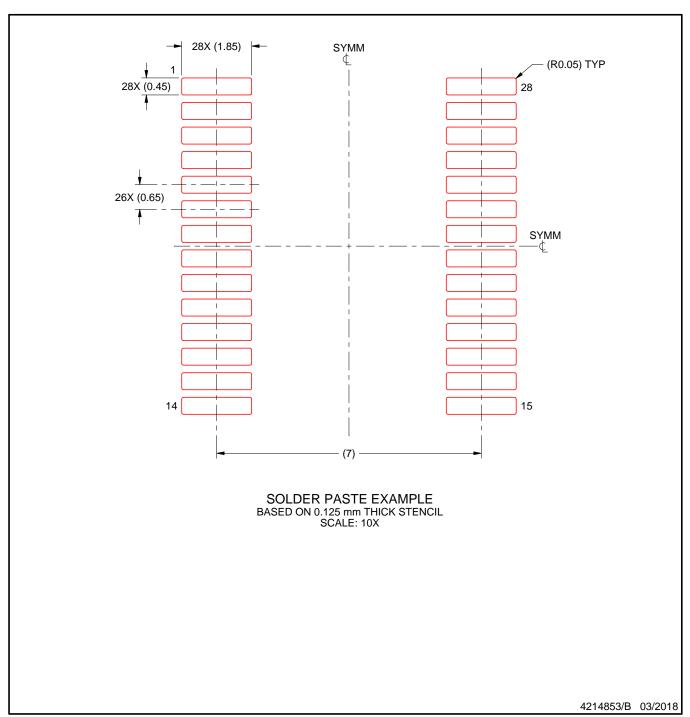
NOTES: (continued)

6. Publication IPC-7351 may have alternate designs.

7. Solder mask tolerances between and around signal pads can vary based on board fabrication site.



SMALL OUTLINE PACKAGE



NOTES: (continued)

- 8. Laser cutting apertures with trapezoidal walls and rounded corners may offer better paste release. IPC-7525 may have alternate design recommendations.
- 9. Board assembly site may have different recommendations for stencil design.



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