LDC0851 差分感应开关

1 特性

- 阈值容限：<1% 线圈直径
- 开关操作在整个温度范围内保持稳定
- 平均电源电流：10sps 时 < 20µA
- 关断电源电流：140nA
- 推挽式输出
- 可通过电阻编程设定阈值
- 对直流磁场不敏感
- 非接触式开关操作
- 采样速率：高达 4ksps
- 电源电压：1.8V – 3.3V
- 工作温度范围：40°C 至 125°C

2 应用

- 打开/关闭开关
  - 家庭安防和篡改检测
  - 打印机
  - 个人电子产品
- 事件计数
  - 风扇转速 RPM 检测
  - 旋转编码器
  - 流量计
  - 增量旋钮/拨盘
- 简易按钮
  - 工业用键盘
  - 个人电子产品
- 工业用接近开关

3 说明

LDC0851 是一款近距离感应开关，是存在检测、事件计数和简易按钮等 应用 的理想选择。

当导电物体进入感应线圈的接近范围内时将触发开关。该器件包含的滞后功能可保证一个可靠的开关阈值，从而不受机械振动的影响。差分实现方案可防止因温度变化或湿度影响等环境因素而导致误触发。

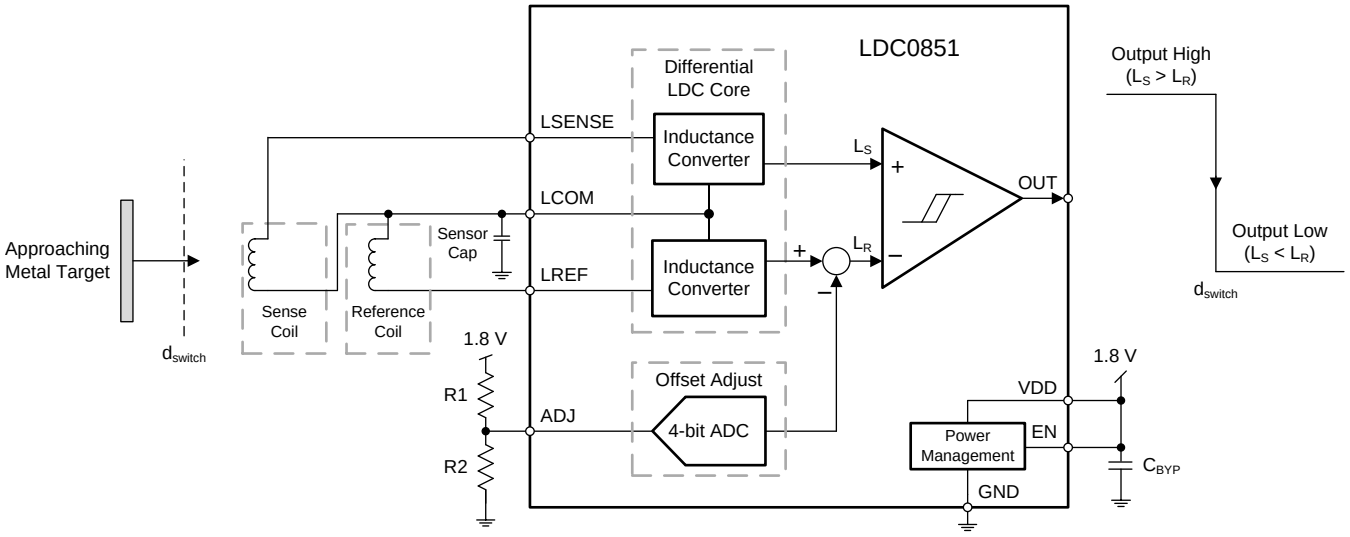
电感式传感技术即使在有尘土、油污或潮气的环境中也可实现可靠而准确的感应，因此非常适合严苛或脏污的环境。固态开关消除了磁簧、机械或接触开关常会引发的失败。与同类竞争产品不同的是，LDC0851 无需使用磁体，而且不受直流磁场的影响。

器件信息(1)

器件型号	封装	封装尺寸（标称值）
LDC0851	WSON-8	2mm x 2mm

(1) 要了解所有可用封装，请见数据表末尾的可订购产品附录。

4 简化电路原理图



An IMPORTANT NOTICE at the end of this data sheet addresses availability, warranty, changes, use in safety-critical applications, intellectual property matters and other important disclaimers. PRODUCTION DATA.

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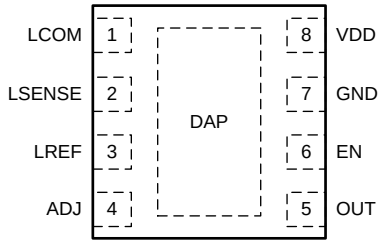
5 修订历史记录

Changes from Original (December 2015) to Revision A	**Page**
• 产品预览至量产数据版本	1

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6 Pin Configuration and Functions

DSG Package
8-Pin WSON with DAP
Top View



Pin Functions

PIN		TYPE(1)	DESCRIPTION
NAME	NO.		
LCOM	1	A	Common coil input
LSENSE	2	A	Sense coil input
LREF	3	A	Reference coil input
ADJ	4	A	Threshold adjust pin
OUT	5	O	Switch output
EN	6	I	Enable input
GND	7	G	Ground
VDD	8	P	Power Supply
DAP	DAP	G	Connect to Ground for improved thermal performance(2)

(1) I = Input, O = Output, P = Power, A = Analog, G = Ground
(2) There is an internal electrical connection between the exposed Die Attach Pad (DAP) and the GND pin of the device. Although the DAP can be left floating, for best performance the DAP should be connected to the same potential as the device's GND pin. Do not use the DAP as the primary ground for the device. The device GND pin must always be connected to ground.

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7 Specifications

7.1 Absolute Maximum Ratings

over operating free-air temperature range (unless otherwise noted) (1)

		MIN	MAX	UNIT
V_{DD}	Supply Voltage Range		3.6	V
V_i	Voltage on LSENSE, LREF, and EN	-0.3	3.6	V
	Voltage on ADJ and LCOM	-0.3	2	V
I_A	Current LSENSE, LREF, and VOUT		5	mA
T_J	Junction Temperature	-55	150	°C
T_{stg}	Storage Temperature	-65	150	°C

(1) Stresses beyond those listed under *Absolute Maximum Ratings* may cause permanent damage to the device. These are stress ratings only, which do not imply functional operation of the device at these or any other conditions beyond those indicated under *Recommended Operating Conditions*. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

7.2 ESD Ratings

			VALUE	UNIT
$V_{(ESD)}$	Electrostatic discharge	Human-body model (HBM), per ANSI/ESDA/JEDEC JS-001 (1)	±1000	V
		Charged-device model (CDM), per JEDEC specification JESD22-C101 (2)	±250	

(1) JEDEC document JEP155 states that 500-V HBM allows safe manufacturing with a standard ESD control process.
(2) JEDEC document JEP157 states that 250-V CDM allows safe manufacturing with a standard ESD control process.

7.3 Recommended Operating Conditions

over operating free-air temperature range (unless otherwise noted)

		MIN	NOM	MAX	UNIT
V_{DD}	Supply Voltage	1.71		3.46	V
T_A	Operating Temperature	-40		125	°C

7.4 Thermal Information

over operating free-air temperature range (unless otherwise noted)

THERMAL METRIC (1)		LDC0851 DSG (WSON) 8 PINS	UNIT
$R_{\theta JA}$	Junction-to-ambient thermal resistance	67.4	°C/W
$R_{\theta JC(top)}$	Junction-to-case (top) thermal resistance	89.3	°C/W
$R_{\theta JB}$	Junction-to-board thermal resistance	37.3	°C/W
ψ_{JT}	Junction-to-top characterization parameter	2.4	°C/W
ψ_{JB}	Junction-to-board characterization parameter	37.7	°C/W
$R_{\theta JC(bot)}$	Junction-to-case (bottom) thermal resistance	9.2	°C/W

(1) For more information about traditional and new thermal metrics, see the *Semiconductor and IC Package Thermal Metrics* application report (SPRA953).

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7.5 Electrical Characteristics(1)

Over recommended operating conditions unless otherwise noted. V_{DD}= 3.3 V, EN tied to 3.3 V, T_A=25 °C, ADJ tied to GND.

PARAMETER		TEST CONDITIONS	MIN(2)	TYP(3)	MAX(2)	UNIT
POWER						
V_{DD}	Supply Voltage		1.71		3.46	V
I_{STATIC}	Static Supply Current (4)			0.70		mA
I_{DYN}	Dynamic Supply Current (not including sensor current)(4)	f_{SENSOR} = 15 MHz $C_{PARASITIC}$ = 22 pF		0.66		mA
I_{SD}	Shutdown Mode Supply Current			0.14	1	µA
SENSOR						
I_{SENSOR_MAX}	Maximum sensor current(4)	V_{DD} = 1.71 V	4.35			mA
		V_{DD} = 3.3 V	6			mA
L_{SENSOR_MIN}	Sensor Minimum Inductance(5)	C_{TOTAL} = 33 pF V_{DD} = 1.71 V		2.5		
		C_{TOTAL} = 33 pF V_{DD} = 3.3 V		1.8		µH
f_{SENSOR_MAX}	Max Sensor Resonant Frequency(5)	Sensor inductance = 2 µH C_{TOTAL} = 33 pF		19		MHz
C_{TOTAL}	Minimum total capacitance on LCOM(5) Includes parasitic pin capacitance and PCB parasitic capacitance			33		pF
C_{IN}	Pin parasitic capacitance on LCOM			12		pF
	Pin parasitic capacitance on LREF and LSENSE			8		pF
DETECTION						
d_{HYST}	Switching distance hysteresis(6)			2.5 %		
d_{TOL}	Switching threshold tolerance(6)			0.1 %		
THRESHOLD ADJUST						
V_{ADJ}	Adjust input range		0		V_{DD}/2	V
V_{ADJ_TOL}	Adjust threshold tolerance			± 6		mV

(1) Electrical Characteristics Table values apply only for factory testing conditions at the temperature indicated. Factory testing conditions result in very limited self-heating of the device such that $T_J = T_A$. No guarantee of parametric performance is indicated in the electrical tables under conditions of internal self-heating where $T_J > T_A$. Absolute Maximum Ratings indicate junction temperature limits beyond which the device may be permanently degraded, either mechanically or electrically.
(2) Limits are ensured by testing, design, or statistical analysis at 25°C. Limits over the operating temperature range are ensured through correlations using statistical quality control (SQC) method.
(3) Typical values represent the most likely parametric norm as determined at the time of characterization. Actual typical values may vary over time and will also depend on the application and configuration. The typical values are not tested and are not guaranteed on shipped production material.
(4) Refer to section *Active Mode* for a description and calculation of the various supply currents.
(5) See *Sensor Design* for sensor guidance.
(6) Two matched 10 mm diameter sensors were used with a switching distance of 3 mm. See *Hysteresis* for more information.

7.6 Interface Voltage Levels

PARAMETER		MIN	TYP	MAX	UNIT
V_{IH}	Input High Voltage	$0.8^{x}V_{DD}$			V
V_{IL}	Input Low Voltage			$0.2^{x}V_{DD}$	V
V_{OH}	Output High Voltage(1mA source current)	V_{DD}-0.4			V
V_{OL}	Output Low Voltage (1mA sink current)			0.4	V

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7.7 Timing Requirements

Over recommended operating conditions unless otherwise noted. V_{DD}= 3.3 V, EN tied to 3.3 V, T_A=25 °C, ADJ tied to GND.

PARAMETER		TEST CONDITIONS	MIN	TYP	MAX	UNIT
VOLTAGE LEVELS						
$t_{CONVERSION}$	Conversion time	f_{SENSOR} = 15 MHz		290		µs
t_{DELAY}	Output delay time (Response time)	f_{SENSOR} = 15 MHz		580		µs
t_{START}	Start-up time			450		µs
t_{AMT}	Shutdown-to-active mode transition time			450		µs
t_{SMT}	Active-to-shutdown mode transition time			<1		µs

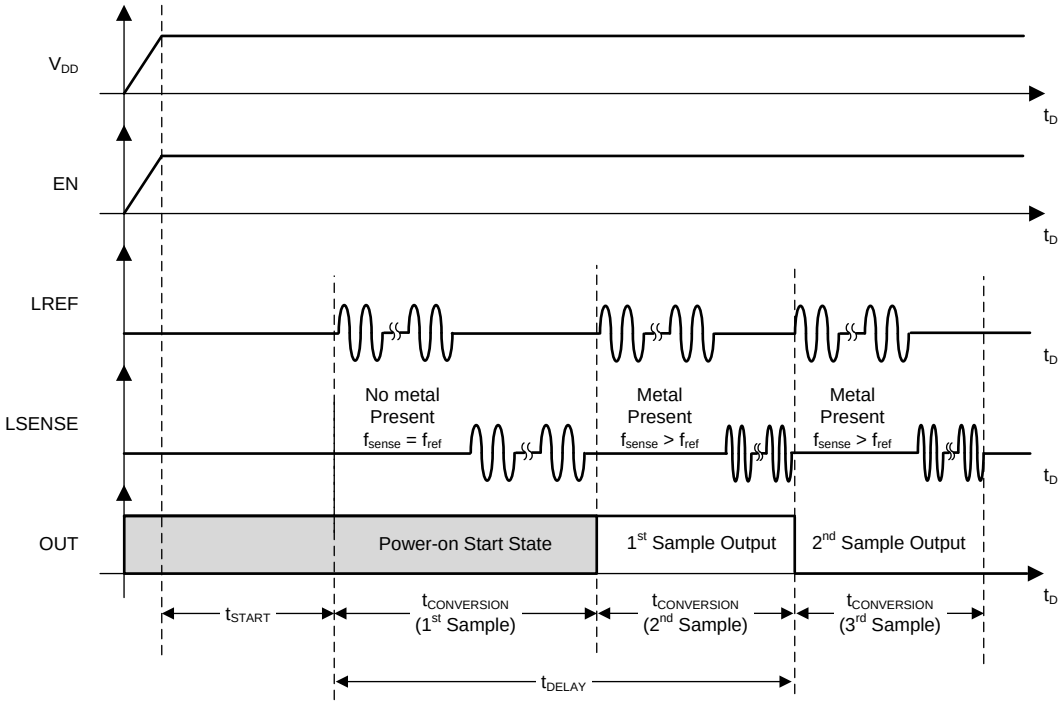


Figure 1. Start-up and Delay Time Diagram

Refer to *Power-Up Conditions* for more information on the Power-On Start State.

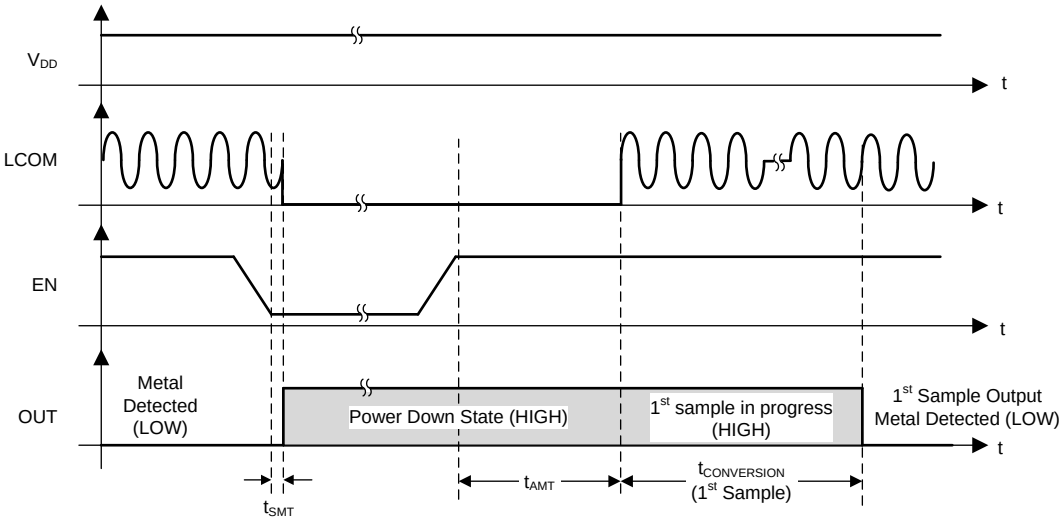
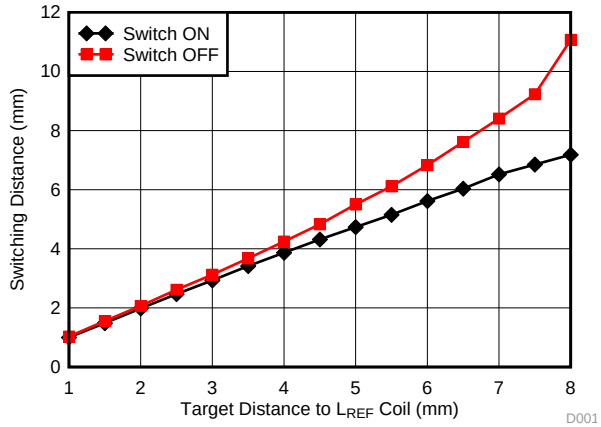


Figure 2. Shutdown and Resume Active Mode Timing Diagram

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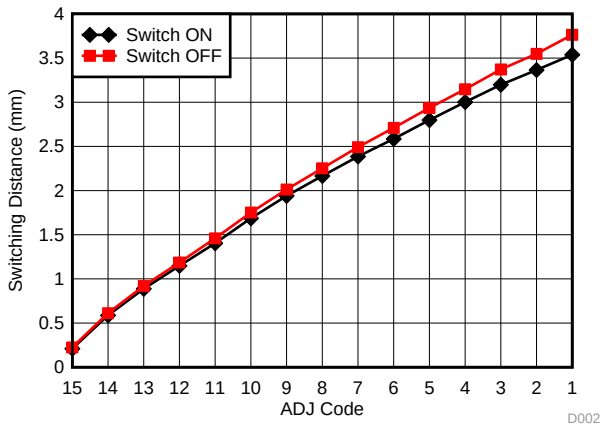
7.8 Typical Characteristics

Common test conditions (unless specified otherwise): V_{DD} = 3.3 V, Sense coil diameter = reference coil diameter, Target: Aluminum, 1.5 mm thickness, Target area / Coil area > 100%



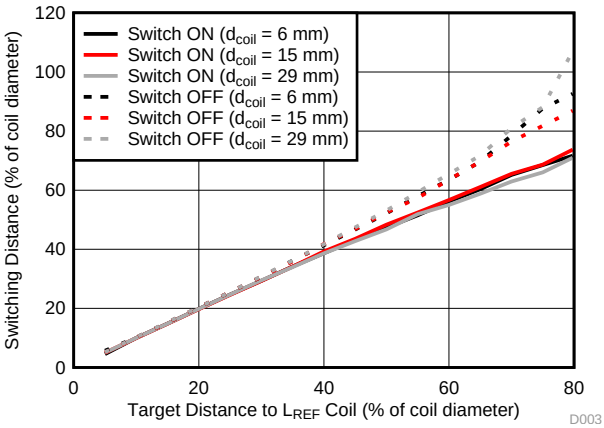
Basic Operation Mode ADJ Code = 0
Coil diameter = 10 mm

图 3. Switching Distance vs. LREF Target Distance



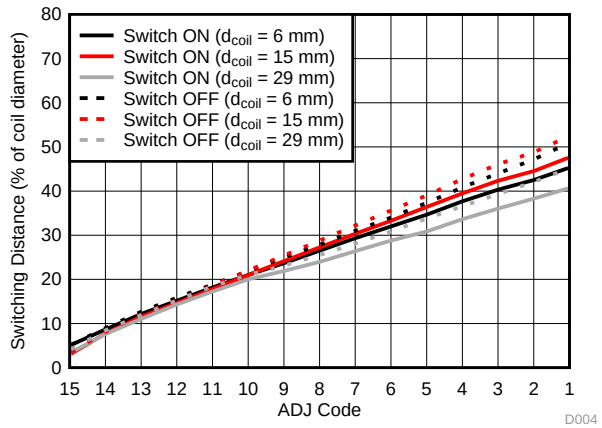
Threshold Adjust Mode No reference target
Coil diameter = 10 mm

图 4. Switching Distance vs. ADJ code



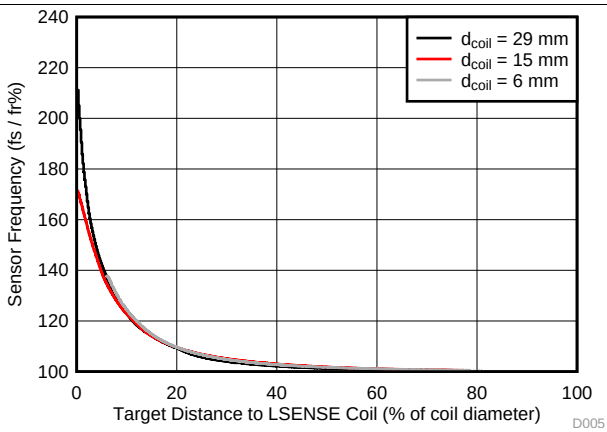
Basic Operation Mode ADJ Code = 0
Coil diameter = 6 mm, 15 mm, 29 mm

图 5. Normalized Switching Distance vs. LREF Target Distance



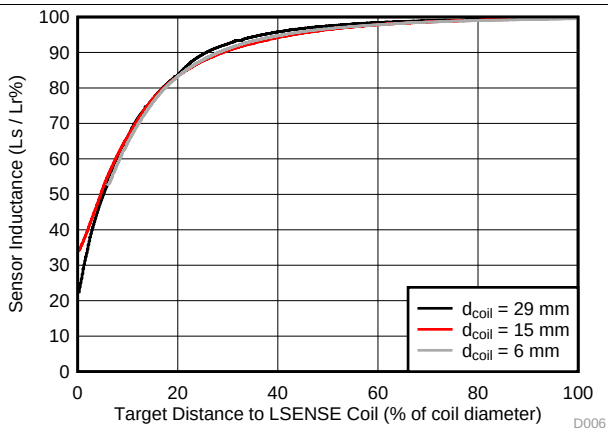
Threshold Adjust Mode No reference target
Coil diameter = 6 mm, 15 mm, 29 mm

图 6. Normalized Switching Distance vs. ADJ Code



LSENSE frequency (f_s) varied LREF frequency (f_r) fixed

图 7. Frequency vs. Distance

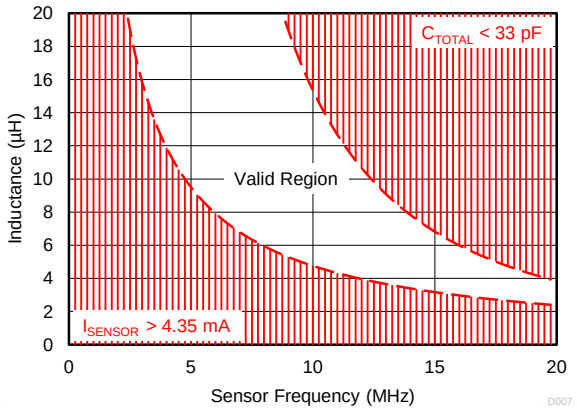


LSENSE inductance (L_s) varied LREF inductance (L_r) fixed

图 8. Inductance vs. Distance

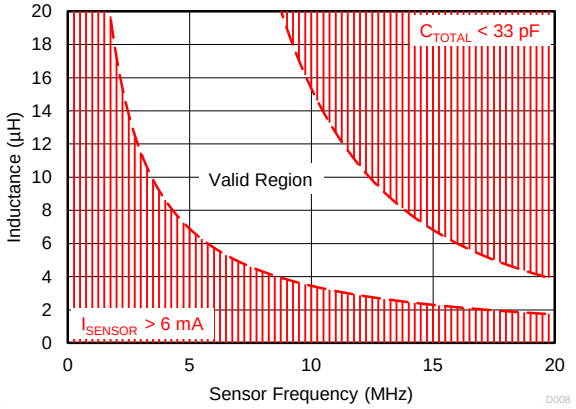
Typical Characteristics (接下页)

Common test conditions (unless specified otherwise): V_{DD} = 3.3 V, Sense coil diameter = reference coil diameter, Target: Aluminum, 1.5 mm thickness, Target area / Coil area > 100%



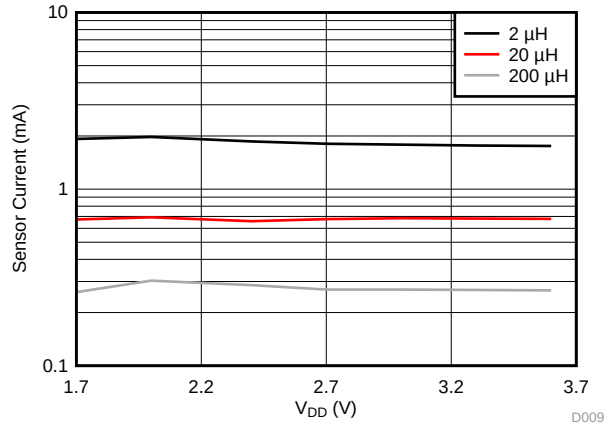
I_{SENSOR_MAX} = 4.35 mA
Specified for closest target proximity or minimum inductance in the application.

图 9. Sensor Design Space for V_{DD} = 1.8 V



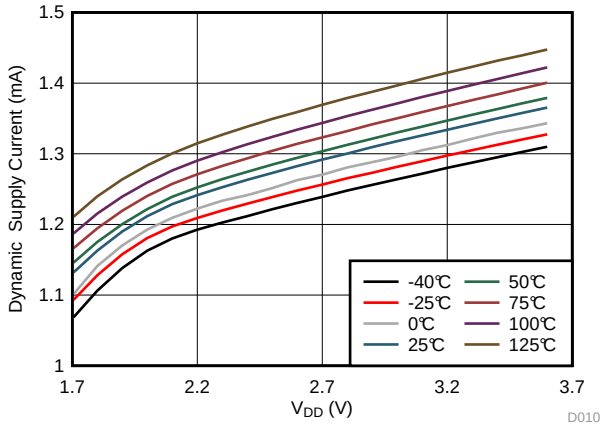
I_{SENSOR_MAX} = 6 mA
Specified for closest target proximity or minimum inductance in the application.

图 10. Sensor Design Space for V_{DD} = 3.3 V



C_{TOTAL} = 100 pF

图 11. I_{SENSOR} vs. V_{DD}



C_{BOARD} = 12 pF
f_{SENSOR} = 30 MHz

图 12. I_{DYN} vs. V_{DD}

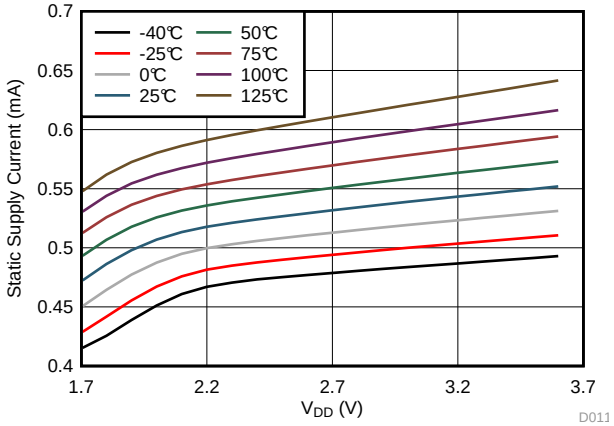


图 13. I_{STATIC} vs. V_{DD}

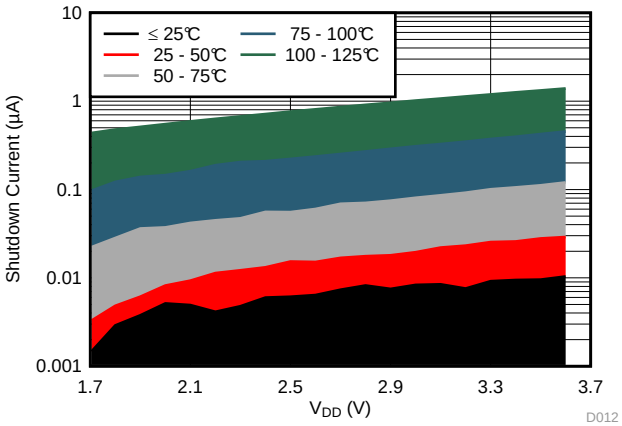
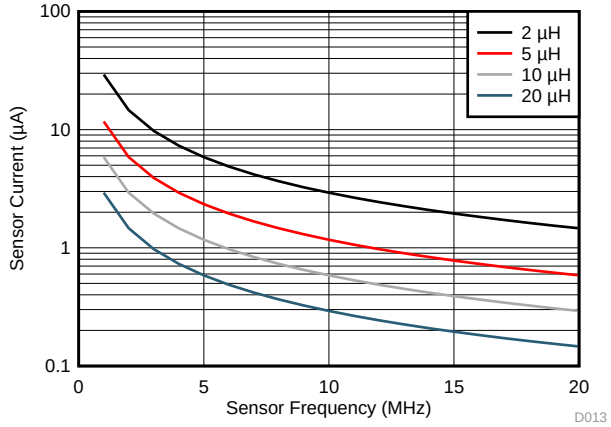


图 14. I_{SD} vs. V_{DD}

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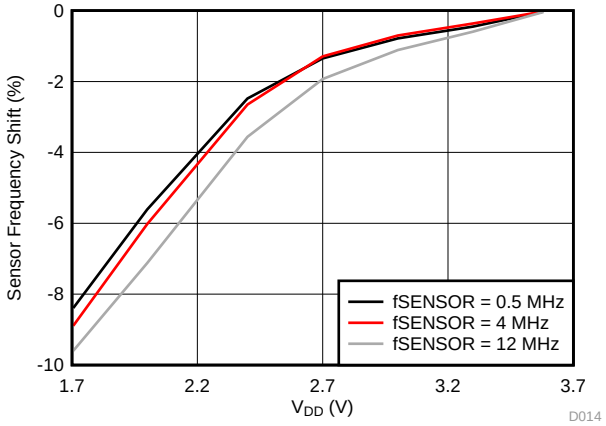
Typical Characteristics (接下页)

Common test conditions (unless specified otherwise): V_{DD} = 3.3 V, Sense coil diameter = reference coil diameter, Target: Aluminum, 1.5 mm thickness, Target area / Coil area > 100%



See 公式 4

图 15. I_{SENSOR} vs. f_{SENSOR}



Normalized to frequency at V_{DD} = 3.6 V

图 16. f_{SENSOR} Shift vs. V_{DD}

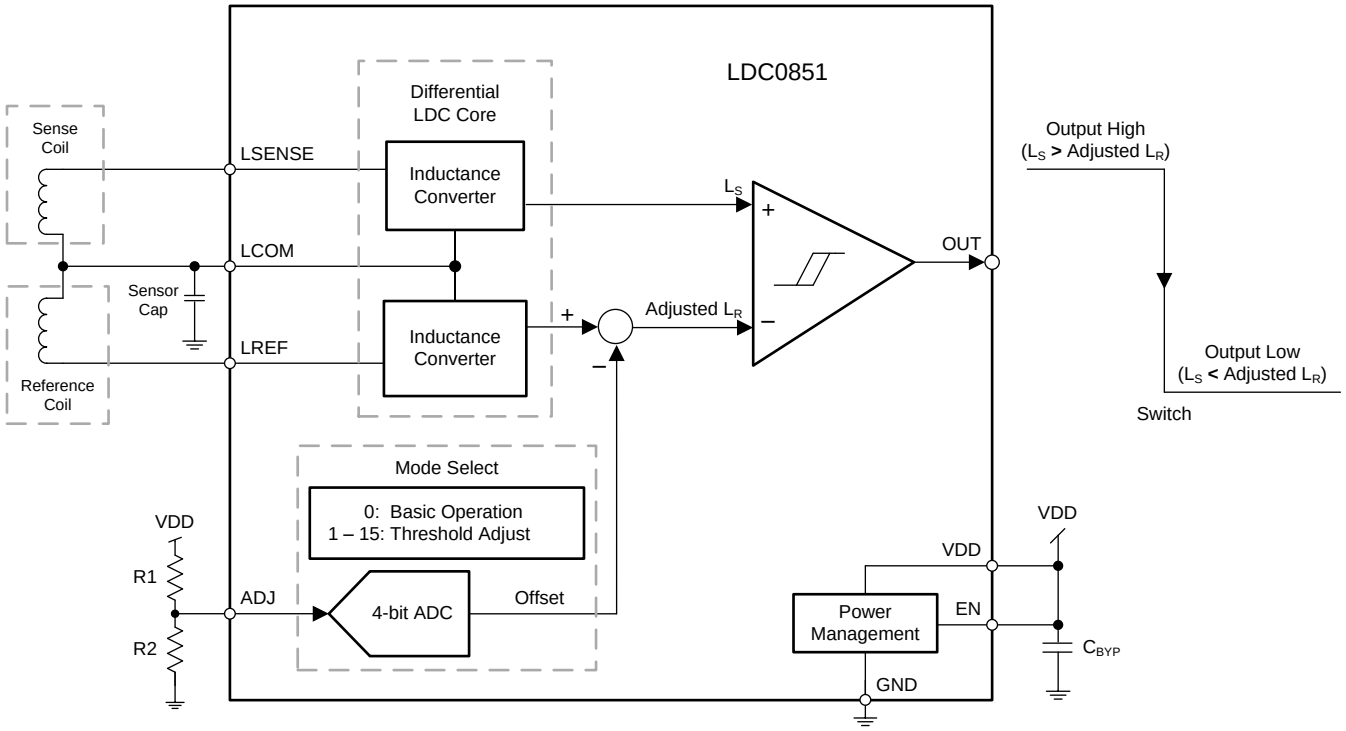
8 Detailed Description

8.1 Overview

The LDC0851 is an inductance comparator with push/pull output. It utilizes a sensing coil and a reference coil to determine the relative inductance in a system. The push/pull output (OUT) switches low when the sense inductance drops below the reference and returns high when the reference inductance is higher than the sense inductance. Matching the sense and reference coils is important to maintain a consistent switching distance over temperature and to compensate for other environmental factors. The LDC0851 features internal hysteresis to prevent false switching due to noise or mechanical vibration at the switching threshold. The switching threshold is set by the sensor characteristics and proximity to conductive objects, which is considered Basic Operation Mode described further in section *Basic Operation Mode*. The LDC0851 also features a Threshold Adjust Mode where an offset is subtracted from the reference inductance to change the effective switching point as described in section *Threshold Adjust Mode*.

The sensing coil is connected across the LSENSE and LCOM pins and the reference coil is connected across the LREF and LCOM pins. A sensor capacitor is connected from LCOM to GND to set the sensor oscillation frequency. The sensor capacitor is common to both LSENSE and LREF making the inductance measurement differential.

8.2 Functional Block Diagram



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8.3 Feature Description

8.3.1 Basic Operation Mode

The LDC0851 is configured for Basic Operation mode when the ADJ pin is tied to ground. Two identical coils should be used for LSENSE and LREF. The switching point occurs when the inductances of both coils are equal. Basic Operation mode can be used for a wide variety of applications including event counting or proximity sensing. An example showing gear tooth counting can be found in section *Event Counting*.

For proximity sensing the switching point can be set by placing a conductive target at a fixed distance from the reference coil as shown in 图 17. The output will switch when a conductive target approaches LSENSE and reaches the same distance set by the fixed reference target. For reliable and repeatable switching it is recommended to place the reference target at a distance less than 40% of the coil diameter from the reference coil.

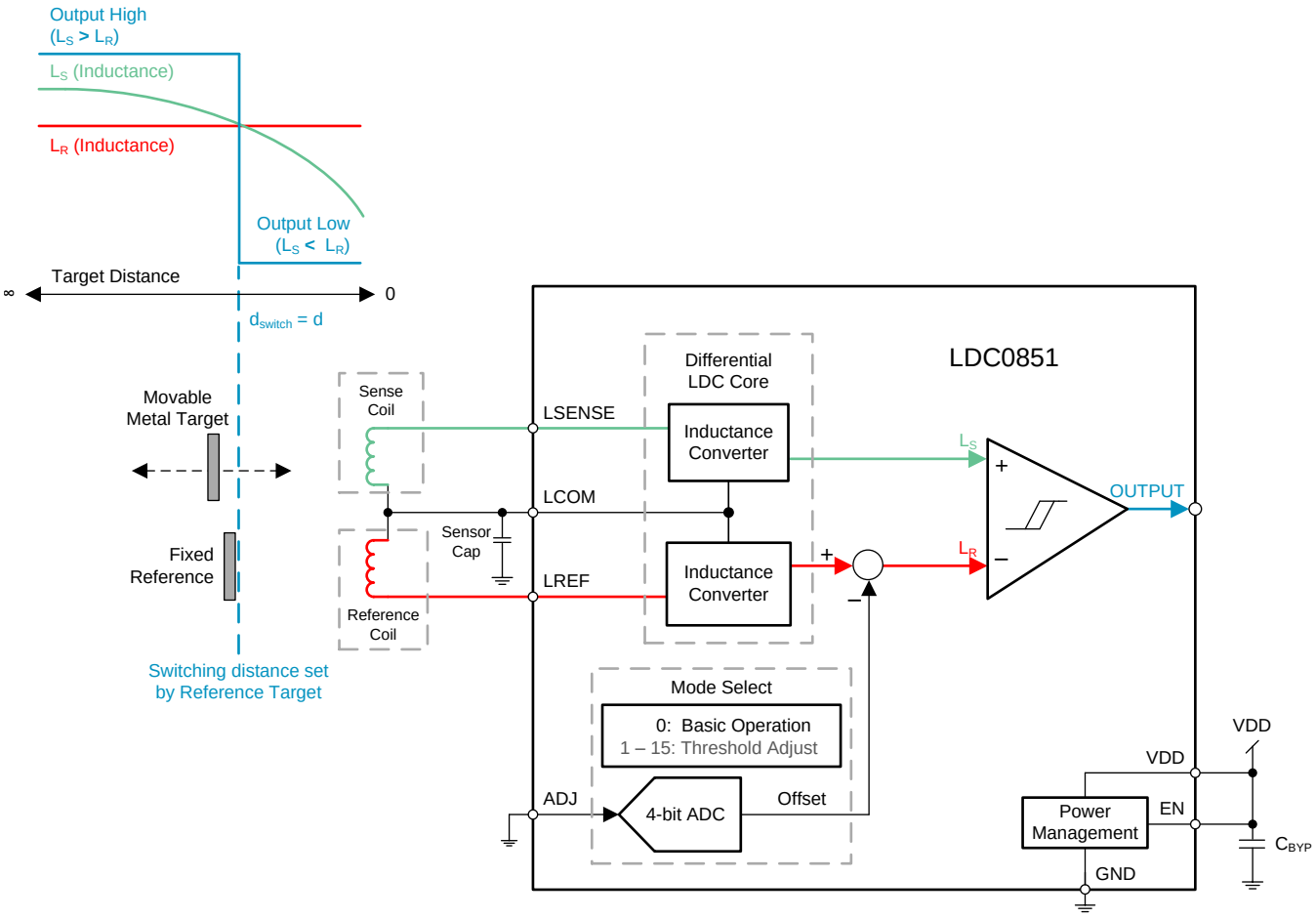


图 17. Basic Operation Mode Diagram for Distance Sensing With Reference Target

Feature Description (接下页)

In some systems adding a reference target at a fixed height to set the switching distance is not feasible. Therefore to set the switching distance a small amount of mismatch between the sense and reference coils can be introduced. To achieve the maximum switching distance the reference inductance should be approximately 0.4% less than the sense inductance as shown in 图 18 below. The 0.4% mismatch will ensure that the output will switch off when the target is removed.

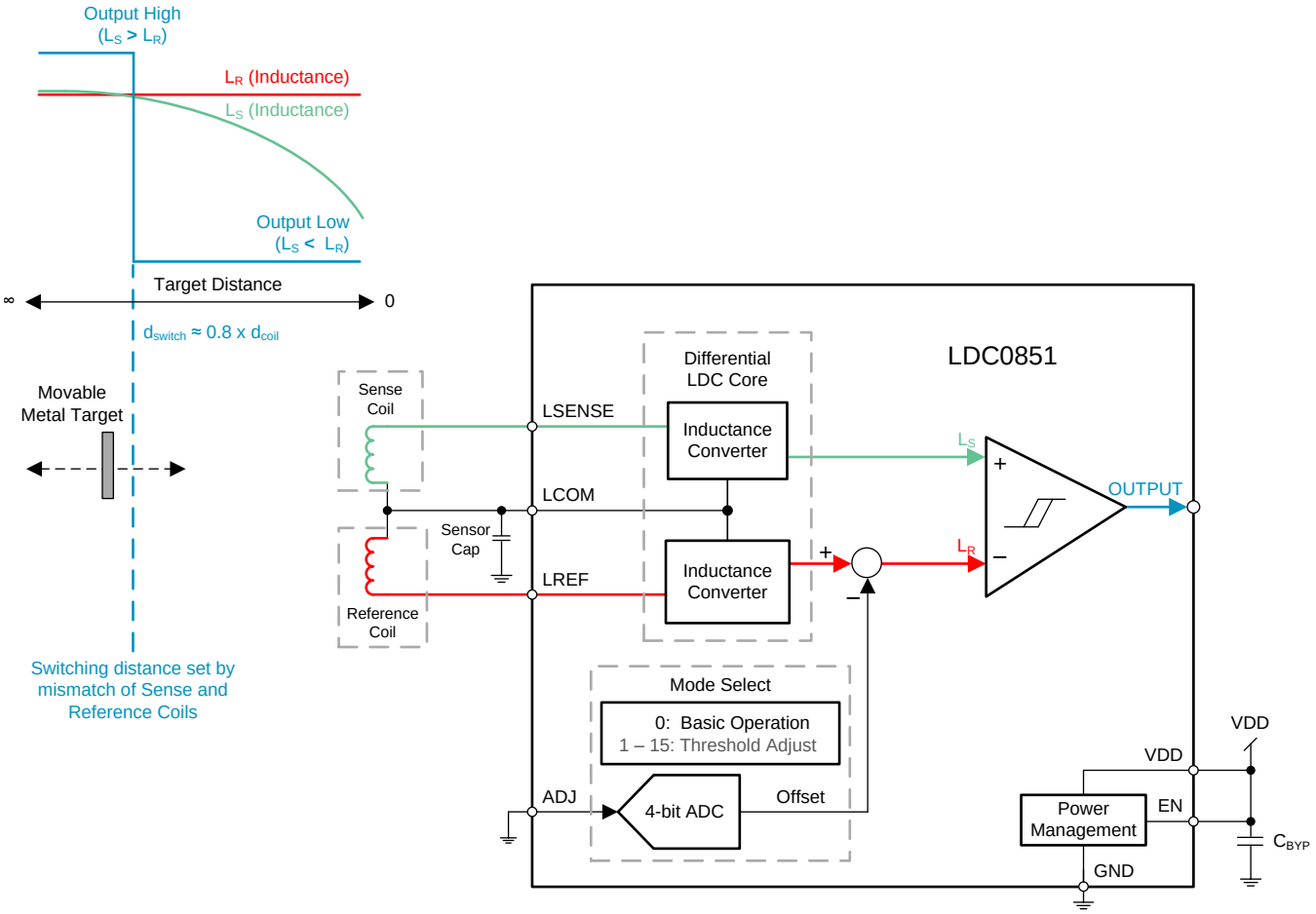


图 18. Basic Operation Mode Diagram for Distance Sensing With Mismatched Coils

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Feature Description (接下页)

8.3.2 Threshold Adjust Mode

In Threshold Adjust mode, an offset inductance is subtracted from LREF to alter the switching threshold without the use of a reference target. In order to configure the LDC0851 for Threshold Adjust mode, place a resistor divider between VDD and GND as shown in *图 19*. The threshold adjust values can then be easily changed as described in section *Setting the Threshold Adjust Values*. Threshold adjust mode can be used in a variety of applications including coarse proximity sensing and simple button applications as shown in *Coarse Position Sensing*. Two example coil configurations for proximity sensing are shown below for side by side coil orientation in 图 19 as well as stacked configuration in 图 20.

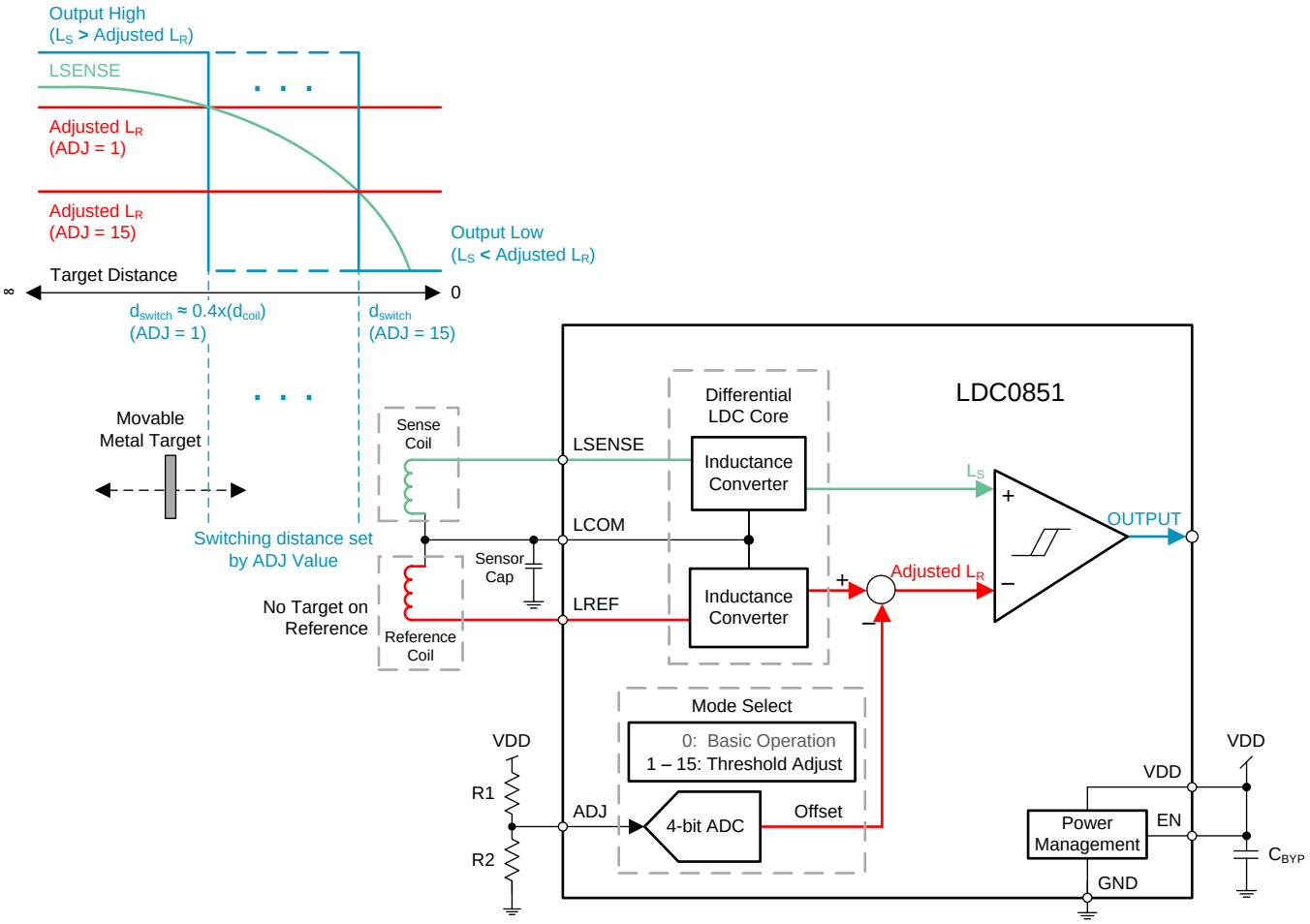


图 19. Threshold Adjust Mode for Distance Sensing Using Side by Side Coils

Feature Description (接下页)

Stacked coils can be utilized in designs where PCB space is a concern or if the user only wants to detect proximity to metal from one side of the PCB such as a button application. The sensing range is slightly reduced due to the fact that both the sense and the reference coil are affected by same conductive target, however since the sense coil is closer to the target its respective inductance decreases more than the reference inductance allowing the output to switch as shown in 图 20.

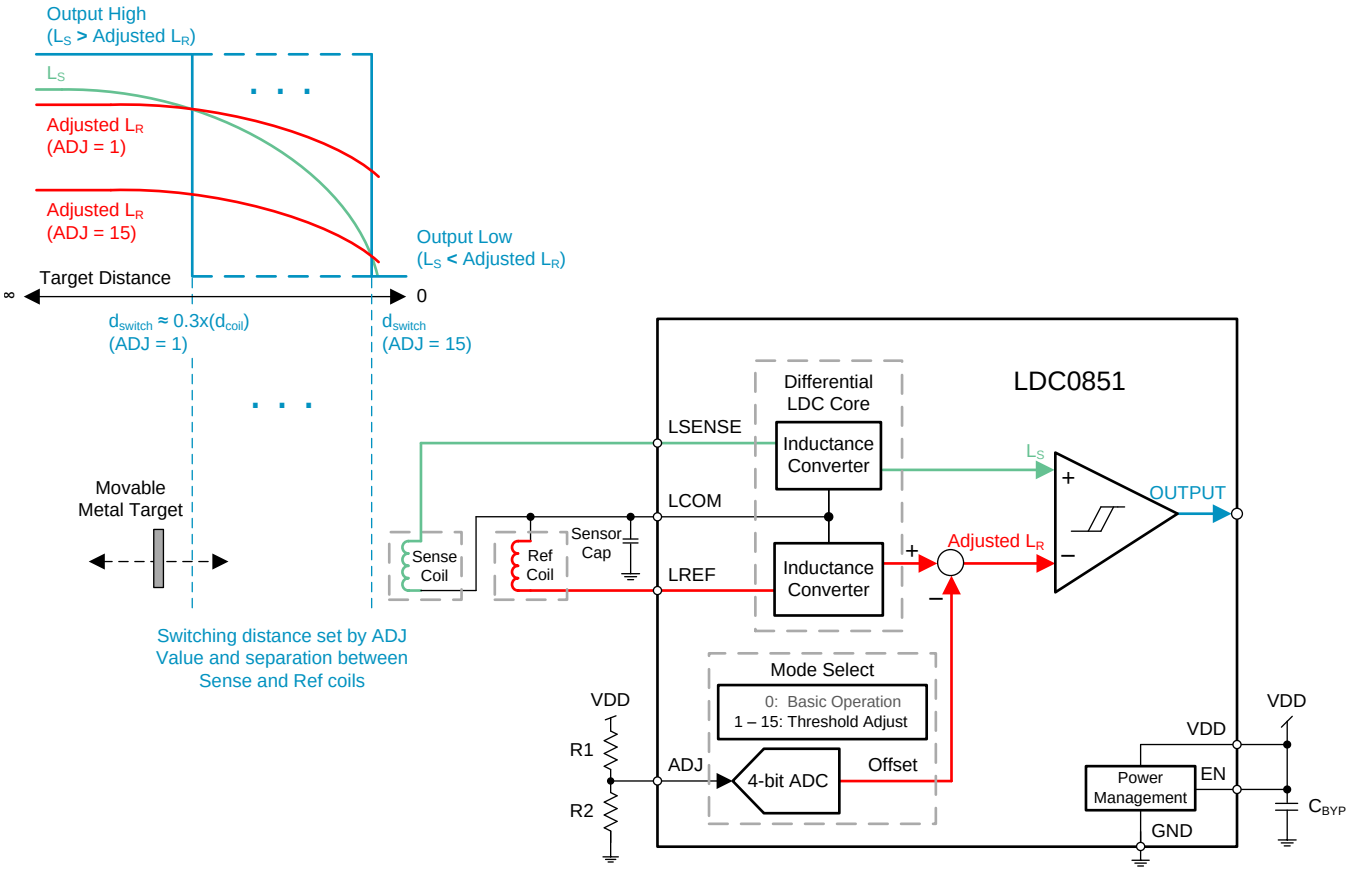


图 20. Threshold Adjust Mode for Distance Sensing Using Stacked Coils

To get the most sensing range with stacked coils the spacing between the sensing coil and reference coil (height = h) should be maximized as shown in 图 21. See section Stacked Coils for more information on the layout of stacked coils.



图 21. Stacked Coil Separation (PCB Side View)

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Feature Description (接下页)

8.3.3 Setting the Threshold Adjust Values

To configure a threshold setting, connect a 49.9 kΩ resistor (R1) between VDD and the ADJ pin as shown in 图 20. The threshold is determined by the value of R2 as shown in the 表 1 below. R1 and R2 should be 1% or tighter tolerance resistors with a temperature coefficient of <50 ppm/°C.

表 1. Resistor Values for ADJ Code

ADJ Code	R2 (kΩ)
1	3.32
2	5.11
3	7.15
4	9.31
5	11.5
6	14
7	16.5
8	19.6
9	22.6
10	26.1
11	30.1
12	34
13	39
14	44.2
15	49.9

The switching distance for each ADJ code can be approximated with the following formula:

$$d_{switch} = d_{coil} \times 0.4 \times \left(1 - \frac{ADJ_{Code}}{16}\right) \quad (1)$$

where:

- d_{switch} is the approximated switching distance threshold
- d_{coil} is the coil diameter, in the same units as d_{switch}
- ADJ_{Code} is the desired value from 表 1

For example, consider a coil with a diameter of 10 mm: An ADJ code of 1 will yield a switching distance of 3.75 mm and for a code of 15 a switching distance of 0.25 mm. This method helps reduce the effort needed to design the coil ratio precisely for a specific switching distance. It should be noted that the maximum sensing distance is determined almost entirely by the diameter of the coil for circular coils or by the minimum outer dimension for rectangular coils.

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8.3.4 Hysteresis

The LDC0851 includes hysteresis for the switching threshold. The switch point is determined by the inductance ratio between LSENSE and LREF. When the ratio of LSENSE to LREF drops below 99.6%, the device switches ON (output low). When LSENSE/LREF becomes greater than 100.4% it switches OFF (output high). The hysteresis window is therefore specified 0.8% from the switch ON point.

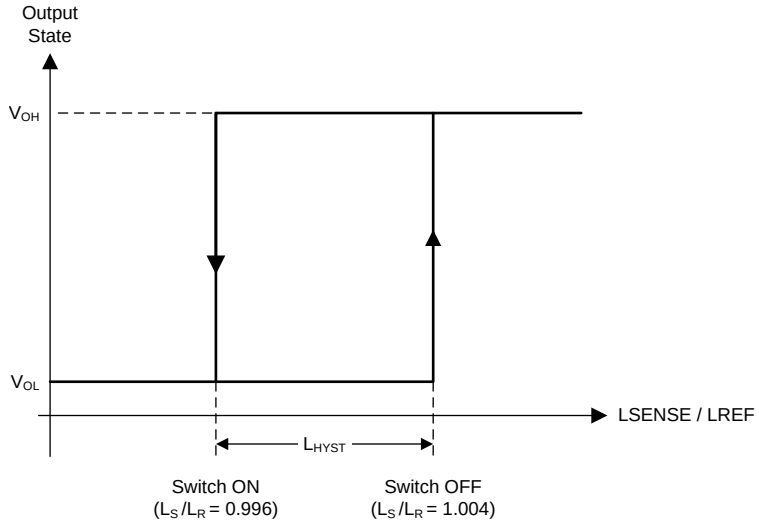


图 22. Inductance Hysteresis

For proximity sensing, hysteresis may also be approximated in terms of distance as shown in *图 23*.

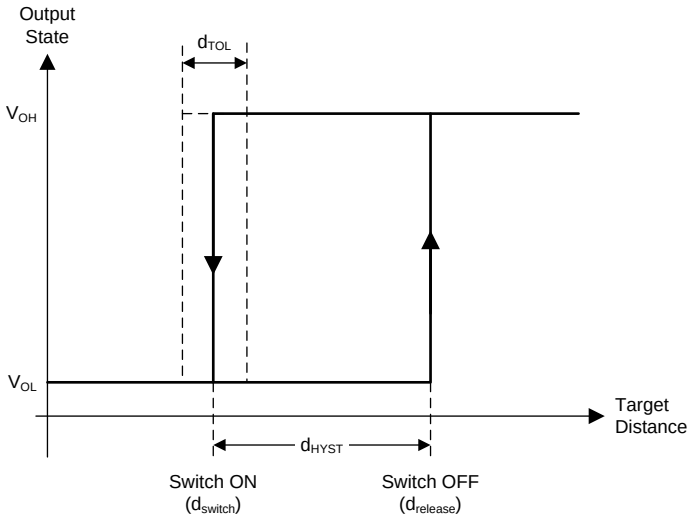


图 23. Switching Distance Hysteresis and Threshold Tolerance

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8.3.5 Conversion Time

The length of time for the LDC0851 to complete one conversion and update the output is called the conversion time and is a function of sensor frequency. The conversion time is calculated with the following equation:

$$t_{CONVERSION} = \frac{1}{231.0 \times 10^{-6} \times f_{SENSOR}} \quad (2)$$

where:

- $t_{CONVERSION}$ is the conversion time interval
- f_{SENSOR} is the sensor frequency given by 公式 6

It is important to note that the frequency of the sensor increases in the presence of conductive objects. Therefore the worst case conversion time is calculated with no target present or when the target is at the maximum distance from the sensor.

8.3.6 Power-Up Conditions

This indicates the switch output state when there is no metal target within the switching distance of LDC0851. On power-up the LDC0851 output will be held HIGH until the part performs the sensor test and is ready for normal operation. This remains true even if the enable pin (EN) is pulled low. A HIGH to LOW transition on the OUT line occurs when the metal target comes within the switching distance of LDC0851. In the case of any sensor fault condition the LDC0851 maintains a HIGH state. An example of a sensor fault is if the sensor gets disconnected or damaged.

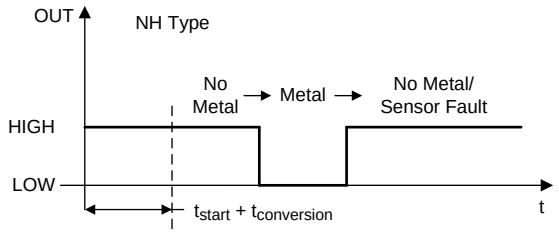


图 24. Output Status at Power up and in Presence of Metal Target

8.4 Device Functional Modes

8.4.1 Shutdown Mode

To save power, the LDC0851 has a shutdown mode. In order to place the LDC0851 in shutdown mode set the EN (Enable) pin low. This mode is useful for low power applications where the EN pin can be duty cycled at a low rate for wake-up applications to achieve a very low average supply current. An example of a duty-cycled application can be found in the applications section *Low Power Operation*. To resume active operation, set EN high and wait t_{AMT} + t_{DELAY} for valid output data. The current consumption in this mode is given in the electrical table as I_{SD}. Note that the output will remain high (OFF) when EN is low. See *Power-Up Conditions* for more information on the startup conditions.

8.4.2 Active Mode

When the LDC0851 EN pin is pulled high, the LDC0851 is put into active mode. The active supply current (I_{DD}) is broken up into three pieces: Static current (I_{static}), Dynamic current (I_{dyn}), and Sensor current (I_{sensor}).

Static current is the DC device current given in the electrical characteristics and does not vary over frequency.

Dynamic current is the AC device current which varies with both sensor frequency (f_{SENSOR}) and board parasitic capacitance (C_{BOARD}). Dynamic current can be computed with the following equation:

$$I_{dyn} = (24.262 \times 10^{-12}) \times f_{SENSOR} + 1.5 \times f_{SENSOR} \times C_{BOARD}$$

where:

- I_{dyn} is the dynamic current drawn by the device and board parasitics
- f_{SENSOR} is the sensor frequency calculated from 公式 6
- C_{BOARD} is the parasitic capacitance of the board, see 图 25

(3)

Sensor current is the AC current required to drive an external LC sensor. Sensor current varies with both the frequency and inductance of the sensor and is given by the following equation:

$$I_{sensor} = \frac{1}{17.1 \times L_{SENSOR} \times f_{SENSOR}}$$

where:

- I_{SENSOR} is current required to drive the sensor
- L_{SENSOR} is the measured inductance of the sensor
- f_{SENSOR} is the sensor frequency calculated from 公式 6

(4)

The total active supply current is given by the following equation:

$$I_{DD} = I_{dyn} + I_{static} + I_{sensor}$$

where:

- I_{DD} is the total active supply current
- I_{dyn} is the dynamic current drawn by the device as given by 公式 3
- I_{static} is the static current as given in the electrical table
- I_{SENSOR} is current required to drive the sensor as given by 公式 4

(5)

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9 Application and Implementation

注

Information in the following applications sections is not part of the TI component specification, and TI does not warrant its accuracy or completeness. TI's customers are responsible for determining suitability of components for their purposes. Customers should validate and test their design implementation to confirm system functionality.

9.1 Application Information

9.1.1 Sensor Design

The LDC0851 relies on two externally placed sensors (L_{SENSE} and L_{REF}) and a capacitor (C_{SENSOR}) for proper operation. The design and matching of the coils is very critical to ensure a proper switching occurrence. It is also important to note that the parasitic capacitance of the board (C_{BOARD}) and of the LCOM input pin (C_{IN_COM}) are in parallel with C_{SENSOR}, and the sum of all three capacitances create a total capacitance (C_{TOTAL}) which is considered part of the system. C_{TOTAL} must be greater than 33 pF to be considered in the valid design space.

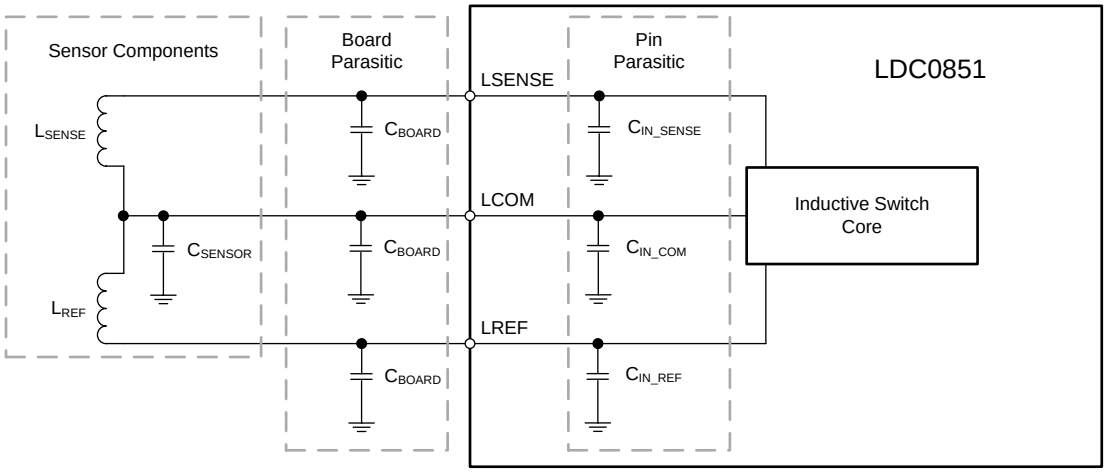


图 25. Sensor Components, Board Parasitics, and Package Parasitics Diagram

9.1.1.1 Sensor Frequency

The sensor frequency is calculated with the following equation.

$$f_{SENSOR} = \frac{\sqrt{2}}{2\pi \times \sqrt{L_{SENSOR} \times C_{TOTAL}}} \quad (6)$$

where:

- f_{SENSOR} is the calculated oscillation frequency with no target present
- L_{SENSOR} is the inductance of the sense coil or reference coil
- C_{TOTAL} is sum of external sensor, board parasitic, and pin parasitic capacitances connected to LCOM, refer to 图 25

Application Information (接下页)

9.1.1.2 Sensor Design Procedure

The following procedure should be followed for determining the sensor characteristics:

1. Determine the diameter of coil (d_{coil}), which should be 3 times larger than the desired switching distance (d_{switch})
2. Determine the desired frequency (f_{SENSOR}) which should be between 300 kHz and 19 MHz
3. Calculate the range of allowable inductance from the following equation:

$$L_{SENSOR} \geq \frac{1}{4.83 \times (f_{SENSOR}) \times (I_{SENSOR_MAX})} \quad (7)$$

where:

- L_{SENSOR} is the inductance of the LSENSE coil or LREF coil
- I_{SENSOR_MAX} is given in the electrical table

4. Calculate the externally placed sensor capacitor:

$$C_{SENSOR} = \frac{1}{(\pi \times f_{SENSOR})^2 (2 \times L_{SENSOR})} - C_{BOARD} - C_{IN_COM} \quad (8)$$

where:

- C_{BOARD} is the parasitic capacitance introduced by the board layout (~4 pF for good layout)
- C_{IN_COM} is the parasitic pin capacitance of LCOM specified as 12 pF in the electrical table

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9.2 Typical Application

9.2.1 Event Counting

The LDC0851 can be used for event counting applications such gear tooth detection or rotational speed measurements. An example of gear tooth detection using side by side coils is shown below where the gear is made of a conductive material and rotates over the coils. Two identical coils can be placed such that when one of the coils is covered by a gear tooth, the other is uncovered. The output will toggle when the inductance values of both coils are equal as the gear passes by.

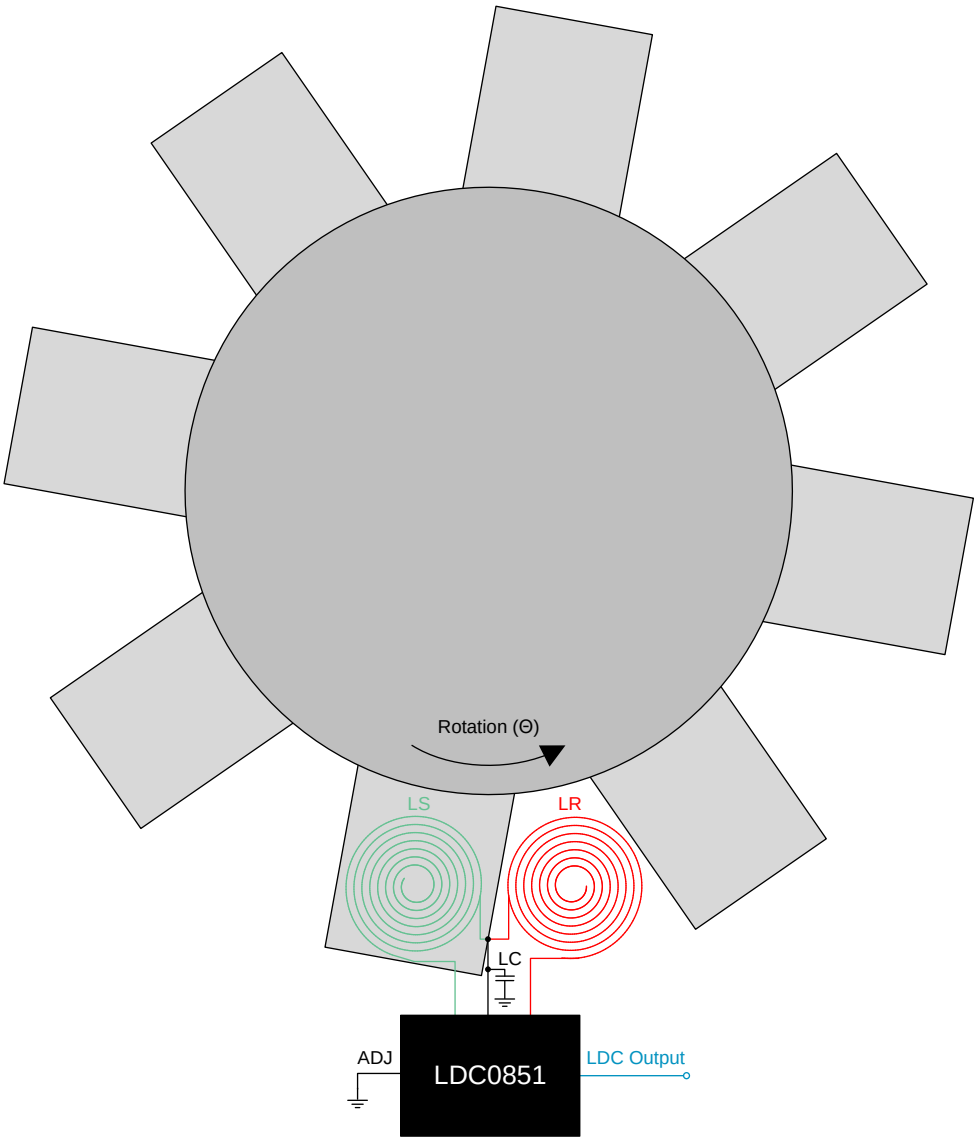


图 26. Gear Tooth Functional Diagram

9.2.1.1 Design Requirements

Assume a gear with 8 conductive teeth is used in a system to determine flow rate. Determine the maximum speed that can be reliably detected by the LDC0851 using a sensor frequency of 15 MHz.

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Typical Application (接下页)

9.2.1.2 Detailed Design Procedure

To ensure a reliable reading an event must be sampled twice when the gear tooth covers more of the LSENSE coil than the LREF coil (LS<LR) producing a LOW output and equivalently twice when the gear tooth covers more of LREF than LSENSE (LR<LS) producing a HIGH output. The maximum speed can be achieved when the output toggles at a duty cycle rate of 50%. This can be achieved by using a gear where the width of each tooth is the same as the width of the gaps between the teeth. For asymmetric systems, the minimum width of either the gap or the gear tooth determines the maximum detectable speed. For symmetrical systems, the maximum rotational speed that can be reliably detected in revolutions per minute (rpm) for a given number of gear teeth can be determined by the following formula:

$$\text{Gear Speed (rpm)} = \frac{1}{4} \times \left[f_{SENSOR} \times \left(231.0 \times 10^{-6} \right) \right] \times \left(\frac{60}{\text{\# gear teeth}} \right) \quad (9)$$

where:

- Gear Speed (rpm) is the calculated speed of the gear
- f_{SENSOR} is the sensor frequency given by 公式 6
- # gear teeth is the total number of events per rotation

A gear with 8 teeth and sensor frequency of 15MHz could reliably measure a gear rotational speed of 6500 rpm.

9.2.1.3 Application Curves

The metal coverage has an inverse relationship to coil inductance. 图 27 shows the relationship between the output of the LDC0851 and relative inductance of the coils as the gear is rotating.

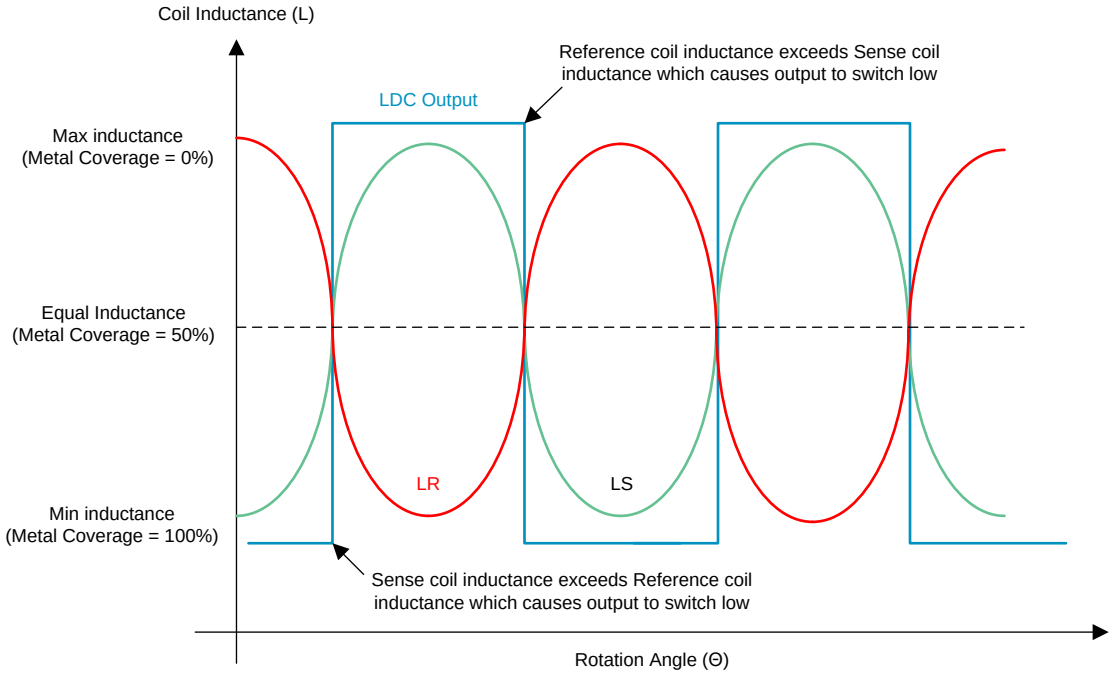


图 27. Angular Position vs Coil Inductance

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Typical Application (接下页)

9.2.2 Coarse Position Sensing

The LDC0851 may be used for coarse proximity sensing such as a push button application. A conductive target may be added to the underside of a mechanical push button as shown below.

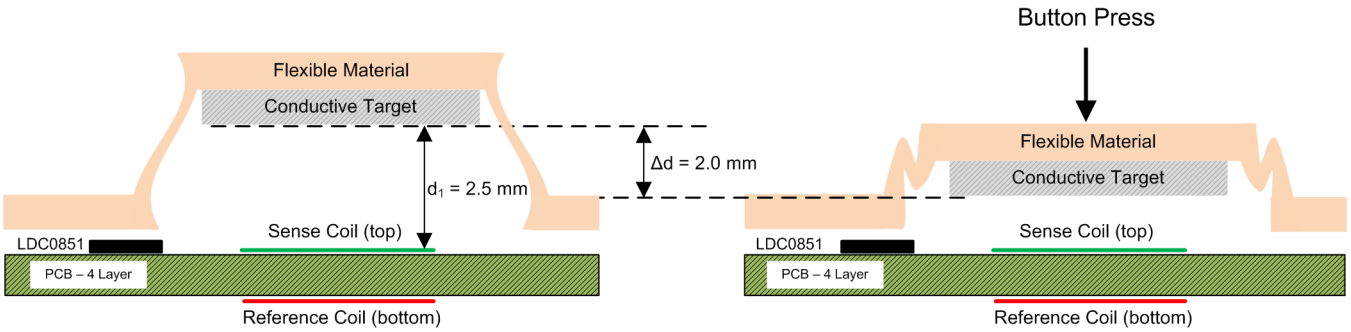


图 28. Coarse Position Sensing Side View

9.2.2.1 Design Requirements

A push button that is made of flexible material has a conductive target attached to the underside and a contactless solution using the LDC0851 is required for reliability purposes. Determine the coil characteristics as well as the threshold adjust setting if the following conditions are true:

1. The target is made of a conductive material, such as aluminum foil or copper tape
2. The conductive target is circular and measures 10 mm in diameter
3. The resting height of the conductive target is 2.5 mm above the PCB when no button push
4. The maximum travel distance when pressed is 2 mm, leaving an airgap of 0.5mm above the PCB

9.2.2.2 Detailed Design Procedure

To conserve PCB area, a 4 layer stacked coil approach is used with the sense coil on the top 2 layers and reference on the bottom 2 layers. The LDC0851 switching threshold is then determined by following parameters:

1. Conductive Target Size: The best response is achieved when the target area is ≥100% compared to the coil area.
2. Coil diameter: The diameter of the coil should be at least 3x greater than the desired switching distance.
3. ADJ code: Increasing ADJ code linearly scales down the switching distance estimated by 公式 1.

The coil diameter should not exceed the diameter of the conductive target of 10mm in order to keep the target-to-coil coverage ≥100%. Additionally, in order to detect the lightest button pushes where the conductive target rests at a height of 2.5 mm, the coil should be at least 3 times greater giving a minimum size of 7.5 mm. The user may therefore select a coil size between 7.5 mm and 10 mm. A coil diameter of 10mm is chosen for the most flexibility and tuning range. The response versus ADJ code is shown below in 图 29.

In this example the deflection caused by the button press (Δd) is 2mm. Note that the Δd must be enough to cross the “Switch ON” threshold and return past “Switch OFF” threshold of the LDC0851 for a given ADJ code to be considered a valid code. Codes 0 through 6 should not be used because the conductive target has already crossed the "Switch ON" thresholds and would always be in the ON state without a button push. Similarly code 15 should not be used because the output would always be in an OFF state regardless of how hard the button is pushed. Therefore codes 8 through 14 are clearly inside the travel distance of the button. Select code 8 to detect light button pushes, code 11 for medium button pushes, or code 14 to only detect strong button pushes. Once the ADJ code is selected based on user preference, set the resistor divider R1 and R2 values according to section *Setting the Threshold Adjust Values*.

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Typical Application (接下页)

9.2.2.3 Application Curves

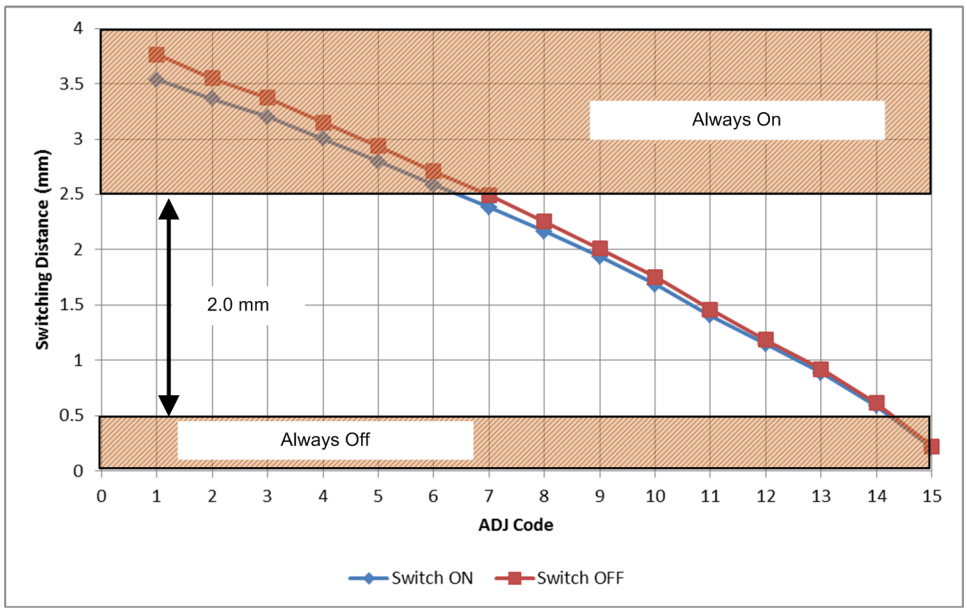


图 29. Threshold Adjust Design Space for 10mm Coil Example

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Typical Application (接下页)

9.2.3 Low Power Operation

It may be desirable to operate the LDC0851 on battery power and take samples at a very low sample rate, such as portable sensor devices or intruder detection systems. By using a nanotimer (ultra low power timer) such as the TPL5110 or a microcontroller such as the MSP430F5500 it is possible to duty cycle the EN pin of the LDC0851 as shown in the application schematic in 图 30.

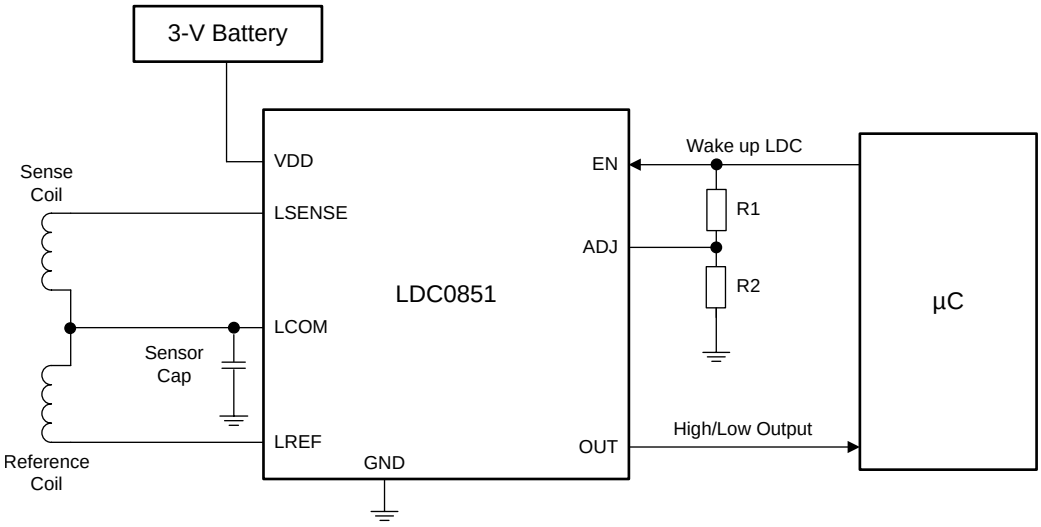


图 30. Application Schematic Showing Low Power Operation

9.2.3.1 Design Requirements

The LDC0851 is used in a low power, battery operated system to detect when a window is opened. Determine the average supply current of the LDC0851 if following requirements exist:

1. A lifetime of greater than 10 years is required from a single CR2032 battery which supplies the power for the LDC0851.
2. A microcontroller can be used to wakeup the LDC0851 and capture the high/low output state.
3. At least 1 sample per second (f_{SAMPLE}) is required to detect if the window is open or closed.

9.2.3.2 Detailed Design Procedure

In order to achieve 10 year lifetime out of a single CR2032 battery, the enable pin (EN) of the LDC0851 can be duty cycled to achieve a low average supply current. Refer to 图 31 to see the three different states of LDC0851 supply current during duty cycle operation. The sum of the Standby, Ramp, and On currents can be used to calculate the average supply current of the LDC0851, which needs to be below 2.5 µA to achieve a 10 year lifetime from a 220 mAh CR2032 battery.

The average supply current can be calculated in the following steps:

1. Select desired system sample rate (f_{SAMPLE}) based on the given application. In this example, f_{SAMPLE} is 1 sample per second.
2. Select the sensor characteristics (f_{SENSOR}, L_{SENSOR}, C_{SENSOR}) based on conversion time and current consumption.
   (a) f_{SENSOR} should be increased as much as possible to minimize the conversion time. 10 MHz is chosen as a starting point.
   (b) L_{SENSOR} should be increased as much as possible to decrease the sensor current (I_{SENSOR}). Based on a reasonable PCB area, 10 µH is a good starting point.
   (c) C_{SENSOR} is calculated to be 34.5 pF from 公式 8 using the inputs above. This makes C_{TOTAL} equal to 50.5 pF which meets the requirement of greater than 33 pF to be inside the design space.
3. Calculate the average active current:

$$I_{ON} = f_{SAMPLE} \times (2 \times t_{CONVERSION}) \times (I_{DD})$$

Typical Application (接下页)

where:

- f_{SAMPLE} is the number of samples per second given from step 1. In this example, f_{SAMPLE} is equal to 1.
- $t_{CONVERSION}$ is calculated from 公式 2 to give a conversion time of 433 µs.
- I_{DD} is the total active supply current given by 公式 5 to be 1.587 mA.
- I_{ON} is the active current consumed by the LDC0851 which comes to be 1.37 µA. (10)

4. Calculate the average ramp current:

$$I_{RAMP} = f_{SAMPLE} \times (t_{AMT}) \times \left(\frac{I_{DD}}{2}\right)$$

where:

- f_{SAMPLE} is the number of samples per second given from step 1. In this example, f_{SAMPLE} is equal to 1.
- t_{AMT} is the active mode transition time given in the electrical table as typically 450µs.
- I_{DD} is the total active supply current given by 公式 5 to be 1.587 mA.
- I_{RAMP} is the current consumed by the LDC0851 before a conversion has started which comes to be 0.357 µA. (11)

5. Calculate the average standby current:

$$I_{OFF} = (1 - f_{SAMPLE} \times (t_{AMT} - 2 \times t_{CONVERSION})) \times (I_{SD})$$

where:

- f_{SAMPLE} is the number of samples per second given from step 1. In this example, f_{SAMPLE} is equal to 1.
- t_{AMT} is the active mode transition time given in the electrical table as typically 450µs.
- $t_{CONVERSION}$ is calculated from 公式 2 to give a conversion time of 433 µs.
- I_{SD} is the shutdown current of the LDC0851 given in the electrical table as typically 140nA.
- I_{OFF} is the standby current of the LDC0851 which comes to be 0.140 µA. (12)

6. Calculate the total average supply current:

$$I_{AVG} = I_{ON} + I_{RAMP} + I_{OFF}$$

where:

- I_{ON} is the active supply current given from 公式 10 to be 1.37 µA.
- I_{RAMP} is the ramp current given by 公式 11 to be 0.357 µA.
- I_{OFF} is the standby current given by 公式 12 to be 0.140 µA.
- I_{AVG} is the average supply current consumed per second which comes to 1.867 µA. (13)

7. Finally the lifetime of the battery can be calculated:

$$\text{Battery Lifetime (years)} = \frac{\text{Battery Capacity}}{I_{AVG}}$$

where:

- Battery Capacity is the amount of charge x time that the battery can hold in mAh. This example uses a CR2032 battery with 220 mAh.
- I_{AVG} is the value reported in 公式 13 to be 1.867 µA.
- Battery Lifetime (years) is how long the battery will last reported in years which comes out to be 13.5 years with the inputs from above. (14)

For example, using a sensor frequency of 10 MHz, sensor inductance of 10 µH, and 1 sample per second yields a lifetime of 13.5 years for a single CR2032 battery.

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Typical Application (接下页)

9.2.3.3 Application Curves

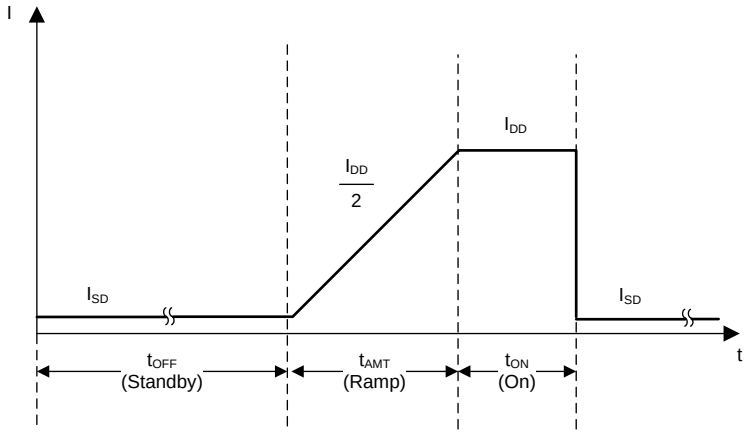


图 31. LDC0851 Supply Current vs. Time During Duty Cycle Operation

10 Power Supply Recommendations

A 0.1 µF capacitor should be used to bypass VDD. If multiple bypass capacitors are used in the system, then the smallest value capacitor should be placed as close as possible to the VDD pin. A ground plane is recommended to connect both the ground and the Die Attach Pad (DAP). If the supply ramp rate must be faster than 4.2 mV/µs the enable pin (EN) may be tied directly to VDD as shown in 图 32.

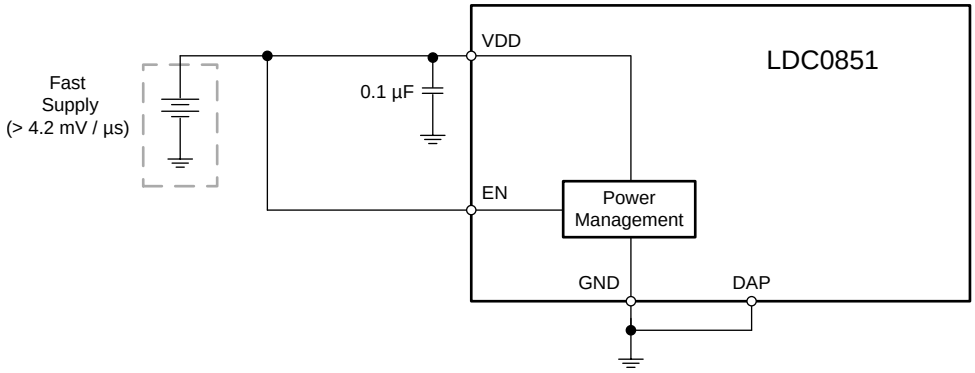


图 32. Supply Connections for Fast Ramp Rate

For supply ramp rates slower than 4.2 mV/µs, an RC low pass filter must be added to the enable input (EN) as shown in *图 33*. Alternatively, the EN pin may be tied to a nanotimer or microcontroller to wake up the LDC0851 after VDD has ramped to its nominal value.

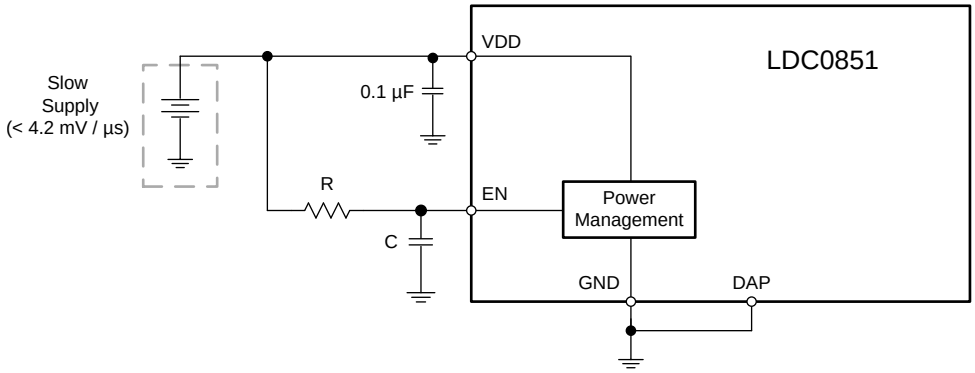


图 33. Supply Connections for Slow Ramp Rate

For applications that require low power, the EN pin may toggled with a GPIO or nanotimer to duty cycle the device and achieve ultra-low power consumption. Although the device may be power cycled to achieve a similar effect, some systems may not have a clean GPIO to supply the LDC0851 or the filtering on the supply may add a time constant delay which can make the use of the EN pin much more efficient and desirable for duty cycled applications. Refer to *Low Power Operation* for a detailed design example.

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11 Layout

11.1 Layout Guidelines

The LDC0851 requires minimal external components for effective operation. An LDC0851 design should follow good layout techniques - providing good grounding and clean supplies are critical for optimum operation. Due to the small physical size of the LDC0851, use of surface mount 0402 or smaller components can ease routing. It is important to keep the routing symmetrical and minimize parasitic capacitances for LSENSE and LREF. The sensor capacitor should be placed close to the IC and keep traces far apart to minimize the effects of parasitic capacitance. For optimum performance, it is recommended to use a C0G/NP0 for the sensor capacitor.

11.2 Layout Example

11.2.1 Side by Side Coils

The use of side by side coils is recommended for many applications that require a 2 layer PCB or that require very accurate temperature compensation. For side by side coils it is recommended to put them on the same PCB, even if using a remote sensing application. This will keep the tolerances and mismatch between the coils as small as possible. An example layout of side by side coils is shown in 图 34.

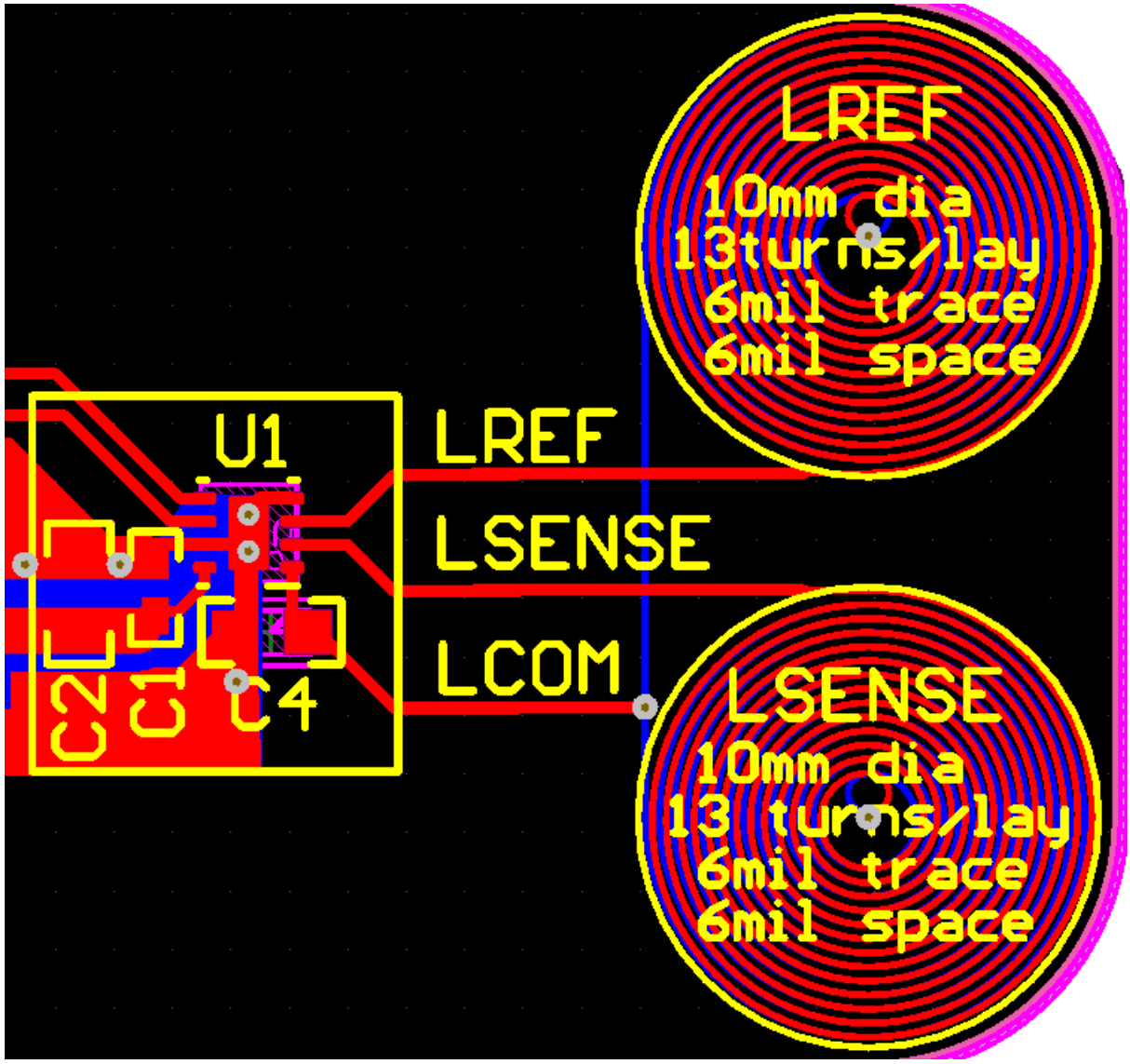


图 34. Side by Side Coil Layout Example

Layout Example (接下页)

11.2.2 Stacked Coils

Use of stacked coils may be desirable to conserve board space and to prevent false triggering when a target approaches from the bottom. A 4 layer PCB with a thick inner layer is recommended to achieve the best results. It is important to note the direction and polarity of the sense coil and reference coils with respect to each other. The recommended configuration is shown below.

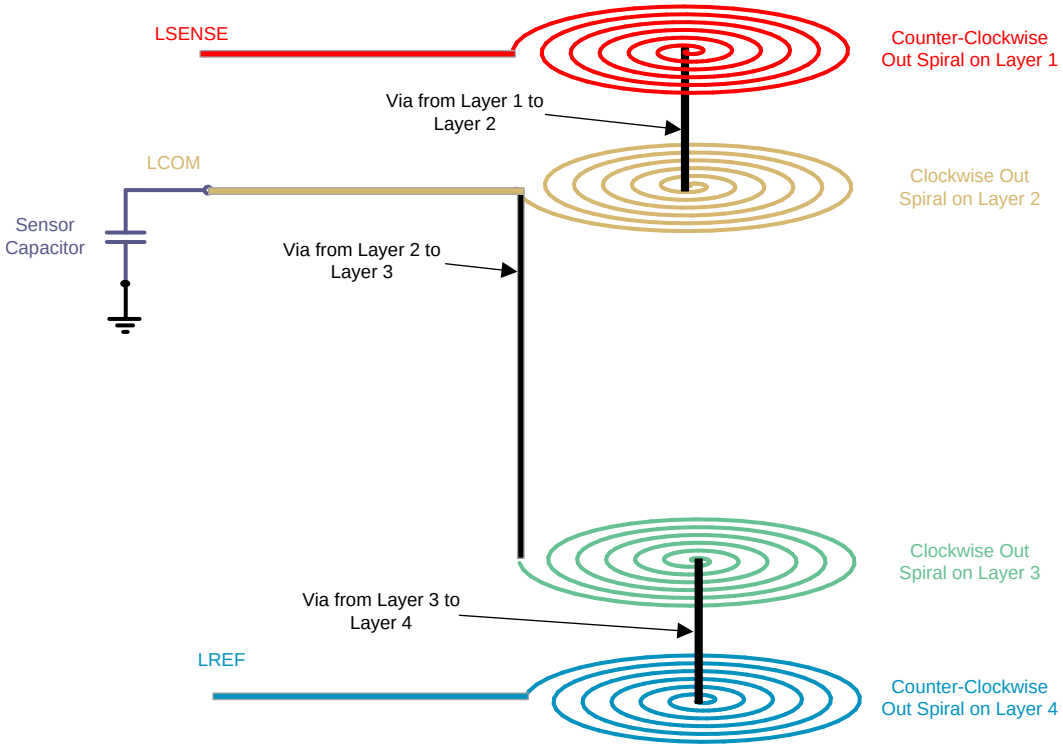


图 35. Stacked Coil Recommended Connections and Direction

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12 器件和文档支持

12.1 器件支持

12.1.1 开发支持

要获取在线 LDC 系统设计工具，请访问德州仪器的 Webench® 工具

LDC 计算器工具提供了一系列在 MS Excel®下运行的计算工具，这些工具对于 LDC 的系统开发非常有用。

12.2 社区资源

The following links connect to TI community resources. Linked contents are provided "AS IS" by the respective contributors. They do not constitute TI specifications and do not necessarily reflect TI's views; see TI's Terms of Use.

TI E2E™ Online Community ***TI's Engineer-to-Engineer (E2E) Community.*** Created to foster collaboration among engineers. At e2e.ti.com, you can ask questions, share knowledge, explore ideas and help solve problems with fellow engineers.

Design Support ***TI's Design Support*** Quickly find helpful E2E forums along with design support tools and contact information for technical support.

12.3 商标

E2E is a trademark of Texas Instruments.
Webench is a registered trademark of Texas Instruments.
Excel is a registered trademark of Microsoft Corporation.

12.4 静电放电警告



这些装置包含有限的内置 ESD 保护。 存储或装卸时，应将导线一起截短或将装置放置于导电泡棉中，以防止 MOS 门极遭受静电损伤。

12.5 Glossary

SLYZ022 — *TI Glossary*.

This glossary lists and explains terms, acronyms, and definitions.

13 机械、封装和可订购信息

以下页中包括机械、封装和可订购信息。这些信息是针对指定器件可提供的最新数据。这些数据会在无通知且不对本文档进行修订的情况下发生改变。欲获得该数据表的浏览器版本，请查阅左侧的导航栏

PACKAGING INFORMATION

Orderable Device	Status (1)	Package Type	Package Drawing	Pins	Package Qty	Eco Plan (2)	Lead finish/ Ball material (6)	MSL Peak Temp (3)	Op Temp (°C)	Device Marking (4/5)	Samples
LDC0851HDSGR	ACTIVE	WSON	DSG	8	3000	RoHS & Green	NIPDAU	Level-1-260C-UNLIM	-40 to 125	0851	Samples

(1) The marketing status values are defined as follows:
ACTIVE: Product device recommended for new designs.
LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.
NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.
PREVIEW: Device has been announced but is not in production. Samples may or may not be available.
OBSOLETE: TI has discontinued the production of the device.

(2) **RoHS:** TI defines "RoHS" to mean semiconductor products that are compliant with the current EU RoHS requirements for all 10 RoHS substances, including the requirement that RoHS substance do not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, "RoHS" products are suitable for use in specified lead-free processes. TI may reference these types of products as "Pb-Free".
RoHS Exempt: TI defines "RoHS Exempt" to mean products that contain lead but are compliant with EU RoHS pursuant to a specific EU RoHS exemption.
Green: TI defines "Green" to mean the content of Chlorine (Cl) and Bromine (Br) based flame retardants meet JS709B low halogen requirements of <=1000ppm threshold. Antimony trioxide based flame retardants must also meet the <=1000ppm threshold requirement.

(3) MSL, Peak Temp. - The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

(4) There may be additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.

(5) Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "~" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.

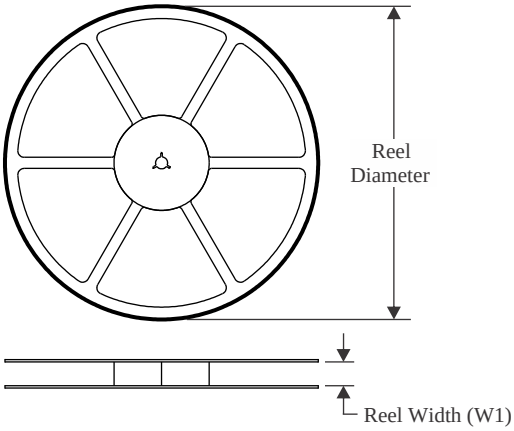
(6) Lead finish/Ball material - Orderable Devices may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead finish/Ball material values may wrap to two lines if the finish value exceeds the maximum column width.

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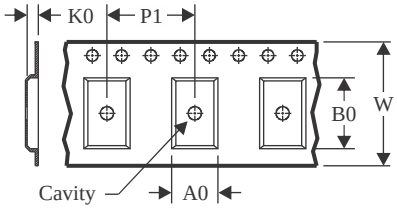
In no event shall TI's liability arising out of such information exceed the total purchase price of the TI part(s) at issue in this document sold by TI to Customer on an annual basis.

TAPE AND REEL INFORMATION

REEL DIMENSIONS

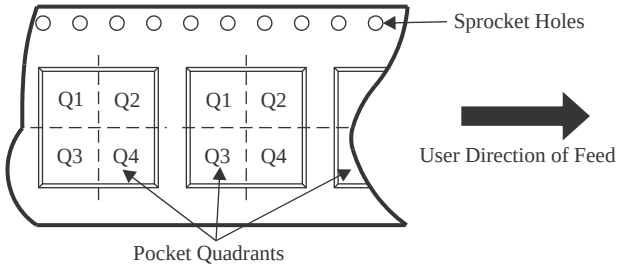


TAPE DIMENSIONS



A0	Dimension designed to accommodate the component width
B0	Dimension designed to accommodate the component length
K0	Dimension designed to accommodate the component thickness
W	Overall width of the carrier tape
P1	Pitch between successive cavity centers

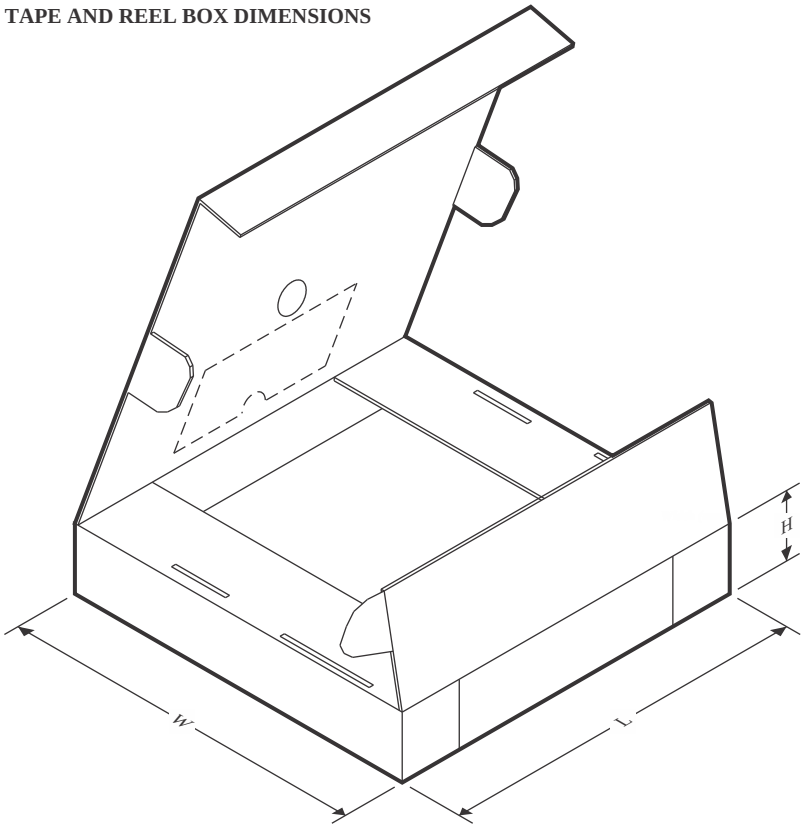
QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE



*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
LDC0851HDSGR	WSON	DSG	8	3000	180.0	8.4	2.3	2.3	1.15	4.0	8.0	Q2

TAPE AND REEL BOX DIMENSIONS



*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
LDC0851HDSGR	WSON	DSG	8	3000	182.0	182.0	20.0

GENERIC PACKAGE VIEW

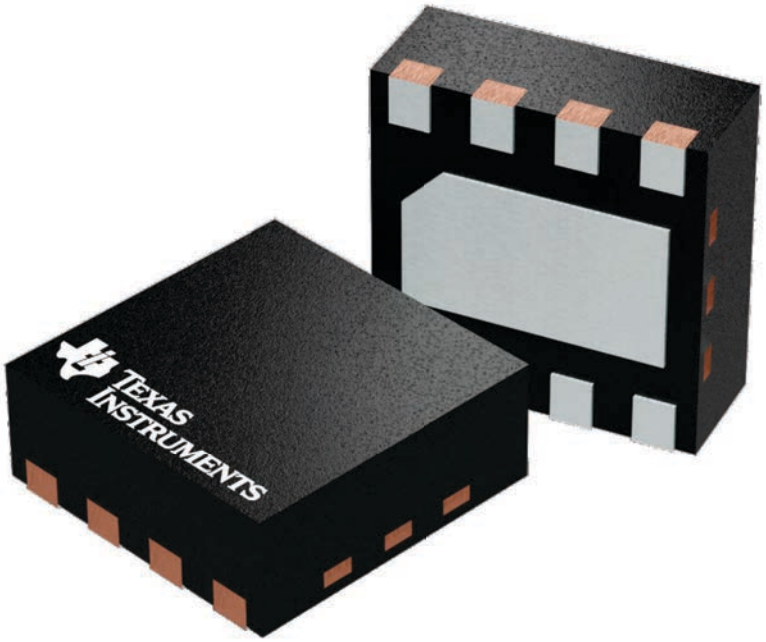
DSG 8

WSON - 0.8 mm max height

2 x 2, 0.5 mm pitch

PLASTIC SMALL OUTLINE - NO LEAD

This image is a representation of the package family, actual package may vary.
Refer to the product data sheet for package details.



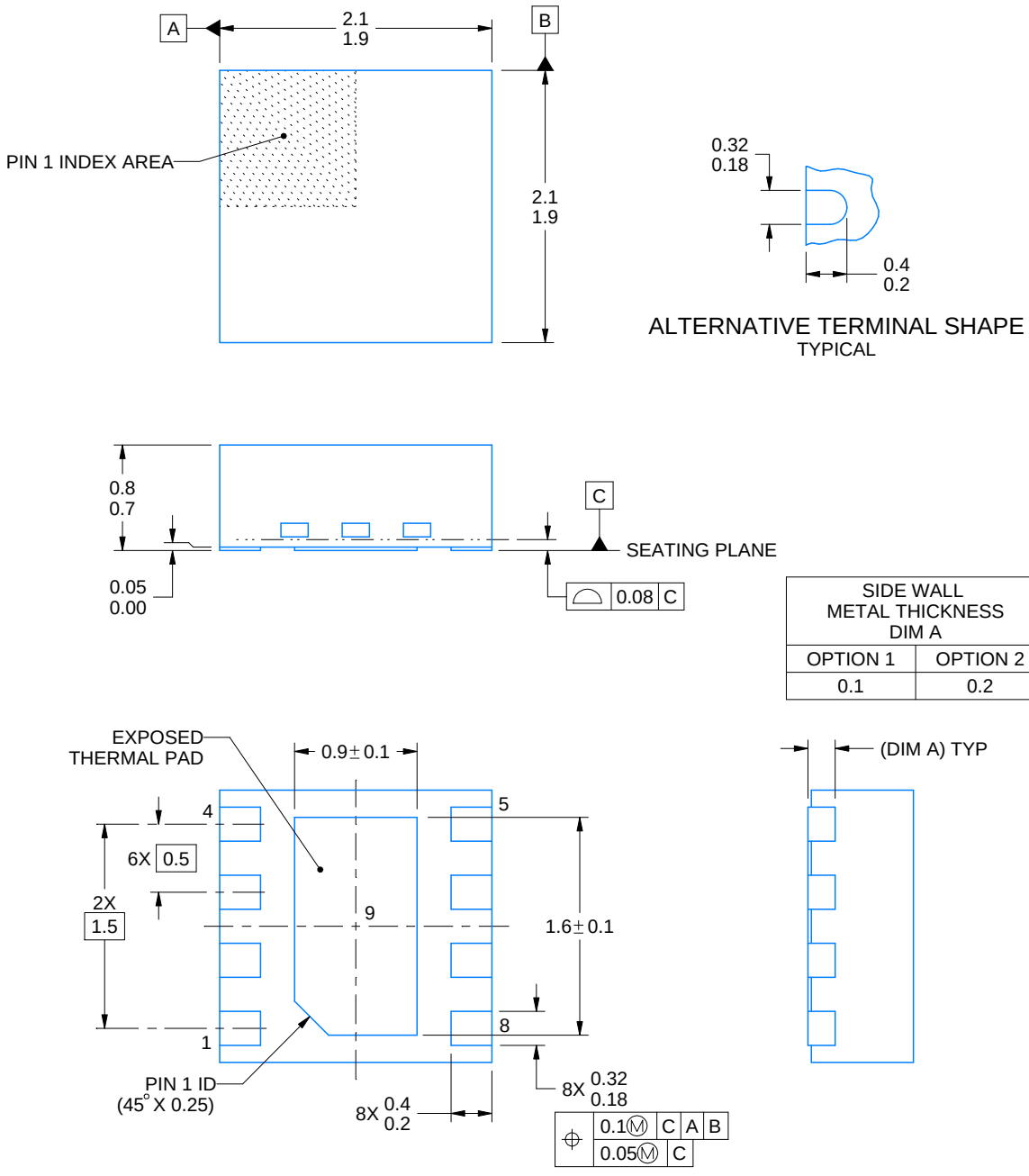
4224783/A

PACKAGE OUTLINE

DSG0008A

WSON - 0.8 mm max height

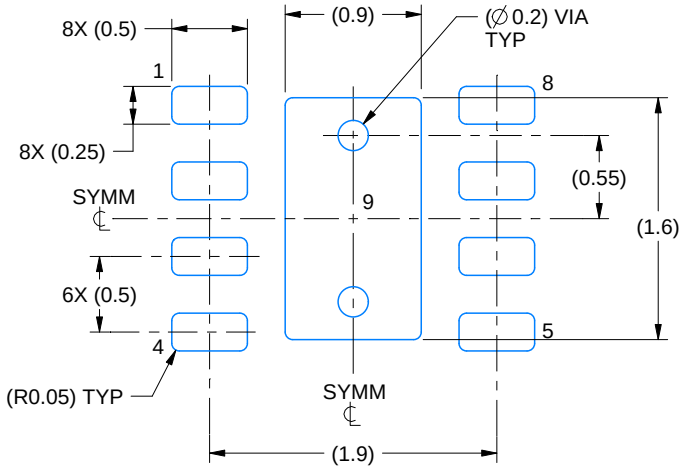
PLASTIC SMALL OUTLINE - NO LEAD



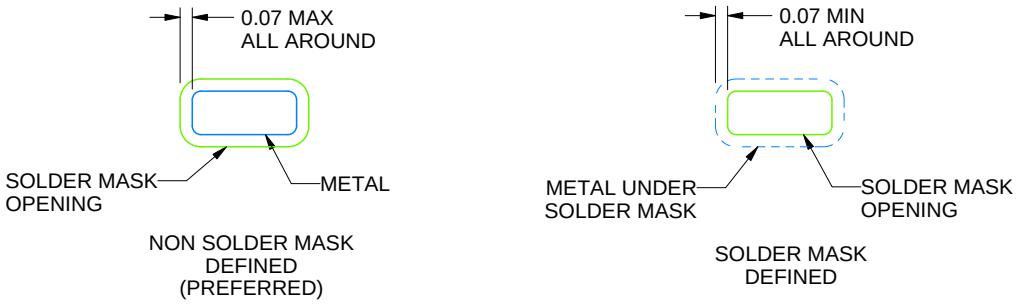
4218900/E 08/2022

NOTES:

1. All linear dimensions are in millimeters. Any dimensions in parenthesis are for reference only. Dimensioning and tolerancing per ASME Y14.5M.
2. This drawing is subject to change without notice.
3. The package thermal pad must be soldered to the printed circuit board for thermal and mechanical performance.



LAND PATTERN EXAMPLE
SCALE:20X

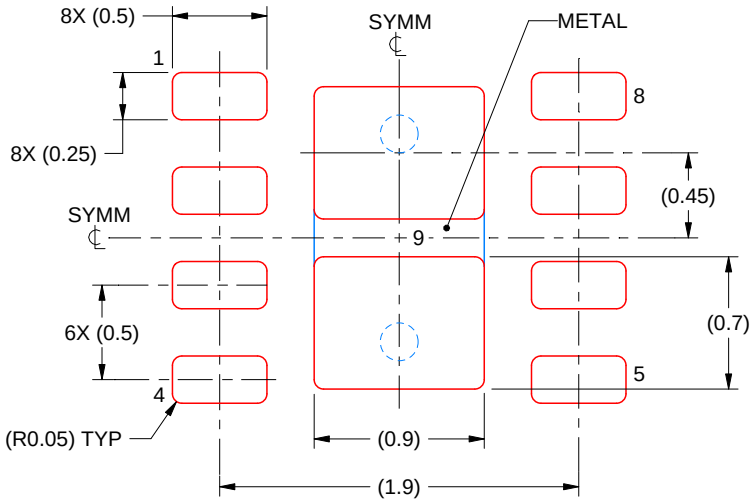


SOLDER MASK DETAILS

4218900/E 08/2022

NOTES: (continued)

4. This package is designed to be soldered to a thermal pad on the board. For more information, see Texas Instruments literature number SLUA271 (www.ti.com/lit/slua271).
5. Vias are optional depending on application, refer to device data sheet. If any vias are implemented, refer to their locations shown on this view. It is recommended that vias under paste be filled, plugged or tented.



SOLDER PASTE EXAMPLE
BASED ON 0.125 mm THICK STENCIL

EXPOSED PAD 9:
87% PRINTED SOLDER COVERAGE BY AREA UNDER PACKAGE
SCALE:25X

4218900/E 08/2022

NOTES: (continued)

6. Laser cutting apertures with trapezoidal walls and rounded corners may offer better paste release. IPC-7525 may have alternate design recommendations.

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